2SK1374

Silicon N-channel MOSFET



Electrical Characteristics T₂ = 25°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source surrender voltage	V _{DSS}	$I_{\rm D} = 10 \mu A, V_{\rm GS} = 0$	50	100		V
Drain-source cutoff current	I _{DSS}	$V_{\rm DS} = 20$ V, $V_{\rm GS} = 0$			1.0	μΑ
Gate-source cutoff current	I _{GSS}	$V_{GS} = 10 \text{ V}, V_{DS} = 0$			1.0	μΑ
Gate threshold voltage	V _{th}	$I_{\rm D} = 100 \ \mu \text{A}, \ V_{\rm DS} = 5 \ \text{V}$	0.5	0.8	1.1	V
Forward transfer admittance	Y _{fs}	$I_D = 10 \text{ mA}, V_{DS} = 5 \text{ V}, f = 1 \text{ kHz}$	20	39		mS
Drain-source ON resistance	R _{DS(on)}	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$		27	50	Ω
Short-circuit forward transfer capacitance (Common source)	C _{iss}	$V_{BS} = 5 V$, $V_{SS} = 0$, $f = 1 MHz$		4.5		pF
Short-circuit output capacitance (Common source)	Coss	il panas		4.1		pF
Reverse transfer capacitance (Common source)	C _{TSS}			1.2		pF
Turn-on time *1, 2	t _{on}	V_{DD} = 5 V, R_L = 470 Ω,V_{GS} = 0 V to 2.5 V		0.2		μs
Turn-off time ^{*1, 2}	t _{off}	V_{DD} = 5 V, R_L = 470 $\Omega,$ V_{GS} = 2.5 V to 0 V		0.2		μs

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *1: Pulse measurement

*2: ton , toff test circuit



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