PQ07VR5MAZ Series

Low Power-Loss Voltage Regulators with Reset Signal Generating Function in Detecting Input Voltage Drop

Features

- Built-in reset signal generating function (The reset detection voltage can be custom-ordered in the range of 3.5 to 4.5V.)
- Low power-loss (Dropout voltage: Max. 0.5V at Io=0.3A)
- Compact, surface mount package (Equivalent to SC-63)
- Variable output voltage type (1.5 to 7V)
- Overcurrent protection and overheat protection function
- Tape-packaged products and sleeve-packaged products are available.

Applications

- Power supplies for AV, OA equipment, and various electronic equipment
- CD-ROM drives and CD-R drives
- DVD-ROM drives



Absolute Maximu	(Ta=25°C)		
Parameter	Symbol	Rating	Unit
*1 Input voltage	VIN	10	V
*1 Output adjustment terminal voltage	VADJ	7	V
*1 Reset output voltage	Vr	10	V
Output current	Io	500	mA
Reset output current	Ir	5	mA
*2 Power dissipation	PD	8	W
*3 Junction temperature	Tj	150	°C
Operating temperature	Topr	-20 to +80	°C
Storage temperature	Tstg	-40 to +150	°C
Soldering temperature	Tsol	260 (10s)	°C

*1 All are open except GND and applicable terminals

*2 PD:With infinite heat sink

*3 Overheat protection may operate at the condition T)=125°C to 150°C

· Please refer to the chapter " Handling Precautions ".

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Outline Dimensions

Electrical Characteristics (Unless otherwise specified, condition shall be VIN=5V, Vo=3V (R1=1kΩ), Io=300mA, Ta=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input voltage	VIN		3.4	_	10	V
Output voltage	Vo		1.5	—	7	V
Load regulation	RegL	Io=5mA to 0.5A	I	0.3	2	%
Line regulation	RegI	VIN=5 to7V, Io=5mA		0.5	2	%
Ripple rejection	RR	Refer to Fig.2	45	60	—	dB
Dropout voltage	VI-0	VIN=3.4V		_	0.5	V
Reference voltage	Vref	—	1.22	1.245	1.27	V
Temperature coefficient of reference voltage	TcVref	T _j =0 to 125°C, Io=5mA	I	±1	_	%
Quiescent current	I_q	Io=0A	-	—	5	mA
Input detection voltage	Vri	Vr 0.8, Rr=10kΩ, Io=5mA	4.116	4.2	4.284	V
"L" Reset output voltage	Vrl	2.5V <vin<vri, io="5mAV</td"><td>_</td><td></td><td>0.8</td><td>V</td></vin<vri,>	_		0.8	V
Hysteresis voltage	ΔV_{ri}	Rr=10kΩ	50	150	200	mV
Reset output leak current	Irlk	Vr=5V, Rr=10kΩ		_	1	μΑ

Reset Threshold Voltage Line-up

Parame	ter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Reset threshold voltage PQC	PQ07VR5MAZ	Ve	*4 Vr≤0.8V, Rr=10kΩ	4.116	4.2	4.284	v
	PQ07VR5MBZ			4.214	4.3	4.386	
	PQ07VR5MCZ	Vrt		4.312	4.4	4.488	
	PQ07VR5MDZ			4.41	4.5	4.59	

 $\ast 4~$ Output voltage shall be the value when input voltage lowers and V_r becomes low

Fig.1 Test Circuit



Fig.2 Test Circuit for Ripple Rejection















Fig.6 Output Voltage vs. Input Voltage (Output:3V)



Fig.8 Quiescent Current vs. Junction Temperature



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Fig.13 Ripple Rejection vs. Output Current





Fig.12 Ripple Rejection vs. Input Ripple Frequency





Fig.14 Typical Application



Fig.15 Reset Output Response (Typical Value)





Fig.16 Reset Output Delay Time vs. Time Constant

Fig.17 External Connection



Fig.18 Power Dissipation vs. Ambient Temperature (Typical Value) 3 Power dissipation PD (W) Cu area 740mm² $\mathbf{2}$ Cu area 180mm² Cu area 100mm Cu area 70mm² 1 Cu area 36mm 0 -20 0 2040 60 80 Ambient temperature Ta (°C)







Size : 50×50×1.6mm Cu thickness : 35µm

Setting of Output Voltage

Output voltage is able to set from 1.5V to 7V when resistors R_1 and R_2 are attached to @, ③, ⑤ terminals. As for the external resistors to set output voltage, refer to the figure below and Fig.19.



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