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Data Sheet

November 2013

50 A, 600 V, Hyperfast Diode

The RHRG5060 is a hyperfast diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction. These devices are intended to be used as freewheeling/ clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Ordering Information

PART NUMBER	PACKAGE	BRAND	
RHRG5060	TO-247-2L	RHRG5060	

NOTE: When ordering, use the entire part number.

Symbol



Features

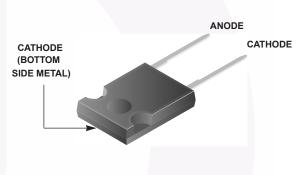
- Hyperfast Recovery t_{rr} = 50 ns (@ I_F = 50 A)
- Max Forward Voltage, V_F = 2.1 V (@ T_C = 25°C)
- 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

Applications

- Switching Power Supplies
- · Power Switching Circuits
- · General Purpose

Packaging

JEDEC STYLE TO-247



Absolute Maximum Ratings T_C = 25°C, Unless Otherwise Specified

	RHRG5060	UNIT
Peak Repetitive Reverse Voltage	600	V
Working Peak Reverse Voltage	600	V
DC Blocking VoltageV _R	600	V
Average Rectified Forward Current $I_{F(AV)}$ ($T_C = 93^{\circ}C$)	50	Α
Repetitive Peak Surge Current	100	А
Nonrepetitive Peak Surge Current I _{FSM} (Halfwave, 1 Phase, 60 Hz)	500	А
Maximum Power Dissipation	150	W
Avalanche Energy (See Figures 10 and 11)	40	mJ
Operating and Storage Temperature	-65 to 175	оС

Electrical Specificatio s $T_C = 25^{\circ}C$, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
V _F	I _F = 50 A	-	-	2.1	V
	$I_F = 50 \text{ A}, T_C = 150^{\circ}\text{C}$	-	-	1.7	V
I _R	V _R = 600 V	-	-	250	μΑ
	$V_R = 600 \text{ V}, T_C = 150^{\circ}\text{C}$	-	-	1.5	mA
t _{rr}	I _F = 1 A, dI _F /dt = 100 A/μs	-	-	45	ns
	$I_F = 50 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	-	50	ns
t _a	$I_F = 50 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	25	-	ns
t _b	$I_F = 50 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	20	-	ns
Q _{rr}	$I_F = 50 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	65	-	nC
СЛ	V _R = 10 V, I _F = 0 A	-	140	-	pF
$R_{ heta JC}$		-	-	1.0	°C/W

DEFINITIONS

 V_F = Instantaneous forward voltage (pw = 300 μ s, D = 2%).

 I_R = Instantaneous reverse current.

 T_{rr} = Reverse recovery time (See Figure 9), summation of t_a + t_b .

 t_a = Time to reach peak reverse current (See Figure 9).

 t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 9).

Q_{rr} = Reverse recovery charge.

C_J = Junction Capacitance.

 $R_{\theta JC}$ = Thermal resistance junction to case.

pw = pulse width.

D = Duty cycle.

Typical Performance Curves

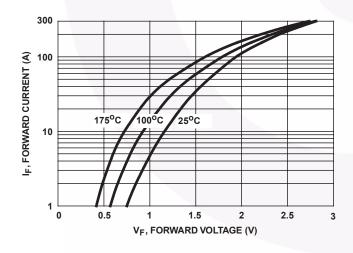


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

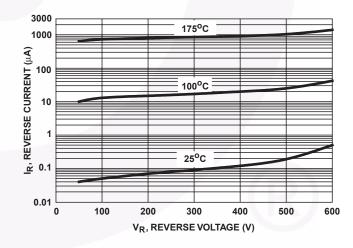


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

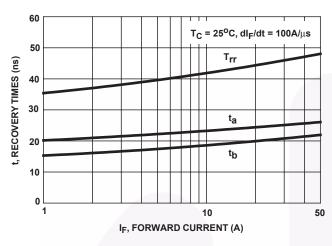


FIGURE 3. T_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

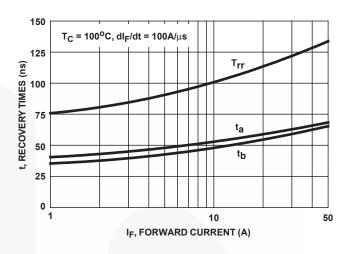


FIGURE 4. T_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

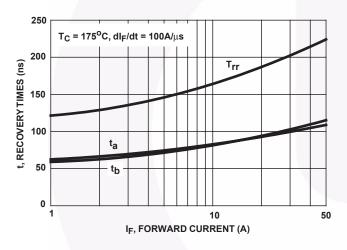


FIGURE 5. T_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

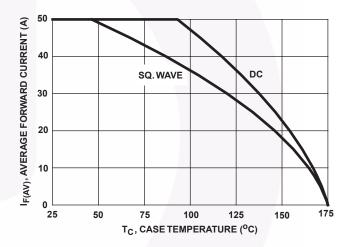


FIGURE 6. CURRENT DERATING CURVE

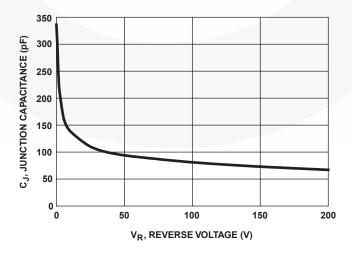


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

Test Circuits and Waveforms

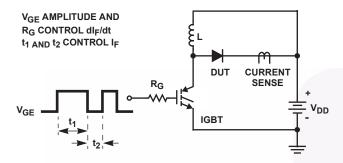


FIGURE 8. T_{rr} TEST CIRCUIT

 $I_{MAX} = 1.4A$ L = 40mH $R < 0.1\Omega$ $E_{AVL} = 1/2LI^2 \left[V_{R(AVL)} / (V_{R(AVL)} - V_{DD}) \right]$ $Q_1 = IGBT \left(BV_{CES} > DUT V_{R(AVL)} \right)$ U_{DD} V_{DD} V_{DD} V_{DD}

FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

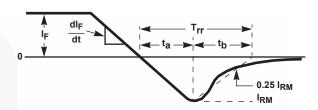


FIGURE 9. T_{rr} WAVEFORMS AND DEFINITIONS

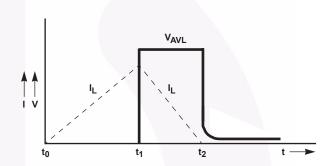


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

Mechanical Dimensions

TO247-2L

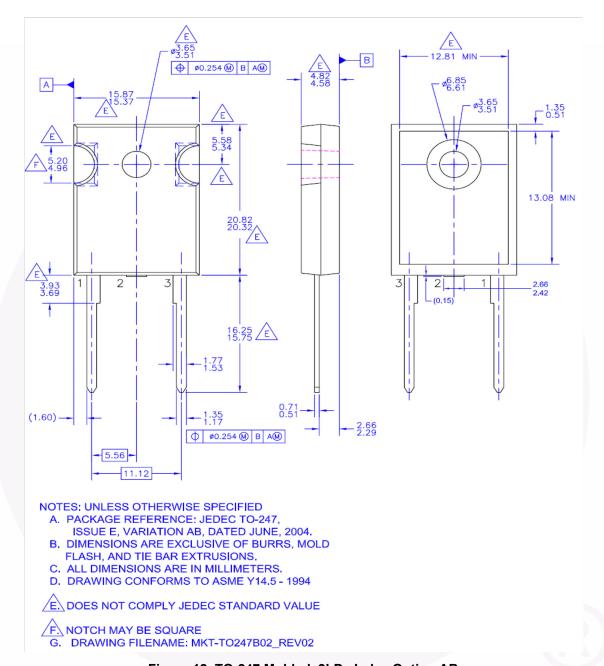


Figure 12. TO-247, Molded, 2LD, Jedec Option AB

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