LTC5533

## DESCRIPTION

Demonstration circuit 821 is a precision dual RF power detector featuring the LTC®5533.

The LTC5533 is a dual channel RF power detector for RF applications operating in the 300MHz to 11GHz range. Two independent temperature compensated Schottky diode peak detectors and buffer amplifiers are combined in a small 4mm x 3mm DFN package.

The RF input voltage is peak detected using on-chip Schottky diodes. The detected voltage is buffered and supplied to the  $V_{OUT}$  pins. A power saving shut-

down mode reduces current to less than  $2\mu A/channel$ . The initial output starting voltages of  $130mV\pm35mV$  can be precisely adjusted using the  $V_{OS}$  pins.

The LTC5533 operates with input power levels from -32dBm to 12dBm.

Design files for this circuit board are available. Call the LTC factory.

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Table 1. Typical Performance Summary ( $V_{CC} = 3.6V$ ,  $\overline{SHDN} = 3V$ ,  $T_A = 25^{\circ}C$ , source impedance =  $50\Omega$ , unless otherwise noted. Test circuit shown in Figure 2.)

PARAMETER	CONDITION	VALUE
V <sub>CC</sub> Operating Voltage		2.7V to 6V
I <sub>VCC</sub> Operating Current, per channel	I <sub>VOUT</sub> = 0mA	0.45mA
I <sub>VCC</sub> Shutdown Current, per channel	SHDN = LO	0.01μΑ
SHDN Voltage, Chip Disabled	V <sub>CC</sub> = 2.7V to 6V	0.35V max
SHDN Voltage, Chip Enabled	V <sub>CC</sub> = 2.7V to 6V	1.4V min
SHDN Input Current, per channel	SHDN = 3.6V	22μΑ
RF <sub>IN</sub> Input Frequency Range		300MHz to 11GHz
RF <sub>IN</sub> Input Power Range	RF Frequency = 300MHz to 7GHz, V <sub>CC</sub> = 2.7V to 6V	-32dBm to 12dBm
Channel to Channel Isolation	f = 2GHz	45dB
V <sub>OS</sub> Voltage Range		0V to 1V
V <sub>OS</sub> Input Current	V <sub>0S</sub> = 1V	-0.5μA to 0.5μA
V <sub>OUT</sub> Start Voltage (No RF Input)	$R_{LOAD} = 2k\Omega$ , $V_{OS} = 0V$	110mV to 150mV
	SHDN = LO	1mV
V <sub>OUT</sub> Output Current	$V_{OUT} = 1.75V$ , $V_{CC} = 2.7V$ , $\Delta V_{OUT} < 10$ mV	4mA
V <sub>OUT</sub> Load Capacitance		33pF max
V <sub>OUT</sub> Bandwidth	$C_{LOAD} = 33pF, R_{LOAD} = 2k\Omega$	2MHz
V <sub>OUT</sub> Slew Rate	$V_{RFIN}$ = 1V Step, $C_{LOAD}$ = 33pF, $R_{LOAD}$ = 2k $\Omega$	3V/μs
V <sub>OUT</sub> Noise	$V_{CC}$ = 3V, Noise BW = 1.5MHz, $50\Omega$ RF Input Termination	1mV <sub>P-P</sub>
V <sub>OUT</sub> Enable Time	SHDN = LO to HI, C <sub>LOAD</sub> = 33pF, R <sub>LOAD</sub> = 2k	8µs



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## **QUICK START PROCEDURE**

Demonstration circuit 821 is easy to set up to evaluate the performance of the LTC5533. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

- 1. Connect all DC power supplies' negative (-) outputs to demo board Gnd test points (E5 and E10).
- 2. Connect  $V_{CC}$  DC power supplies' positive (+) outputs (2.7V to 6V) to demo board  $V_{CC}$  test points (E1 and E6).

**NOTE**: Do not exceed 6.5V, the absolute maximum supply voltage.

- 3. Connect  $V_{OS}$  DC power supplies' positive (+) outputs (0V to 1V) to demo board  $V_{OS}$  test points (E3 and E9).
- 4. Connect voltmeters' negative (-) leads to demo board Gnd test points (E5 and E10).

- 5. Connect voltmeters' positive (+) leads to the demo board Vout test points (E2 and E7).
- Connect RF signal generators' outputs to demo board RF in ports (SMA connectors J1 and J2) via coaxial cables.
- Using jumper cables, connect demo board V<sub>CC</sub> test points (E1 and E6) to SHDN test points (E4 and E8). Now both the detectors are enabled (on) and are ready for measurement.

**NOTE:** Make sure that the power is not applied to the  $\overline{SHDN}$  test points before it is applied to the  $V_{CC}$  test points. The voltages on the  $\overline{SHDN}$  test points must never exceed  $V_{CC}$ .

8. Apply RF input signals and measure Vout DC voltages.

**NOTE**: Do not exceed +12dBm, the absolute maximum RF input power.

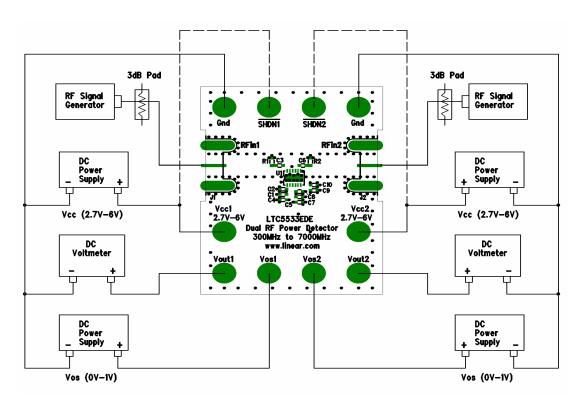


Figure 1. Proper Measurement Equipment Setup



## QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 821 300MHZ TO 11GHZ PRECISION DUAL RF POWER DETECTOR

