GM5BW96320A

Light Emitting Diode



Features

- 1. High brightness (1,400 mcd @ $I_F = 20 \text{ mA}$)
- 2. White Color (achieved via InGaN Blue LED chip in combination with Yellow Phosphor)

■ Agency Approvals/Compliance

1. RoHS compliant

■ Applications

1. General Indication

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External Dimensions



 $(T_{C} - 25^{\circ}C)$

Absolute Maximum Ratings

	(Tc = 25°C)	
Parameter	Rating	Unit	
Power dissipation	Р	117	mW
Forward current	١ _F	30	mA
Peak pulsed forward current *1	I _{FM}	50	mA
Forward current derating factor	DC	0.6	mA/°C
Forward current derating factor	Pulse	1.0	mA/°C
Reverse voltage	V _R	5	mA
Operating temperature *2	Tc	-30 to +100	°C
Storage temperature	Tstg	-40 to +100	°C
Soldering temperature *3	Tsol	295	°C

*1 Duty ratio = 1/10, Pulse width = 0.1 ms

*2 Case temperature (See Outline Dimensions on page 2)

*3 Each terminal must be soldered with a 30 W soldering iron within 3 seconds under 295°C. For Reflow Soldering information, see Fig. 15.

*4 Operating current values here follow the derating curves shown in Fig. 1 through Fig. 3.

*5 This device uses the leads for heat sinking, therefore the Operating Temperature range is prescribed by Tc.

Electro-optical Characteristics

					(10	5 = 25 C
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Forward voltage	V _F		-	3.3	3.9	V
Luminous intensity *1	Ι _V	I _F = 20 mA	*3	1,400	*3	mcd
Chromaticity coordinates *2	х, у		*4	0.31	*4	-
Reverse current *2	I _R	V _R = 5 V	_	-	10	μΑ

*1 Measured by EG&G Model 550 (Radiometer/Photometer) after 20 ms drive (Tolerance: ±15%)

*2 Measured by Ohtsuka Electronics Model MCPD-2000 after 20 ms drive (Tolerance: x,y: ±0.02)

*3 See Luminous Intensity Rank table on page 8.

*4 See Chromaticity Rank table on page 8.

Derating Curves



Fig. 2 Peak Forward Current vs. Case Temperature



Fig. 3 Peak Forward Current vs. Duty Ratio



■ Characteristic Diagram (TYP.)





Fig. 5 Forward Current vs. Forward Voltage



Fig. 6 Relative Luminous Intensity vs. Case Temperature



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■ Tape Specifications

Fig. 7 Tape Shape and Dimensions



■ Tape Dimension Specifications

Parameter	Symbol	Dimension (mm)	Remarks	
	Vertical	A	3.0	Dimension excludes corner R at the bottom inside
Concave square hole for parts insertion	Horizontal	В	3.7	
	Pitch	P ₁	4.0	
	Diameter	D ₀	1.5	
Round sprocket hole	Pitch	P ₀	4.0	Accumulated error ±0.5 mm/10 pitch
	Position	E	1.75	Distance between the edge of the tape and center of the hole
Center to center distance	Vertical	P ₂	2.0	Distance between center line of the concave square hole and
	Horizontal	F	3.5	round sprocket hole
Cover tape	Width	W ₁	5.4	
Cover lape	Thickness	t ₃	0.1	
Corrier topo	Width	W ₀	8.0	
Carrier tape	Thickness	t ₁	0.3	
Thickness of entire unit		t ₂	2.6	With cover tape and carrier tape combined

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■ Reel Specifications

Fig. 8 Reel Shape and Dimensions



■ Reel Dimension Specifications

	Parameter	Symbol	Dimension (mm)	Remarks
	Diameter	A	φ 1 80	
Flange	Thickness	t	1.3	
	Inner space direction	W	9.5	Shaft core dimension
Hub	External diameter	В	φ60	
	Spindle hole diameter	С	φ 1 3	
	Key slit width	E	2.0	
	Key slit depth	U	4	

*1 Label on side of flange: part number, quantity, lot number, and rank. *2 Material: described on flange.

■ Taping Specifications

1. Leader tape standard: JIS C0806

Fig. 9 Leader Tape



2. Cover tape peel resistance: F = 0.1 to 1.0 N (θ = 10° or less). See Fig. 10.

Fig. 10 Tape Separation



- 3. Tape bending resistance: Cover tape will remain in place on radii of 30 mm or more. Under 30 mm radii, the cover may separate.
- 4. Joints are not allowed in the cover tape.
- 5. Parts are packed with an average quantity of 2,000 pieces per reel.
- 6. Product mass: 30 mg (approximately)
- 7. Sharp guarantees the following:
 - a. No contiguous empty spaces in the tape
 - b. Missing parts will not make up more than 0.1% of the total quantity.
 - c. Parts will be easily removed from the tape.
- 8. Parts will not stick to the cover tape as it is peeled.

Label and Marking Information

Fig. 11 Label Contents



LOT Number

Μ	<u> </u>	<u>07</u>	<u>A</u>	0	1
1	2	3	4	(5)

(1) Production plant code (alphabetically)

(2) Production lot (single or double digits)

(3) Production year (the last two digits of the year)

(4) Production month

(to be indicated alphabetically with January corresponding to A)

(5) Production date (01 ~ 31)

Rank \bigcirc \land : \bigcirc : Luminous intensity rank

∴ Chromaticity coordinates rank

■ Luminous Intensity Rank Table

■ Luminous Intensity Rank Table (Tc = 25°C)									
Rank	Luminous Intensity	Unit	Condition						
М	795 ~ 1,548								
Ν	1,144 ~ 2,229	mcd	I _F = 20 mA						
0	1,648 ~ 3,210	1							

*1 Parts are marked to the highest rank of their tested luminosity level.

*2 Quantity of each rank is decided by Sharp.

Chromaticity Rank Table

 $(Tc = 25^{\circ}C)$

			Chr	omaticity C	oordinates	(x,y)			
Rank	Poi	nt 1	Poi	nt 2	Poi	nt 3	Poi	nt 4	Condition
	x	У	x	У	x	У	x	У	
a0	0.263	0.270	0.283	0.243	0.297	0.270	0.280	0.295	
b1	0.280	0.295	0.289	0.282	0.326	0.345	0.320	0.355	l _F = 20 mA
b2	0.289	0.282	0.297	0.270	0.333	0.334	0.326	0.345	

*1 Tolerance: ±0.02.

*2 Quantity of each rank is decided by Sharp.

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Fig. 12 Chromaticity Diagram



Design Notes

- This product is not designed to resist electromagnetic and ionized-particle radiation. Moreover, it is not designed to directly resist excessive moisture, such as dew or condensation; or corrosive (salt) air or corrosive gases, such as Cl, H₂S, NH₃, SO₂, NO_X.
- 2. Do not allow the circuit design to apply any reverse voltage to the LEDs.
- 3. This part can be easily damaged by external stress. Make sure they are not mechanically stressed during or after assembly.
- 4. This product uses a blue LED chip in combination with fluorescent materials to achieve its color. There may be some afterglow of the phosphor when driving this part with pulsed power.
- 5. This part has a high light output. Looking directly at it during full power output may cause injury.
- 6. Sharp recommends taking proper personal and environmental static control precautions when handling this part.
- 7. Materials of high thermal conductivity are incorporated in this device to allow generated heat to be effectively transferred from it to the circuit board. For best reliability, Sharp recommends against locating other sources of heat near the LED, and to design the circuit board in such a way that heat can easily escape from the circuit board. Sharp also recommends designing the circuit board so that the part's case temperature is always kept under 100° (when the LED is turned on) including self-heating.
- 8. Sharp recommends handling these parts in a clean, non-dusty environment since surface dust may be difficult to remove and can affect the optical performance of the part.
- 9. Sharp recommends confirming the part's performance, reliability, and resistance to any of these conditions, if it is to be used in any of these environments:
 - Direct sunlight, outdoor exposure, dusty conditions
 - In water, oil, medical fluids, and organic solvents
 - Excessive moisture, such as dew or condensation
 - \bullet Corrosive (salt) air or corrosive gases, such as CI, H_2S, NH_3, SO_2, NO_X

Operational lifetime

Samples of this device were subjected to an accelerated life test; the results are shown in Fig. 13. This data was gathered from a random sample of 22 parts, chosen from different lots and so cannot be guaranteed. The dotted line indicates estimates to 4400 hours based on trend data from these samples.

Fig. 13 Illuminance vs. Time



Manufacturing Guidelines

• Storage and Handling

1. Moisture-proofing: These parts are shipped in vacuum-sealed packaging to keep them dry and ready for use. See Fig. 14.

Fig. 14 Factory Moisture-proof Packaging



- 2. Store these parts between 5°C and 30°C, at a relative humidity of less than 60%.
- 3. After breaking the package seal, maintain the environment within 5°C to 30°C, at a relative humidity of less than 60%. Solder the parts within 3 days.
- 4. If the parts will not be used immediately, repack them in a dry box, or re-vacuum-seal them with a desiccant.
- 5. If the parts are exposed to air for more than 3 days, or if the silica gel telltale indicates moisture contamination, bake the parts:
 - When in the tape carrier, bake them at a temperature of 95°C to 100°C, for 16 to 24 hours.
 - When loose or on a PCB, bake them at a temperature of 110°C to 120°C, for 8 to 12 hours.
 - Note that the reels may become distorted if they are in a stack when baking. Confirm that the parts have cooled to room temperature after baking.

• Cleaning Instructions

- 1. Sharp does not recommend cleaning printed circuit boards containing this device, or cleaning this device with ultrasonic methods. Process chemicals will affect the structural and optical characteristics of this device.
- 2. Sharp recommends the use of a solder paste that does not require cleaning.

Soldering Instructions

- 1. When soldering with reflow methods, Sharp recommends following the soldering profile in Fig. 15.
- 2. Do not subject the package to excessive mechanical force during soldering as it may cause deformation or defects in plated connections. Internal connections may be severed due to mechanical force placed on the package due to the PCB flexing during the soldering process.
- 3. When using a second reflow, the second process should be carried out as soon as possible after the first.
- 4. Electrodes on this part are silver-plated. If the part is exposed to a corrosive environment, the plating may be damaged, thereby affecting solderability.
- 5. The Reflow Profile shown in Fig. 15 should be considered as a set of maximum parameters. Since this part uses the leads for heatsinking, the peak temperature should be kept as cool as possible and the cooldown period lengthened as much as possible. Thermal conduction into the LED will be affected by the performance of the reflow process, so verification of the reflow process is recommended. These parts may be used in a nitrogen reflow process.



Fig. 15 Temperature Profile

Recommended Solder Pad Design

- 1. Solderability depends on reflow conditions, solder paste, and circuit board materials. Check the entire process before production commences.
- 2. Fig. 16 shows the recommended solder pattern for this part.
- 3. When using backside dip methods, Sharp recommends checking the process carefully: board warping from heat can cause mechanical failure in these parts, in addition to the high heat conducted into the part through the leads. Performing reflow after dip is recommended, with the interval between the two as short as possible.

Fig. 16 Recommended Solder Pad Design



NOTE: Unit: mm

Presence of ODCs

This product shall not contain the following materials, and they are not used in the production process for this product:

• Regulated substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform). Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

• Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

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- --- Personal computers
- --- Office automation equipment
- --- Telecommunication equipment (terminal)
- --- Test and measurement equipment
- --- Industrial control
- --- Audio visual equipment
- --- Consumer electronics

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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.

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