Unit: mm

TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

# GT10G131

### Strobe Flash Applications

- 5th generation (trench gate structure) IGBT
- Enhancement-mode
- 4-V gate drive voltage:  $V_{GE} = 4.0 \text{ V (min)}$  (@IC = 200 A)
- Peak collector current: IC = 200 A (max)
- Built-in zener diode between gate and emitter
- · SOP-8 package

# Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit \
Collector-emitter voltage		$V_{CES}$	400	(V)
Gate-emitter voltage	DC	$V_{GES}$	±6 (	
	Pulse	$V_{GES}$	± 8	<b>y</b>
Collector current	Pulse (Note 1)	I <sub>CP</sub>	200	A
Collector power dissipation(t=10 s)	(Note 2a)	P <sub>C</sub> (1)	1.9	W
	(Note 2b)	P <sub>C</sub> (2)	1.0	W
Junction temperature		Tj <	150	/°e
Storage temperature range		T <sub>stg</sub>	-55~150	< ⟨c

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

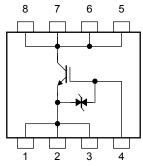
# 1.2.3 Emitter 4 Gate 5.6.7.8 Collector JEDEC JEITA TOSHIBA 2-6J1C

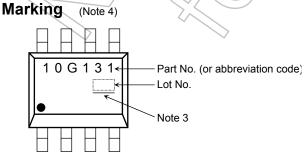
Weight: 0.08 g (typ.)

### Thermal Characteristics

Characterístics	Symbol	Rating	Unit	
Thermal resistance , junction to ambient (t = 10 s) (Note2a)	R <sub>th (j-a)</sub> (1)	65.8	°C/W	
Thermal resistance , junction to ambient (t = 10 s) (Note2b)	R <sub>th</sub> (j-a) (2)	125	°C/W	

## **Circuit Configuration**





Note 3: A line under a Lot No. identifies the indication of product Labels.

Not underlined : [[Pb]]/INCLUDES > MCV

Underlined : [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Note: For (Note 1), (Note 2a), (Note 2b) and (Note 4) Please refer to the next page.

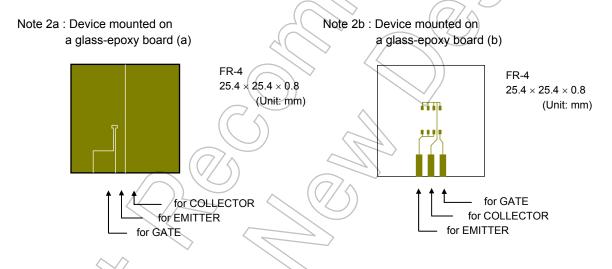
Start of commercial production 2003-06

### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GES</sub>	$V_{GE} = \pm 6 \text{ V}, V_{CE} = 0 \text{ V}$	_	_	± 10	μА
Collector cut-off of	current	I <sub>CES</sub>	V <sub>CE</sub> = 400 V, V <sub>GE</sub> = 0 V	_	_	10	μА
Gate-emitter cut-	off voltage	V <sub>GE</sub> (OFF)	I <sub>C</sub> = 1 mA, V <sub>CE</sub> = 5 V	0.6	0.9	1.2	V
Collector-emitter	saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 200 A, V <sub>GE</sub> = 4 V		2.3	_	V
Input capacitance	)	C <sub>ies</sub>	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0 V, f = 1 MHz	1	2800	_	pF
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c} 4 \text{ V} \\ 0 \end{array}$ $\begin{array}{c} 51 \Omega \\ \text{O-W} \end{array}$ $\begin{array}{c} 51 \Omega \\ \text{V}_{\text{IN}} \text{: } t_r \leq 100 \text{ ns} \\ \text{t}_f \leq 100 \text{ ns} \\ \text{Duty cycle} \leq 1\% \end{array}$ $\begin{array}{c} 0 \\ \text{Vcc} \approx 300 \text{ V} \\ \text{O-W} \end{array}$	) A	2.8	_	μs
	Turn-on time	t <sub>on</sub>			3.1		
	Fall time	t <sub>f</sub>			1.8	_	
	Turn-off time	t <sub>off</sub>		- /	2.0	$\searrow$	

### **Note**

Note 1: Please use devices on condition that the junction temperature is below 150°C. Repetitive rating: pulse width limited by maximum junction temperature.



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Note 4: O on lower right of the marking indicates Pin 1.

Weekly code: (Three digits)
 Week of manufacture
 (01 for first week of year, continues up to 52 or 53)
 Year of manufacture
 (One low-order digits of calendar year)

 [[G]]/RoHS [[Pb]]:
 It is marking about an underline to a week of manufacture mark.

### Caution on handling

This device is MOS gate type. Therefore, please care of a protection from ESD in your handling.

## Caution in design

The slope of the collector-emitter voltage, dv/dt, during turn-off should be kept below 400 V/µs. There is no limit to the slope of the collector-emitter voltage during turn-on. If there is a gate resistor, Rg(oN), that controls the gate current, ensure that it will not exceed the gate driver's current capability.

In cases where both gate turn-on and turn-off are controlled with a single gate resistor, use of a resistor of 51  $\Omega$  or greater is recommended.

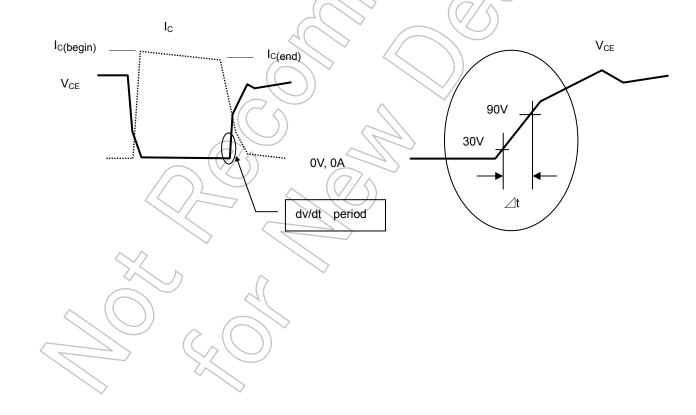
definition of dv/dt

The slope of vce from 30v to 90v (attached figure.1)

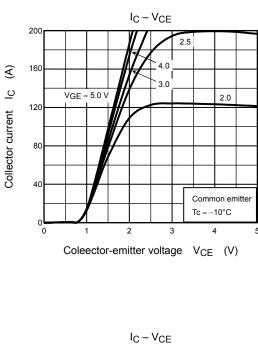
$$dv/dt = (90V-30V) / (\triangle t)$$
$$= 60V / \triangle t$$

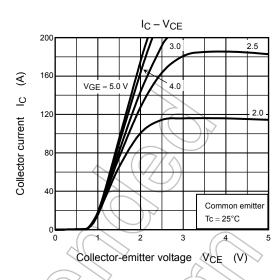
waveform

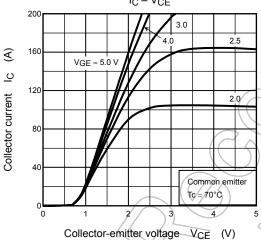
waveform (Expanded View of the dv/dt Period)

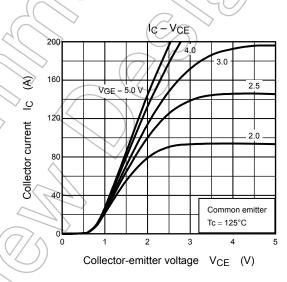


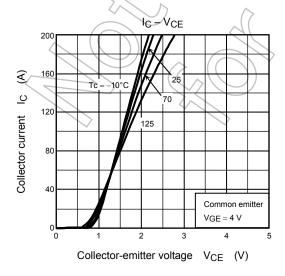
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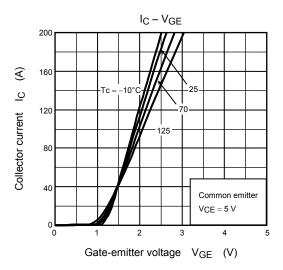


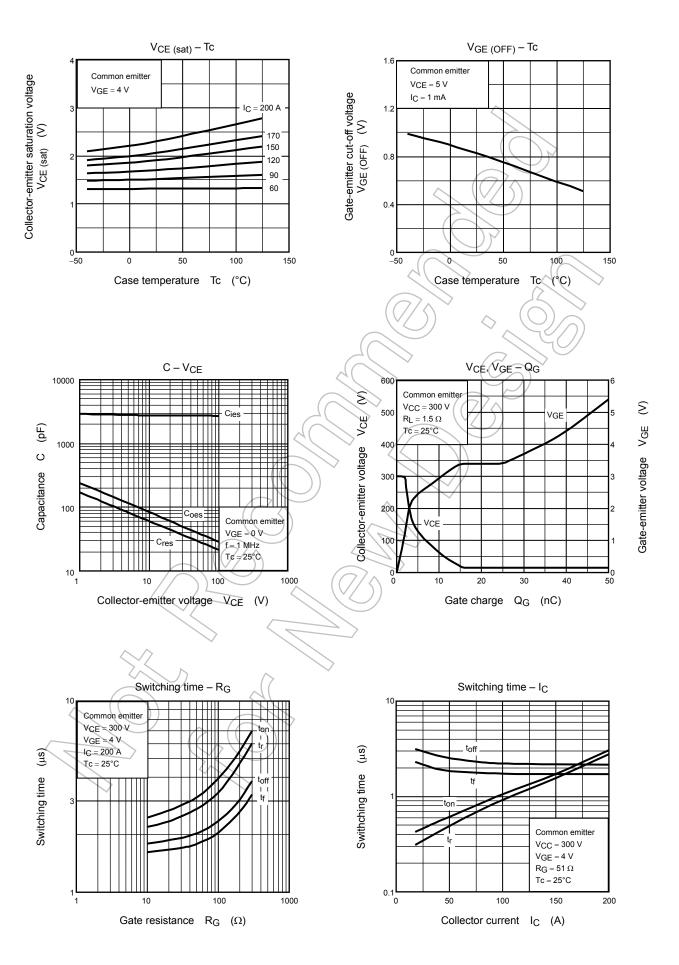




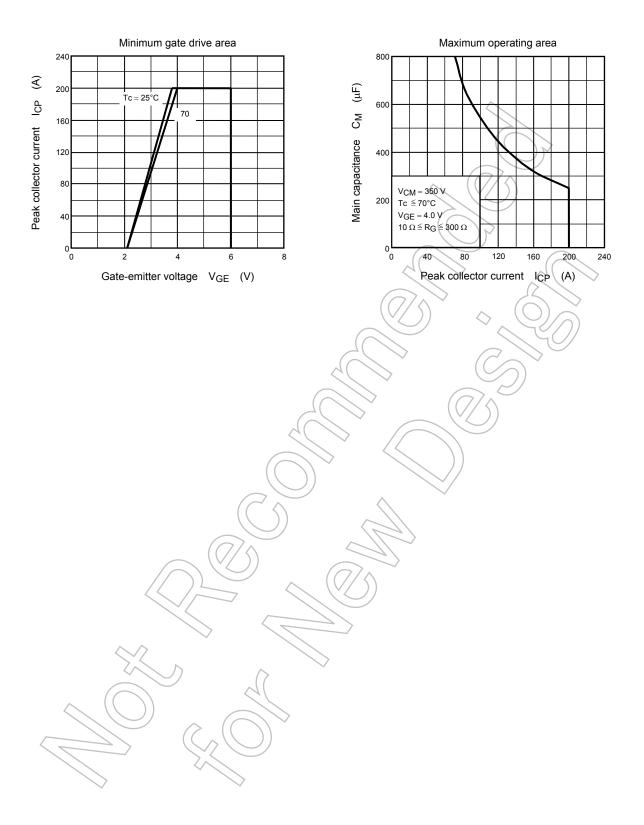








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