PRODUCT SPECIFICATION



Integrated Circuits Group

LH5116NA-10F 16K SRAM

(Model Number: LH5116NC)

Spec. Issue Date: Oct. 22, 2004 Spec No: EL16X148

То;	SPEC No. E L 1 6 X 1 4 8 ISSUE: Oct. 22. 2004
SPE	ECIFICATIONS
Product Type	16k SRAM
Model No *This specification	H 5 1 1 6 N A - 1 0 F (LH5116NC)
CUSTOMER ACCEPTANCE	
DATE: BY:	PRESENTED BY: M. OKADA M. OKADA Dept. General Manager
	REVIEWED BY: M. Haway T. Morimuna. Product Development Dept. 3 System Flash Memory Division Integrated Circuits Group SHARP CORPORATION

Handle this document carefully for it contains material protected by international copyright law. Any reproduction, full or in part, of this material is prohibited without the express written permission of the company.

When using the products covered herein, please observe the conditions written herein and the precautions outlined in the following paragraphs. In no event shall the company be liable for any damages resulting from failure to strictly adhere to these conditions and precautions.

- (1) The products covered herein are designed and manufactured for the following application areas. When using the products covered herein for the equipment listed in Paragraph (2), even for the following application areas, be sure to observe the precautions given in Paragraph (2). Never use the products for the equipment listed in Paragraph (3).
 - \cdot Office electronics
 - · Instrumentation and measuring equipment
 - Machine tools

SHARP

- · Audiovisual equipment
- · Home appliances
- · Communication equipment other than for trunk lines
- (2) Those contemplating using the products covered herein for the following equipment which demands high reliability, should first contact a sales representative of the company and then accept responsibility for incorporating into the design fail-sale operation, redundancy, and other appropriate measures for ensuring reliability and safety of the equipment and the overall system.
 - Control and safety devices for airplanes, trains, automobiles, and other transportation equipment
 - · Mainframe computers
 - · Traffic control systems
 - $\cdot \mbox{ Gas}$ leak detectors and automatic cutoff devices
 - · Rescue and security equipment
 - · Other safety devices and safety equipment, etc.
- (3) Do not use the products covered herein for the following equipment which demands extremely high performance in terms of functionality, reliability, or accuracy.
 - Aerospace equipment
 - · Communications equipment for trunk lines
 - · Control equipment for the nuclear power industry
 - · Medical equipment related to life support, etc.
- (4)Please direct all queries and comments regarding the interpretation of the above three Paragraphs to a sales representative of the company.

Please direct all queries regarding the products covered herein to a sales representative of the company.

LH5116NC

	Contents
1.	Description
2.	Pin Configuration
3.	Truth Table
4.	Block Diagram
5.	Absolute Maximum Ratings
6.	Recommended DC Operating Conditions
7.	DC Electrical Characteristics
8.	AC Electrical Characteristics
9.	Data Retention Characteristics
10.	Pin Capacitance
11.	Timing Chart
12.	Package and Packing Specification



1.Description			
The LH5116NA- with provides low-power It is fabricated using	er standby mode.		, 048×8 bit
Features			
OAccess Time		100 ns (Max.)
O0perating current		40 mA (Max.	
OStandby current		1.0 μ A (Max.)
OData retention current		$0.2 \ \mu A$ (Max.	$V_{CCDR} = 2 V, Ta = 25 °C)$
○Wide operating voltage	range · · · ·	4.5 V to 5.5 V	
O0perating temperature		0°C to +70°C	
OFully static operation			
OThree-state output			•
ONot designed or rated as	s radiation hardene	ed	
O24 pin SOP (S	OP24 - P - 45	0) plastic package	
○P-type bulk silicon			
2. Pin Configuration			
	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$24 \qquad \forall cc \qquad $	
	$A_5 \square 3$	23 A 8 22 A 9	
	$A_4 \square 4$	$\frac{22}{21} \qquad \qquad$	
	$A_3 \square 5$	$20 \qquad \square \qquad \overline{OE}$	
	A 2 🗖 6	19 A 10	
	A1 - 7	18 CE	
	A 0 🗖 8	17 🖂 I/O 8	
]	[/0] [9	16 🗔 I/O 7	
	I/O 2 [] 10	15 🗔 I/O 6	
	1/0 3 🖂 11	14 🔲 I/O 5	
	GND [12]	13 🗖 I/O 4	
	(Тор	view)	
	Pin Name	Function	
	A o to A 10	Address inputs	
	CE	Chip enable	
	WE	Write enable	
	OE	Output enable	
	I/OitoI/O8	Data inputs/outputs	
	Vcc	Power supply	
	GND	Ground	

L H 5 1 1 6 N C

3. Truth Table

CE	WE	OE	Mode	I/O1toI/O8	Supply current
Н	*	*	Standby	High impedance	Standby (Ism)
L	Н	L	Read	Data output	Active (I cc)
L	*	H	Output disable	High impedance	Active (I _{cc})
L	L	*	Write	Data Input	Active (I _{cc})

(*****=Don't Care, L=Low, H=High)

4.Block Diagram





5. Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Supply voltage(*1)	Vcc	-0.3 to $+7.0$	v
<pre>Input voltage(*1)</pre>	VIN	-0.3(*2)to Vcc+0.3	v
Operating temperature	Topr	0 to + 7 0	C
Storage temperature	Tstg	-55 to $+150$	C

Note) *1. The maximum applicable voltage on any pin with respect to GND. *2. Undershoot of -3.0V is allowed width of pulse bellow 50ns.

6. Recommended DC Operating Conditions

(Ta = 0 C to + 7 0 C)

.

Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Vcc	4.5	5.0	5.5	v
Input voltage	VIH	2.2		Vcc+0.3	V
	VIL	- 0.3 (*3)		0.8	V

Note) *3. Undershoot of -3.0V is allowed width of pulse below 50ns.

7.DC Electrical Characteristics

		(Ta = 0 C to + 7 0 C , Vcc = 4.5 V to 5.5 V)					
Parameter	Symbol	Conditions	Min.	Тур. (*4)	Max.	Unit	
Input leakage	ILI	$V_{IN} = 0V$ to V_{CC}					
current				-1.0		1.0	μΑ
Output leakage	ILO	СЕ = Уін					
current		VI/0=0V to Vcc		-1.0		1.0	μΑ
Operating	Icci	CE =0V, Other input is OV t					
supply		$I_{I/O} = OmA, (\overline{OE} = V_{CC})$			25	30	m A
current	I CC2	CE =VIL, Other input is VIL t	о Vін				
		$I_{I/O} = OmA, (\overline{OE} = V_{IH})$			30	4 0	m A
Standby	Іѕв	$\overline{\text{CE}} \geq V_{\text{cc}} - 0.2 \text{V}$	T a = 2 5 $^{\circ}$ C			0.2	μΑ
current		Other input is OV to V_{CC}				1.0	μΑ
Output	Vol	IoL= 2.1mA			0.4	V	
voltage	Vон	Ion=-1.OmA		2.4			V

Note) *4. Typical values at Vcc=5.0V, Ta=25°C.



8. AC Electrical Characteristics

AC Test Conditions

Input pulse level	0.8V to 2.2V
Input rise and fall time	1 0 n s
Input and Output timing Ref. level	1.5V
Output load	$1 T T L + C_{L} (1 0 0 p F) (* 5)$

Note) *****5. Including scope and jig capacitance.

Read cycle

$(Ta = 0 \ C \ to + 7 \ 0 \ C \ , Vcc = 4.5 \ V \ to \ 5.5 \ V)$									
Parameter	Symbol	Min.	Max.	Unit					
Read cycle time	trc	100		ns	1				
Address access time	t a a		100	ns	1				
CE access time	t ACE		100	ns]				
Output enable to output valid	toe		4 0	ns					
Output hold from address change	tон	10		ns]				
CE Low to output active	tclz	10		ns	* 6				
OE Low to output active	tolz	10		ns	*6				
CE High to output in High impedance	tснz	0	4 0	ns	* 6				
OE High to output in High impedance	tонz	0	4 0	ns	* 6				

Write cycle

 $(Ta = 0 \ C \ to + 7 \ 0 \ C \ Vcc = 4.5 \ V \ to \ 5.5 \ V)$

Parameter	Symbol	Min.	Max.	Unit	
Write cycle time	t wc	100		ns	1
CE Low to end of write	tcw	80		ns	1
Address valid to end of write	t aw	80		ns	1
Address setup time	t a s	0		ns	1
Write pulse width	twp	60		ns	
Write recovery time	twr	10		ns	
Input data setup time	tow	30		ns	
Input data hold time	t DH	10		ns	
WE High to output active	tow	10		ns	,
WE Low to output in High impedance	t wн z	0	30	ns	,
OE High to output in High impedance	tонz	0	40	ns	>

Note) *6. Active output to High impedance and High impedance to output active tests specified for a ± 200 mV transition from steady state levels into the test load.



$L\,H\,5\,1\,1\,6\,N\,C$

9.Data Retention Characteristics

					(Ta= 0 °C	C to +	7 0 C)
Parameter	Symbol	Conditions	Min.	Тур. (*7)	Max.	Unit	
Data Retention	Vccdr	$\overline{CE} \ge V ccdr - 0.2 V$	۱.				
supply voltage				2.0		5.5	V
Data Retention	ICCDR	$V_{CCDR} = 2.0 V$	T a = 2 5 ℃		-	0.2	μΑ
supply current		$\overline{CE} \ge V ccdr - 0.2 V$	7		1	1.0	μΑ
Chip enable			-		-		
setup time	tcdr			0			ns
Chip enable				(*8)			
hold time	tr			t rc			ns

Note) *****7. Typical values at Ta=25℃

*8. Read Cycle

10.Pin Capacitance

(Ta = 25 C, f = 1 M H z)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	Cin	$V_{IN} = 0 V$			7	рF	*9
I/O capacitance	C 1 / 0	$V_{1/0} = 0 V$			10	рF	* 9

Note) * 9. This parameter is sampled and not production tested.



LH5116NC



LH5116NC



LH5116NC

12 Package and packing specification
[Applicability]
This specification applies to IC package of the LEAD-FREE delivered as a standard specification.
1.Storage Conditions.
1-1. Storage conditions required before opening the dry packing.
• Normal temperature : $5 \sim 40^{\circ}$ C
• Normal humidity : 80% (Relative humidity) max.
"Humidity" means "Relative humidity"
1-2. Storage conditions required after opening the dry packing.
In order to prevent moisture absorption after opening, ensure the following storage
 (1) Storage conditions for one-time soldering. (Convection reflow^{*1}, IR/Convection reflow.^{*1}
-
or Manual soldering.) • Temperature : $5 \sim 25^{\circ}$ C
• Humidity : 60% max.
Period : 96 hours max. after opening.
(2) Storage conditions for one-time soldering. (Solder dipping.)
• Temperature : $5 \sim 25^{\circ}$ C
• Humidity : 60% Max.
• Period : 96 hours max. after opening.
(3) Storage conditions for two-time soldering. (Convection reflow ^{*1} , IR/Convection reflow. ^{*1})
a. Storage conditions following opening and prior to performing the 1st reflow.
• Temperature : $5 \sim 25^{\circ}$ C
• Humidity : 60% max.
Period : 96 hours max. after opening.
b. Storage conditions following completion of the 1st reflow and prior to performing
the 2nd reflow.
• Temperature : $5 \sim 25^{\circ}$ C
• Humidity : 60% max.
• Period : 96 hours max. after completion of the 1st reflow.
^{*1} :Air or nitrogen environment.
1-3. Temporary storage after opening.
To re-store the devices before soldering, do so only once and use a dry box or place desiccant
(with a blue humidity indicator) with the devices and perform dry packing again using
heat-sealing.
The storage period, temperature and humidity must be as follows :
(1) Storage temperature and humidity.
%1: External atmosphere temperature and humidity of the dry packing.
First opening 4 X1 \longrightarrow Re sealing 4 Y \longrightarrow Re opening 4 X2 \longrightarrow Mounting
※1 Temperature : 5~40℃ 5~25℃ ※1 5~40℃ 5~25℃ Humidity : 80% max. 60% max. 80% max. 60% max.
пипицу обланах. Обланах. Обланах. Обланах. Обланах.
(2) Storage period.
 X1+X2 : Refer to Section 1-2(1),(2), and (3)a, depending on the mounting method.

• Y : Two weeks max.



- 2. Baking Condition.
 - (1) Situations requiring baking before mounting.
 - Storage conditions exceed the limits specified in Section 1-2 or 1-3.
 - · Humidity indicator in the desiccant was already red (pink) when opened.
 - (Also for re-opening.)
 - (2) Recommended baking conditions.
 - Baking temperature and period : 120°C for 16~24 hours or 150°C for 5~10 hours.
 - The above baking conditions do not apply since the magazines are
 - not heat-resistant . Replace the devices on heat-resistant magazine .
 - (3) Storage after baking.
 - After baking, store the devices in the environment specified in Section 1-2 and mount immediately.

250°C max.

40 to 60 seconds as 220°C

It is 1 to 3°C/seconds

It is 150 to 200°C, and is 120±30 seconds

3. Surface mount conditions.

The following soldering condition are recommended to ensure device quality.

- 3-1.Soldering.
- Convection reflow or IR/Convection. (one-time soldering or two-time soldering in air or nitrogen environment)
 - Temperature and period :
 - A) Peak temperature.
 - B) Heating temperature.
 - C) Preheat temperature.
 - D) Temperature increase rate.
 - · Measuring point : IC package surface.
 - Temperature profile :

A B B C backage surface temperature D D

- (2) Solder dipping. (one-time dipping only)
 - Temperature and period :
 - A) Peak temperature. 260°C max. for 10 seconds Max.
 - B) Preheat temperature of 120 to 150°C for 120±60 seconds
 - Measuring point :
 - A) Solder bath.
 - B) IC package surface.
 - Temperature profile:



- (3) Manual soldering (soldering iron) (one-time soldering only) Soldering iron should only touch the IC's outer leads.
 - Temperature and period :
 - 350°C max. for 3 seconds / pin max.
 - (Soldering iron should only touch the IC's outer leads.)
 - · Measuring point : Soldering iron tip.
- 4. Condition for removal of residual flux.
 - (1) Ultrasonic washing power : 25 watts / liter max.
 - (2) Washing time : Total 1 minute max.
 - (3) Solvent temperature : $15 \sim 40^{\circ}$ C

5. Package outline specification.

Refer to the attached drawing.

(Plastic body dimensions do not include burr of resin.)

The contents of LEAD-FREE TYPE application of the specifications. (*2)

6. Markings.

6-1. Marking details. (The information on the package should be given as follows.)

(1) Product name : LH5116NA-10F

(2) Company name : SHARP

- (3) Date code : (Example) YYWW XXX
 - $YY \rightarrow D$ enotes the production year. (Last two digits of the year.)
 - WW \rightarrow Denotes the production week. $(01 \cdot 02 \cdot \sim \cdot 52 \cdot 53)$
 - XXX \rightarrow Denotes the production ref. code (1~3 digits).
- (4) "JAPAN" indicates the country of origin.

6-2. Marking layout.

The layout is shown in the attached drawing.

(However, this layout does not specify the size of the marking character and marking position.)

*2 The contents of LEAD-FREE TYPE application of the specifications.

LEAD FINISH or BALL TYPE	LEAD-FREE TYPE (Sn-Bi)
DATE CODE	They are those with an underline.
The word of "LEAD FREE" is printed on the packing label	Printed







7.Packing Specifications(Dry packing for surface mount packages.)

7-1.Pac	kıng	material	S

7 1.1 acking materials.		
Material name	Material specifications	Purpose
Inner carton	Gardboard (1200 devices / inner carton max.)	Packing the devices.
Magazine	Anti-static treated plastic (30 devices / magazine)	Securing the devices.
Stopper	Plastic or rubber	Securing the devices.
Сар	Plastic (2 caps / bag)	Securing the magazine.
Laminated aluminum bag	Aluminum polyethylene	Keeping the devices dry.
Desiccant	Silica gel	Keeping the devices dry.
Label	Paper	Indicates part number, quantity , and packed date .
Outer carton	Gardboard (4800 devices / outer carton max.)	Outer packing.

(Devices must be placed on the magazine in the same direction.)

- 7-2. Outline dimension of magazine.
 - Refer to the attached drawing.
- 7-3.Outline dimension of carton.

Refer to the attached drawing.

8.Precautions for use.

- (1) Opening must be done on an anti-ESD treated workbench. All workers must also have undergone anti- ESD treatment.
- (2) The magazines have undergone either conductive or anti-ESD treatment. If another magazine is used , make sure it has also undergone conductive or anti-ESD treatment.
- (3) The devices should be mounted within one year of the date of delivery.













SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.

Suggested applications (if any) are for standard use; See Important Restrictions for limitations on special applications. See Limited Warranty for SHARP's product warranty. The Limited Warranty is in lieu, and exclusive of, all other warranties, express or implied. ALL EXPRESS AND IMPLIED WARRANTIES, INCLUDING THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR USE AND FITNESS FOR A PARTICULAR PURPOSE, ARE SPECIFICALLY EXCLUDED. In no event will SHARP be liable, or in any way responsible, for any incidental or consequential economic or property damage.

SHARP®

NORTH AMERICA

SHARP Microelectronics of the Americas 5700 NW Pacific Rim Blvd. Camas, WA 98607, U.S.A. Phone: (1) 360-834-2500 Fax: (1) 360-834-8903 Fast Info: (1) 800-833-9437 www.sharpsma.com

TAIWAN

SHARP Electronic Components (Taiwan) Corporation 8F-A, No. 16, Sec. 4, Nanking E. Rd. Taipei, Taiwan, Republic of China Phone: (886) 2-2577-7341 Fax: (886) 2-2577-7326/2-2577-7328

CHINA

SHARP Microelectronics of China (Shanghai) Co., Ltd. 28 Xin Jin Qiao Road King Tower 16F Pudong Shanghai, 201206 P.R. China Phone: (86) 21-5854-7710/21-5834-6056 Fax: (86) 21-5854-4340/21-5834-6057 Head Office:

No. 360, Bashen Road,

Xin Development Bldg. 22 Waigaoqiao Free Trade Zone Shanghai 200131 P.R. China Email: smc@china.global.sharp.co.jp

EUROPE

SHARP Microelectronics Europe Division of Sharp Electronics (Europe) GmbH Sonninstrasse 3 20097 Hamburg, Germany Phone: (49) 40-2376-2286 Fax: (49) 40-2376-2232 www.sharpsme.com

SINGAPORE

SHARP Electronics (Singapore) PTE., Ltd. 438A, Alexandra Road, #05-01/02 Alexandra Technopark, Singapore 119967 Phone: (65) 271-3566 Fax: (65) 271-3855

HONG KONG

SHARP-ROXY (Hong Kong) Ltd. 3rd Business Division, 17/F, Admiralty Centre, Tower 1 18 Harcourt Road, Hong Kong Phone: (852) 28229311 Fax: (852) 28660779 www.sharp.com.hk **Shenzhen Representative Office:** Room 13B1, Tower C, Electronics Science & Technology Building Shen Nan Zhong Road Shenzhen, P.R. China Phone: (86) 755-3273731 Fax: (86) 755-3273735

JAPAN

SHARP Corporation Electronic Components & Devices 22-22 Nagaike-cho, Abeno-Ku Osaka 545-8522, Japan Phone: (81) 6-6621-1221 Fax: (81) 6117-725300/6117-725301 www.sharp-world.com

KOREA

SHARP Electronic Components (Korea) Corporation RM 501 Geosung B/D, 541 Dohwa-dong, Mapo-ku Seoul 121-701, Korea Phone: (82) 2-711-5813 ~ 8 Fax: (82) 2-711-5819