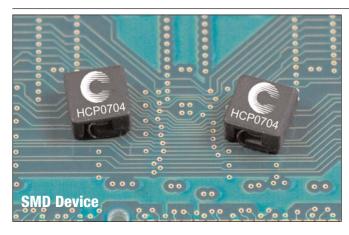


# **High Current, High Frequency, Power Inductors**

# **HCP0704 Series**



#### **Description:**

- Halogen free
- 155°C maximum total temperature operation
- 6.8 x 6.8 x 4.2mm surface mount package
- · Powder iron core material
- · Magnetically shielded, low EMI
- High temperature core material eliminates thermal aging issues
- High current carrying capacity, low core losses
- Controlled DCR tolerance for sensing circuits
- $\bullet$  Inductance range from  $0.40\mu H$  to  $4.7\mu H$
- Current range from 5.0 to 27 amps
- Frequency range up to 2MHz
- RoHS compliant

#### **Applications:**

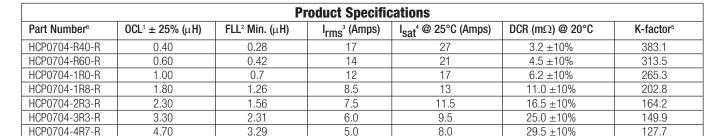
- Voltage Regulator Module (VRM)
- Multi-phase regulators
- Desktop and servers
- Base station equipment
- · Notebook regulators
- Data networking and storage systems
- · Point-of-load modules
- Battery power systems
- DCR sensing

#### **Environmental Data:**

- Storage temperature range: -40°C to +155°C
- Operating temperature range: -40°C to +155°C (Range is application specific)
- Solder reflow temperature: J-STD-020D compliant

#### **Packaging:**

• Supplied in tape-and-reel packaging, 1000 parts per reel, 13" diameter reel



- 1 Open Circuit Inductance (OCL) Test Parameters: 100kHz,  $0.10V_{rms}$ , 0.0Adc
- 2~ Full Load Inductance (FLL) Test Parameters: 100kHz, 0.1V  $_{rms}, \mathrm{I}_{sat}\mathrm{1}$
- 3 I<sub>rms</sub>: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 155°C under worst case operating conditions verified in the end application.
- 4 I<sub>sat</sub>: Peak current for approximately 20% rolloff at +25°C.
- 5 K-factor: Used to determine B<sub>p-p</sub> for core loss (see graph). B<sub>p-p</sub> = K \* L \* ΔI : (Gauss), K: (K-factor from table), L: (inductance in μH), ΔI (peak-to-peak ripple current in amps).

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- 6 Part Number Definition: HCP0704-xxx-R
- HCP0704 = Product code and size
- ullet xxx= Inductance value in  $\mu$ H, R = decimal point. If no "R" is present, then third character = # of zeros
- "-R" suffix = RoHS compliant

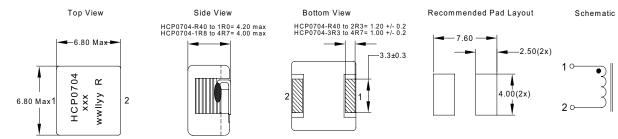


**HALOGEN** 

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#### Dimensions - mm

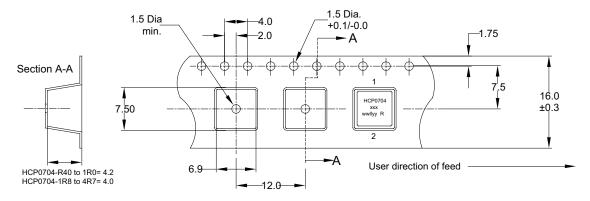


The nominal DCR test point is in the middle of the terminal

Part Marking: HCP0704

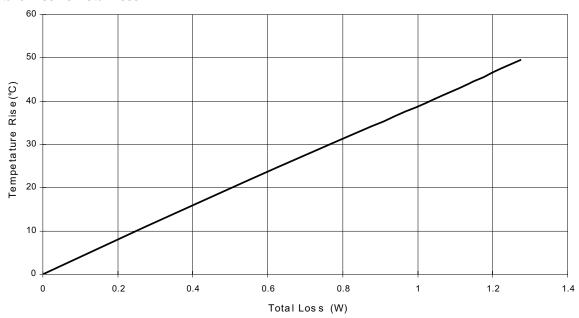
 $\mathsf{R} = \mathsf{Revision} \; \mathsf{level}$ 

# **Packaging Information - mm**



Supplied in tape-and-reel packaging, 1000 parts per reel, 13" diameter reel.

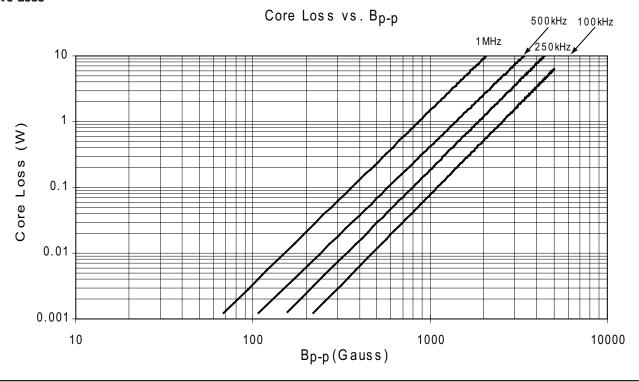
### **Temperature Rise vs. Total Loss**



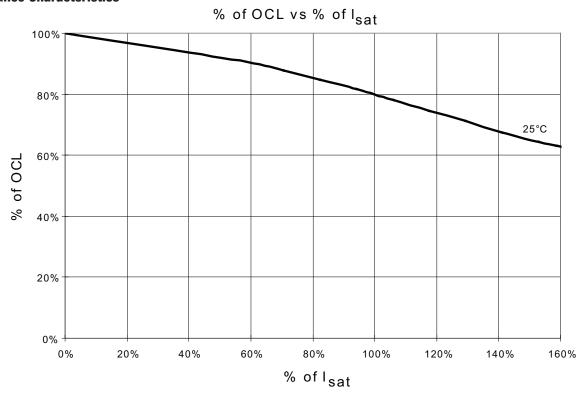
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# **Core Loss**



### **Inductance Characteristics**



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#### **Solder Reflow Profile**

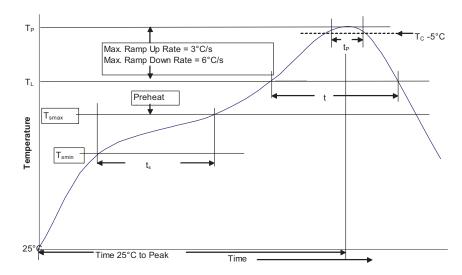


Table 1 - Standard SnPb Solder (T<sub>c</sub>)

	Volume	Volume
Package	mm³	mm³
Thickness	<350	≥350
<2.5mm	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

#### **Reference JDEC J-STD-020D**

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak	• Temperature min. (T <sub>smin</sub> )	100°C	150°C
	Temperature max. (T <sub>smax</sub> )	150°C	200°C
	• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds
Average ramp up rat	te T <sub>smax</sub> to T <sub>p</sub>	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperatu Time at liquidous (t <sub>L</sub>		183°C 60-150 Seconds	217°C 60-150 Seconds
Peak package body	temperature (T <sub>P</sub> )*	Table 1	Table 2
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**
Average ramp-down	rate (T <sub>p</sub> to T <sub>smax</sub> )	6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.

 $<sup>^{\</sup>star}$  Tolerance for peak profile temperature ( $T_{\rm p}$ ) is defined as a supplier minimum and a user maximum.

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<sup>\*\*</sup> Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.