**PQ1CY1032Z** SHARP

# **PQ1CY1032Z**

## ■ Features

- 1. Maximum switching current:3.5A
- 2. Built-in ON/OFF control function
- 3. Built-in soft start function to suppress overshoot of output voltage in power on sequence or ON/OFF control sequence
- 4. Built-in oscillation circuit (Oscillation frequency:TYP. 150kHz)
- 5. Built-in overheat protection function
- 6. Built-in overcurrent shut-down function
- 7. TO-263 package
- 8. PQ1CY1032ZZ:Sleeve-packaged product PQ1CY1032ZP: Tape-packaged product

■ Absolute Maximum Ratings

9. Variable output voltage (Output variable range: V<sub>ref</sub> to 35V/-V<sub>ref</sub> to -30V) [Possible to select step-down output/inversing output

# ■ Applications

- 1. LCD monitors
- 2. Car navigation systems
- 3. Switching power supplies

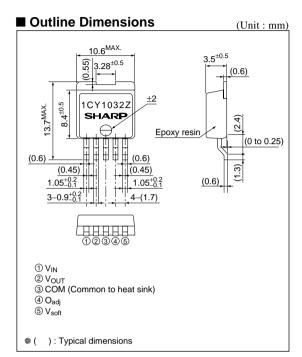
Parameter	Symbol	Rating	Unit			
*1 Input voltage	Vin	40	V			
Error input voltage	V <sub>ADJ</sub>	7	V			
Input-output voltage	V <sub>I-O</sub>	41	V			
*2Output to COM voltage	Vout	-1	V			
*3 V <sub>soft</sub> terminal voltage	V <sub>soft</sub>	-0.3 to +40	V			
Switching current	Isw	3.5	A			
*4 Power dissipation	PD	35	W			
*5 Junction temperature	Tj	150	°C			
Operating temperature	Topr	-20 to +85	°C			
Storage temperature	Tstg	-40 to +150	°C			

<sup>\*1</sup> Voltage between V<sub>IN</sub> terminal and COM terminal

\*6 Soldering temperature

\*6 For 10s

# **TO-263 Surface Mount Type Chopper Regulator**



(Ta=25°C)

°C

260

<sup>\*2</sup> Voltage between VOUT terminal and COM terminal

<sup>\*3</sup> Voltage between VSOFT terminal and COM terminal

<sup>\*4</sup> PD:With infinite heat sink

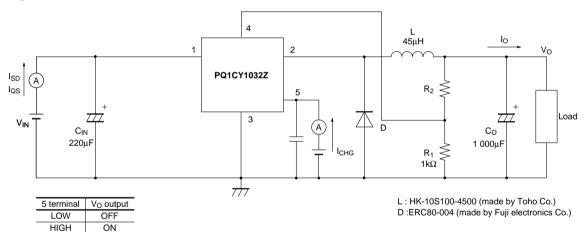
<sup>\*5</sup> Over heat protection may operate at the condition T<sub>i</sub>=125°C to 150°C

■ Electrical Characteristics	eristics (Unless otherwise specified, condition shall be V <sub>IN</sub> =12V, Io=0.5A, Vo=5V, V <sub>soft</sub> terminal=0.1μF, Ta=25°C)							
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Output saturation voltage	Vsat	Isw=3A	_	1.4	1.8	V		
Reference voltage	V <sub>ref</sub>	-	1.235	1.26	1.285	V		
Reference voltage temperature fluctuation	$\Delta V_{ref}$	Tj=0 to 125°C	_	±0.5	_	%		
Load regulation	RegL	Io=0.5 to 3A	_	0.2	1.5	%		
Line regulation	RegI	V <sub>IN</sub> =8 to 35V	_	1	2.5	%		
Efficiency	η	Io=3A	_	80	_	%		
Oscillation frequency	fo	_	135	150	165	kHz		
Oscillation frequency temperature fluctuation	Δfo	T <sub>j</sub> =0 to 125°C	_	±2	_	%		
Overcurrent detection level	IL	-	3.6	4.2	5.8	A		
Charge current	IснG	②,4 terminals is open,5 terminal	_	-10	_	μΑ		
Input threshold voltage	V <sub>THL</sub>	Duty ratio=0%, 4 terminal=0V, 5 terminal	_	1.3	_	V		
	V <sub>THH</sub>	Duty ratio=100%, 4 terminals is open, 5 terminal	_	2.3	_	V		
ON threshold voltage	V <sub>TH(ON)</sub>	4 terminal=0V, 5 terminal	0.7	0.8	0.9	V		
Overcurrent shutdown threshold voltage	V <sub>THIL</sub>	⑤ terminal	3.8	4.6	5.5	V		
Stand-by current	Isd	V <sub>IN</sub> =40V, 5 terminal=0V	_	140	400	μΑ		
Output OFF-state consumption current	Iqs	V <sub>IN</sub> =40V, (5) terminal=0.9V	_	8	16	mA		

Fig.1 Standard Test Circuit

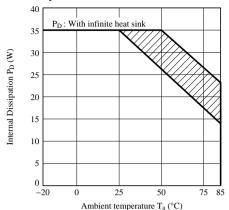
OPEN

ON



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Fig.2 Internal Dissipation vs. Ambient Temperature



Note) Oblique line prtion:Overheat protection may operate in this area

Fig.4 Efficiency vs. Input Voltage

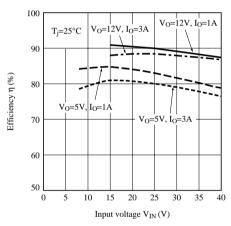


Fig.6 Operating Consumption Current vs. Input Voltage

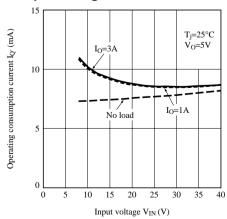


Fig.3 Overcurrent Protection Characteristics (Typical Value)

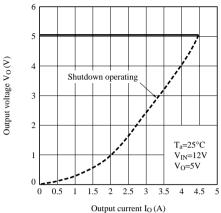


Fig.5 Switching Current vs. Output Saturation Voltage

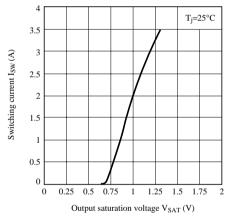
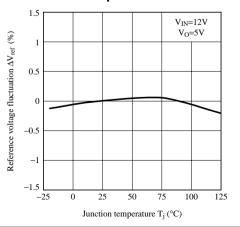


Fig.7 Reference Voltage Fluctuation vs. Junction Temperature



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Fig.8 Load Regulation vs. Output Current

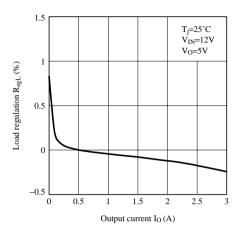


Fig.10 Oscillation Frequency Fluctuation vs. Junction Temperature

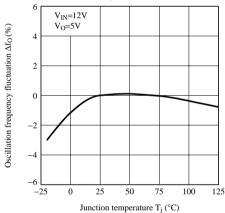


Fig.12 On Threshold Voltage vs. Junction Temperature

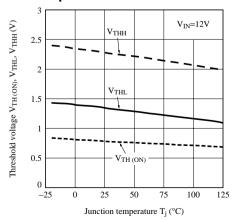


Fig.9 Line Regulation vs. Input Voltage

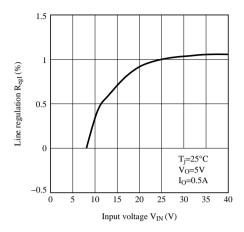


Fig.11 Overcurrent Detection Level Fluctuation vs.
Junction Temperature

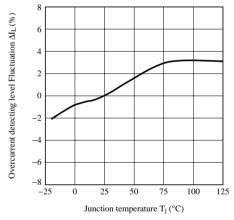
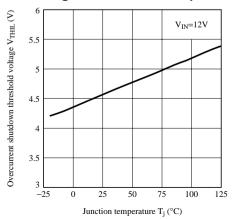
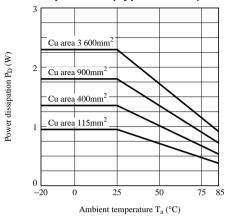


Fig.13 Overcurrent Shutdown Threshold Voltage vs. Junction Temperature



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Fig.14 Power Dissipation vs. Ambient Temperature (Typical Value)



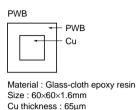


Fig.15 Block Diagram

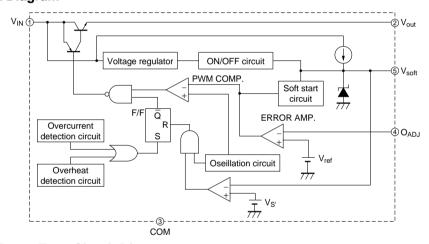


Fig.16 Step Down Type Circuit Diagram

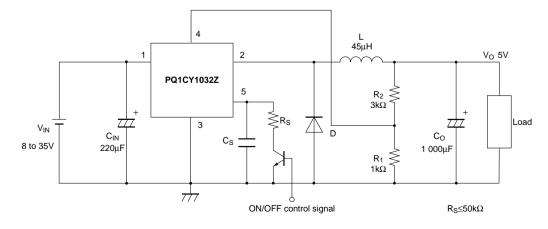
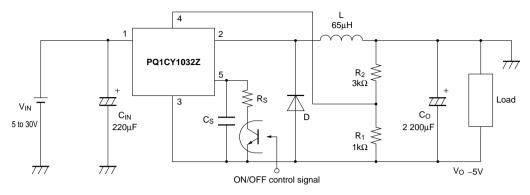
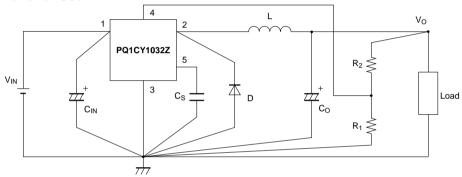


Fig.17 Polarity Inversion Type Circuit Diagram

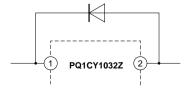


## ■ Precautions for Use



#### 1. External connection

- (1) Wiring condition is very important. Noise associated with wiring inductance may cause problems. For minimizing inductance, it is recommended to design the thick and short pattern (between large current diodos, input/output capacitors, and terminal 1,2.) Single-point grounding (as indicated) should be used for best results.
- (2) High switching speed and low forward voltage type schottky barrier diode should be recommended for the catch-diode D because it affects the efficiency. Please select the diode which the current rating is at least 1.2 times greater than maximum switching current.
- (3) The output ripple voltage is highly influenced by ESR (Equivalent Series Resistor) of output capacitor, and can be minimized by selecting Low ESR capacitor.
- (4) An inductor should not be operated beyond its maximum rated current so that it may not saturate.
- (5) When voltage that is higher than  $V_{IN}$  ①, is applied to  $V_{OUT}$  ②, there is the case that the device is broken. Especially, in case  $V_{IN}$  ① is shorted to GND in normal condition, there is the case that the device is broken since the charged electric charge in output capacitor ( $C_0$ ) flows into input side. In such case a schottly barrier diode or a silicon diode shall be recommended to connect as the following circuit.



# ■ Thermal Protection Design

Internal power dissipation(P) of device is generally obtained by the following equation.

P=Isw(Average.) × Vsat × D' + Vin(voltage between Vin to COM terminal) × Iq'(consumption current)

Step down type

$$D'(Duty) = \frac{T_{on}}{T(period)} = -\frac{V_{O} + V_{F}}{V_{IN} - V_{SAT} + V_{F}}$$

Isw(Average)= Io(Output current.)

Polarity inversion type

$$\begin{split} &D'(Duty) = \frac{T_{on}}{T(period)} = \frac{|V_O| + V_F}{V_{IN+}|V_O| - V_{SAT} + V_F} \\ &Isw(Average) = -\frac{1}{1 - D'} - \times Io(Output \ current.) \end{split}$$

V<sub>F</sub>: Forward voltage of the diode

When ambient temperature Ta and power dissipation  $P_D(MAX)$  during operation are determined, use Cu plate which allows the element to operate within the safety operation area specified by the derating curve. Insufficient radiation gives an unfavorable influence to the normal operation and reliability of the device.

## ■ ON/OFF Control Terminal

1. In the following circuit, when  $V_{soft}$  terminal  $\odot$  becomes low (loss than  $V_{THON}$ ) by switching transistor Tr on, output voltage may be turned OFF and the device becomes stand-by mode. Dissipation current at stand-by mode becomes Max.400 $\mu$ A.

When transistor Tr becomes OFF, output voltage can be ON.

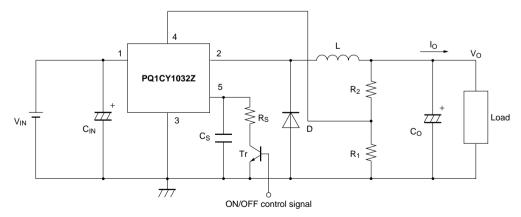
External resistor Rs should be leaded to avoid discharge current of C<sub>s</sub>, and not to break the transistor Tr.

2. Soft startup

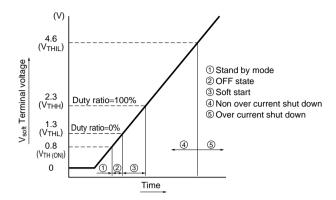
When capacitor C<sub>S</sub> is loaded, output pulse gradually expanded and output voltage will start softly.

3. Over current protection

When the voltage of  $V_{soft}$  is more than  $V_{THIL}$ , over current shut down function will operate. And when the voltage of  $V_{soft}$  is less than  $V_{THIL}$ , over current protection function will operate. Since the **PQ1CY1032Z** must use an capacitor  $C_S$ ,  $V_{soft}$  should be more than  $V_{THIL}$ , over current shut down function will operate.



# ■ ON-OFF Terminal Voltage vs. Time



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