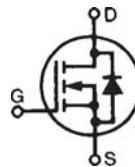


**HiPerFET™
Power MOSFET
Q2-Class**

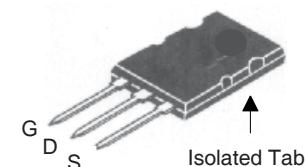
IXFL38N100Q2

N-Channel Enhancement Mode
Avalanche Rated, Low Q_g , Low Intrinsic R_G
High dV/dt , Low t_{rr}



V_{DSS} = 1000V
 I_{D25} = 29A
 $R_{DS(on)}$ ≤ 280mΩ
 t_{rr} ≤ 300ns

ISOPLUS264™(IXFL)



G = Gate D = Drain
S = Source

Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	1000		V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GS} = 1\text{M}\Omega$	1000		V
V_{GSS}	Continuous	± 30		V
V_{GSM}	Transient	± 40		V
I_{D25}	$T_c = 25^\circ\text{C}$	29		A
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	152		A
I_A	$T_c = 25^\circ\text{C}$	38		A
E_{AS}	$T_c = 25^\circ\text{C}$	5		J
dV/dt	$I_s \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$	20		V/ns
P_d	$T_c = 25^\circ\text{C}$	380		W
T_J		-55 ... +150		°C
T_{JM}		150		°C
T_{stg}		-55 ... +150		°C
T_L	1.6 mm (0.063 in.) from case for 10s	300		°C
T_{SOLD}	Plastic body for 10s	260		°C
F_c	Mounting force	30..120/6.7..27		N/lbs
V_{ISOL}	$50/60\text{ Hz, RMS}$ $t = 1\text{ min}$ $I_{ISOL} \leq 1\text{ mA}$ $t = 1\text{ s}$	2500 3000	V~ V~	
Weight		10		g

Symbol	Test Conditions	Characteristic Values			
		($T_J = 25^\circ\text{C}$, unless otherwise specified)	Min.	Typ.	Max.
BV_{DSS}	$V_{GS} = 0\text{ V}$, $I_D = 1\text{ mA}$	1000			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 8\text{ mA}$	3.0		5.5	V
I_{GSS}	$V_{GS} = \pm 30\text{ V}$, $V_{DS} = 0\text{ V}$			± 200	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0\text{ V}$			100 5	μA mA
$R_{DS(on)}$	$V_{GS} = 10\text{ V}$, $I_D = 19\text{ A}$, Note 1			280	mΩ

Features

- Electrically isolated mounting tab
- Double metal process for low gate resistance
- Unclamped Inductive Switching (UIS) rated
- Low package inductance - easy to drive and to protect
- Fast intrinsic diode

Applications

- DC-DC converters
- Switched-mode and resonant-mode power supplies
- DC choppers
- Pulse generation
- Laser drivers

Advantages

- 2500 V~ Electrical isolation
- ISOPLUS 264™ package for clip or spring mounting
- Space savings
- High power density

Symbol	Test Conditions ($T_J = 25^\circ C$, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
g_{fs}	$V_{DS} = 20V, I_D = 19A$, Note 1	24	40	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$	13.5	nF	
		1035	pF	
		180	pF	
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	Resistive Switching Times $V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 19A$ $R_G = 1\Omega$ (External)	25	ns	
		28	ns	
		57	ns	
		15	ns	
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 19A$	250	nC	
		60	nC	
		105	nC	
R_{thJC}			0.33	°C/W
R_{thCS}		0.13		°C/W

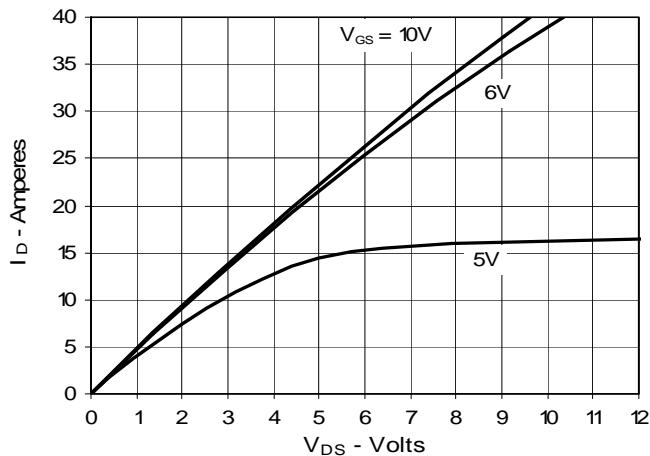
Source-Drain Diode

Symbol	Test Conditions ($T_J = 25^\circ C$, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
I_s	$V_{GS} = 0V$		38	A
I_{SM}	Repetitive, pulse width limited by T_{JM}		152	A
V_{SD}	$I_F = I_S, V_{GS} = 0V$, Note 1		1.5	V
t_{rr} Q_{RM} I_{RM}	$I_F = 25A, V_{GS} = 0V$ -di/dt = 100 A/μs $V_R = 100 V$		300	ns
			1.4	μC
			9.0	A

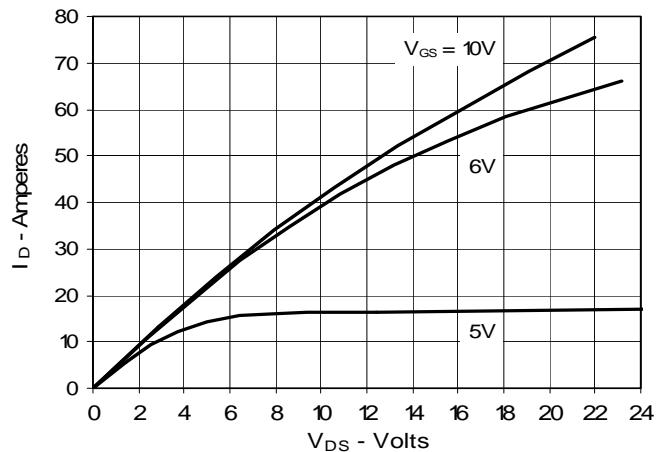
Note: 1. Pulse test, $t \leq 300\mu s$, duty cycle, $d \leq 2 \%$.

ISOPLUS264™ (IXFL) Outline																																																																																																														
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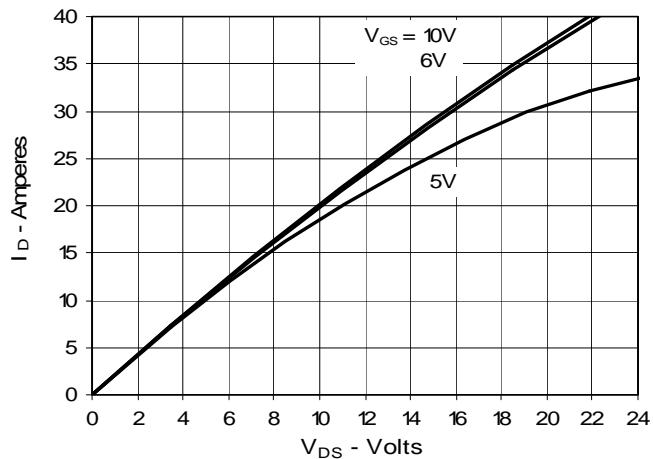
**Fig. 1. Output Characteristics
@ 25°C**



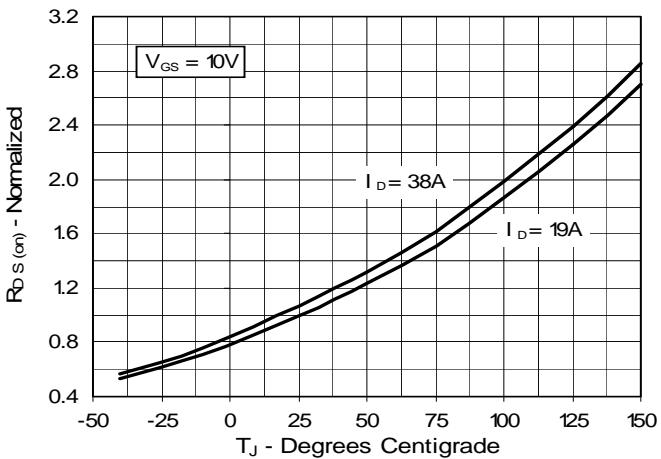
**Fig. 2. Extended Output Characteristics
@ 25°C**



**Fig. 3. Output Characteristics
@ 125°C**



**Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 19A$
Value vs. Junction Temperature**



**Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 19A$
Value vs. Drain Current**

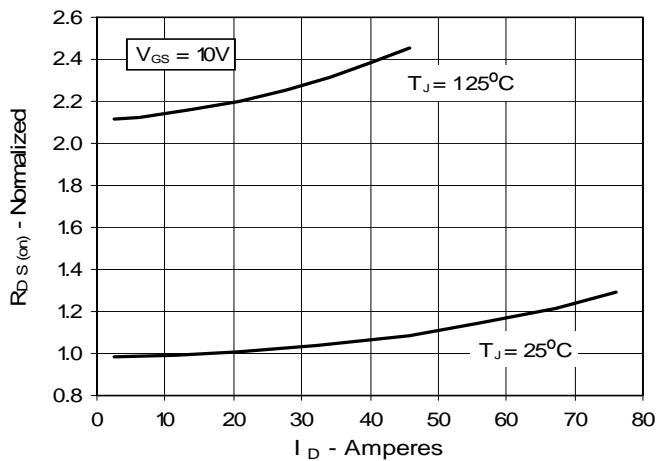


Fig. 6. Drain Current vs. Case Temperature

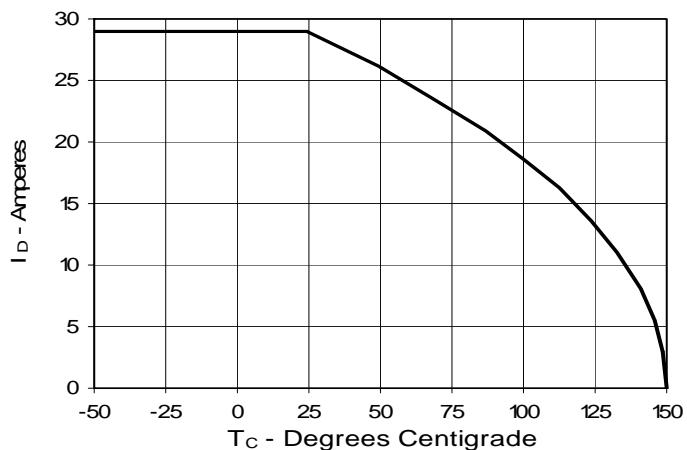
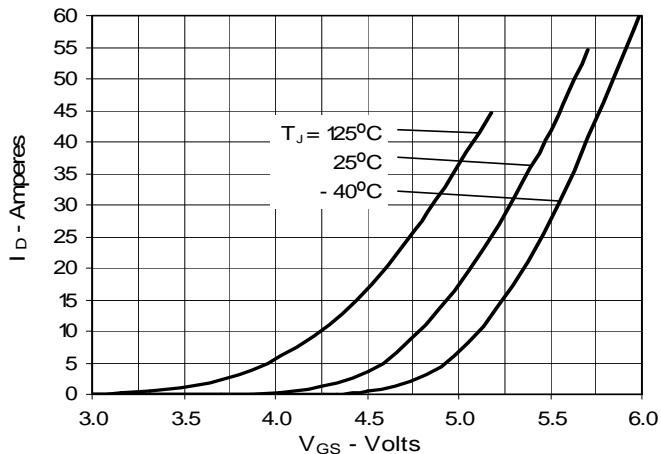
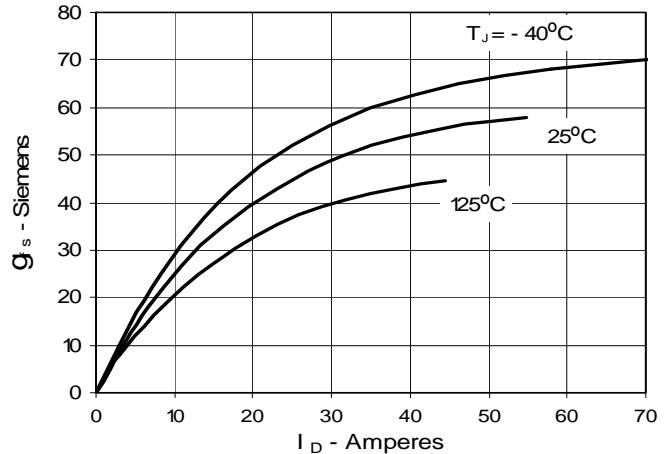
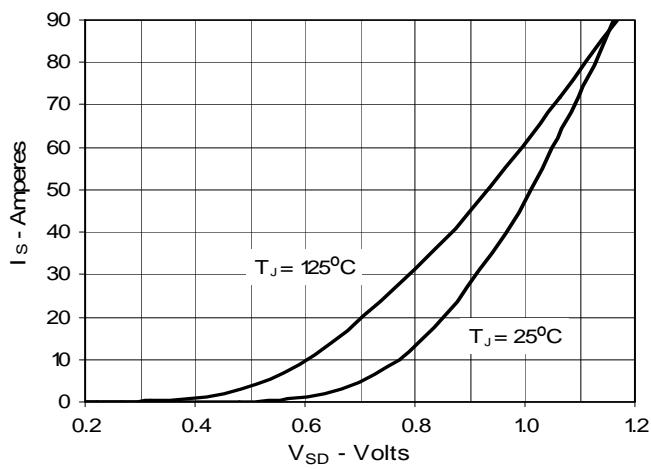
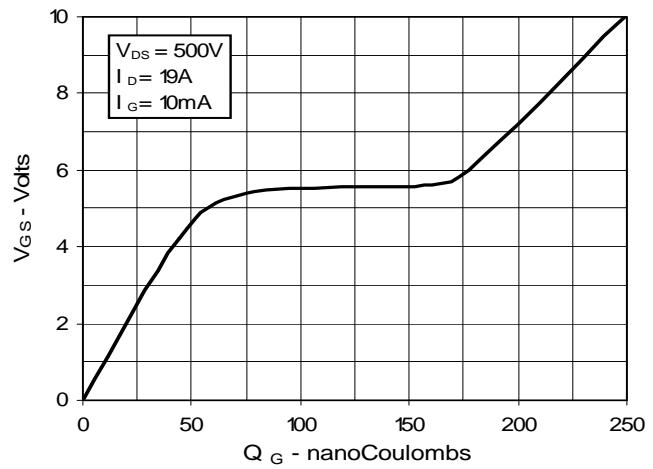
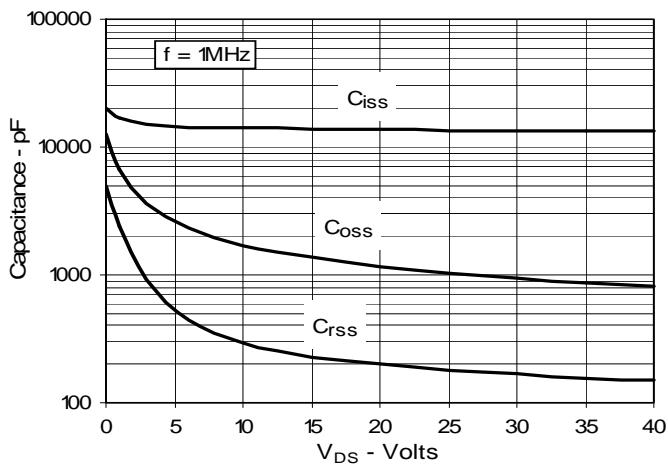


Fig. 7. Input Admittance**Fig. 8. Transconductance****Fig. 9. Source Current vs. Source-To-Drain Voltage****Fig. 10. Gate Charge****Fig. 11. Capacitance****Fig. 12. Maximum Transient Thermal Impedance**