HOA2003

Transmissive Optoschmitt Sensor

FEATURES

- Direct TTL interface
- Buffer logic
- 0.010 in.(2.54 mm) offset detector leads
- 0.125 in.(3.18 mm) slot width
- Accurate position sensing
- Dust protective housing



DESCRIPTION

The HOA2003 consists of an infrared emitting diode facing an Optoschmitt detector encased in a black thermoplastic housing. The photodetector consists of a photodiode, amplifier, voltage regulator, Schmitt trigger and an NPN output transistor with 10 $k\Omega$ (nominal) pullup resistor. The buffer logic provides a high output when the optical path is clear, and a low output when the path is interrupted. The HOA2003 utilizes an $\ensuremath{\mathsf{IR}}$ transmissive polysulfone housing which features smooth optical faces without external aperture openings; this feature is desirable when aperture blockage from airborne contaminants is a possibility. The detector has a 0.010 in.(.254 mm) x 0.040 in.(1.02 mm) vertical aperture which is ideal for use in applications in which maximum position resolution is desired. The HOA2003 employs plastic molded components. For additional component information see SEP8506 and SDP8600.



Housing material is polysulfone. Housings are soluble in chlorinated hydrocarbons and ketones. Recommended cleaning agents are methanol and isopropanol.

OUTLINE DIMENSIONS in inches (mm) Tolerance 3 plc decimals ±0.010(0.25) 2 plc decimals ±0.020(0.51)



DIM_064.ds4

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ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)								
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS		
IR EMITTER								
Forward Voltage	VF			1.6	V	l _F =20 mA		
Reverse Leakage Current	IR			10	μA	V _R =3 V		
DETECTOR								
Operating Supply Voltage	Vcc	4.5		10	V			
Low Level Supply Current	lcc∟	4.0		12	mA	Vcc=5 V		
Low Level Supply Current		5.0		15		Vcc=12 V		
High Level Supply Current	Іссн	2.0		10	mA	Vcc=5 V		
High Level Supply Current		3.0		12		Vcc=12 V		
Low Level Output Voltage	Vol			0.4	V	lo∟=12.8 mA, I _F =0 mA		
High Level Output Voltage	Vон	2.4			V	lон=0, I⊧=20 mA		
Hysteresis (2)	HYST		10		%			
Propagation Delay, Low-High	t PLH		5		μs	Vcc=5 V, I⊧=20 mA		
Propagation Delay, High-Low	t PHL		5		μs	Vcc=5 V, I⊧=20 mA		
Rise Time	tr		60		ns	RL=390 Ω, CL=50 pF		
Fall Time	t _f		15		ns	RL=390 Ω, CL=50 pF		
COUPLED CHARACTERISTICS IRED Trigger Current HOA2003-001	IFT			20	mA	V _{cc} =5 V		

Notes 1. It is recommended that a bypass capacitor, 0.1 µF typical, be added between V_{cc} and GND near the device in order to stabilize power supply line. 2. Hysteresis is defined as the difference between the operating and release threshold intensities, expressed as a percentage of the operate threshold intensity.

ABSOLUTE MAXIMUM RATINGS

(25°C Free-Air Temperature unless otherwise noted)

Operating Temperature Range	-40°C to
Storage Temperature Range	-40°C to
Soldering Temperature (5 sec)	240°C
IR EMITTER	
Power Dissipation	100 mW
Reverse Voltage	3 V
Continuous Forward Current	50 mA
DETECTOR	
Supply Voltage	12 V (2)
Output Sink Current	18 mA
Duration of Output	
Short to V_{CC} or Ground	1.0 sec.



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Fig. 1 gra_073.ds4 100 90 Pulsed Forward current - mA 80 condition 70 = 80°C 1 60 T. 50 40 25°C 30 20 -40°C 10 0 0.8 1.0 1.2 1.4 1.6 1.8 2.0 Forward voltage - V

IRED Forward Bias Characteristics

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