IrDA infrared communication module RPM872

RPM872 is an infrared communication module for IrDA Ver. 1.2 (Low Power). The infrared LED, PIN photo diode, and waveform shaping LSI are all integrated into one single package. This module is designed for low power consumption. The very small package makes it a perfect fit for mobile devices.

Features

- 1) Applied to IrDA Ver. 1.2. (Low Power)
- 2) Designed for low power consumption at waiting mode ($75\mu A$).
- 3) Low operating voltage

 V_{CC} =2.0V to 3.6V

VIO=1.5V to 3.6V

VLED=2.6V to 5.5V

- 4) Small package.
- 5) Power down function.

Applications

Cellular phones, PDA, Digital still camera, Handy terminals, and other portable equipments.

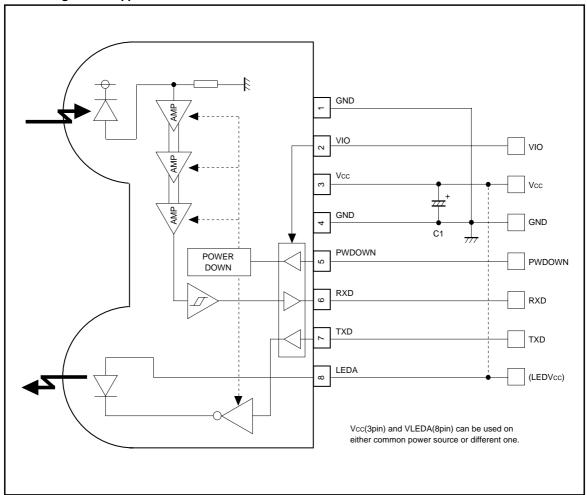
● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	7.0	V
Power dissipation	Pd	100	mW
Operating temperature	Topr	-20 to +85	°C
Storage temperature	Tstg	-30 to +100	°C

● Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Vcc	2.0	3.0	3.6	٧
	VLEDA	2.6	3.0	5.5	V
	VIO	1.5	3.0	Vcc	V

●Block diagram and application circuit



Terminal description

Pin No	Terminal	Circuit	Function
1, 4	GND		GND
2	VIO		VIO Supply voltage for I/O pins (PWDOWN, RXD, TXD)
3	Vcc		Vcc Supply voltage for Transceiver circuits. For preventing from infection, connect a capacitor between Vcc(3pin) and GND(4pin).
5	PWDOWN	VIO W	Power-down Control Terminal H: POWERDOWN MODE L: OPERATING MODE CMOS logic level input When input is H, it will stop the receiving circuit, Pin-PD current and transmitting LED operation.
6	RXD	PWDOWN VIO	Receiving Data Output Terminal CMOS logic level output When PWDOWN (5pin) = H, the RXD output will be pulled up to VIO at approximately $300k\Omega$.
7	TXD	VIO	Transmitting Data Input Terminal H: LED (PWDOWN = L) CMOS logic level input Holding TXD = "H"status, LED will be turn off approximately 48μs.
8	LEDA	LED	LED ANODE Terminal Other power source can be used difference between LEDVcc and Vcc. This can be connected to battery kinds of unregulated voltage source by internal constant current driver.

●Electrical characteristics (Unless otherwise noted, Vcc=3V, VLEDA=3V, VIO=3V, Ta=25°C)

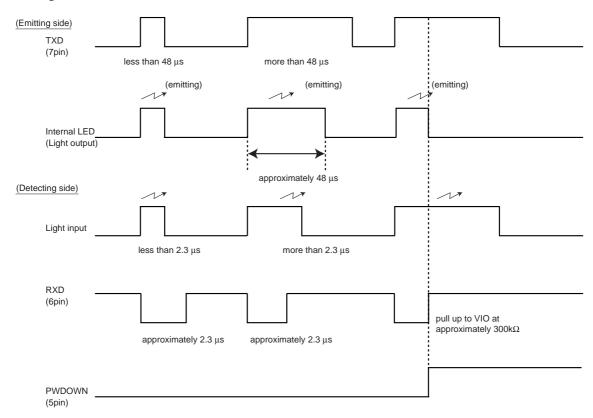
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Consumption Current 1	Icc1	-	75	99	μΑ	VPWDOWN=0V. At no input light
Consumption Current 2	Icc2	-	0.01	0.2	μΑ	VPWDOWN=VIO. At no input light
Transmission rate		2.4	-	115.2	kbps	
DIA/DOMALINIDUT USSIS VAIG	VPDH	2/3*VIO		VIO	V	VIO=1.8 to 3.6 [V]
PWDOWN INPUT High Voltage		1.2	_	VIO		VIO=1.5 to 1.8 [V]
PWDOWN INPUT Low Voltage	\ (0.0)		-	1/3*VIO	V	VIO=1.8 to 3.6 [V]
PWDOWN INPOT Low voltage	VPDL	0		VIO-1.2		VIO=1.5 to 1.8 [V]
PWDOWN INPUT High Current	IPDH	-1.0	0	1.0	μΑ	PWDOWN=VIO [V]
PWDOWN INPUT Low Current	IPDL	-1.0	0	1.0	μΑ	PWDOWN=0 [V]
<transmitter></transmitter>						
TXD INPUT High Voltage	VTXH	2/3*VIO	- VIO	V	VIO=1.8 to 3.6 [V]	
TAD INFOT High voltage		1.2	_	VIO	V	VIO=1.5 to 1.8 [V]
TXD INPUT Low Voltage	VTXL	0	-	1/3*VIO	V	VIO=1.8 to 3.6 [V]
TAD INPOT Low Voltage				VIO-1.2		VIO=1.5 to 1.8 [V]
TXD INPUT High Current	ITXH	2.5	5	10	μΑ	TXD=VIO [V]
TXD INPUT Low Current	ITXL	-1.0	0	1.0	μΑ	TXD=0 [V]
LED ANODE Current	ILEDA	_	30.5	_	mA	
<receiver></receiver>						
RXD OUTPUT High Voltage	VRXH	VIO-0.4	-	VIO	V	IRXH=-200μA
RXD OUTPUT Low Voltage	VRXL	0	-	0.4	V	IRXL=200μA
RXD OUTPUT Rise Time	tRR	_	35	_	ns	C _L =15pF
RXD OUTPUT Fall Time	tFR	-	35	-	ns	C _L =15pF
RXD OUTPUT Pulse Width	twRXD	1.5	2.3	4.2	μs	C _L =15pF, 2.4 to 115.2kbps
Receiver Latency Time	tRT	_	100	200	μs	

●Optical characteristics (Unless otherwise noted, Vcc=3V, VLEDA=3V, VIO=3V, Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Peak wave length	λP	850	870	900	nm	
Intensity 1	IE1	4.0	10	26	mW / Sr	$-15^{\circ} \le \theta$ L $\le 15^{\circ}$
Half-Angle	θL/2	_	±18	±30	deg	
Optical pulse width	TWLED	1.42	1.63	2.02	μs	TXD=1.63μs pulse input
Rise time / Fall time	Tr / Tf	_	_	100	ns	10% to 90%
Optical over shoot		_	_	25	%	
Edge jitter	Tj	-40	_	40	ns	
Minimum irradiance	Eemin	_	_	6.8	μW / cm ²	-15deg ≤ θL ≤ +15deg
Maximum irradiance	Eemax	500	_	_	mW / cm ²	-15deg ≤ θL ≤ +15deg
INPUT Half-Angular	θD / 2	±15	_	_	deg	
Maximum Emitting Time	TLEDmax	16	48	120	μs	TXD=VIO

This product is not designed for protection against radioactive rays.
 This product dose not include laser transmitter.
 This product includes one pin photo diode.
 This product dose not include optical load.

●Timing chart



Recommended values

Part symbol	Recommended value	Notice
C1	1μF, tantalum or ceramic Ex.) TCFGA1A105M8R (ROHM)	Bigger capacitance is recommended with much noise from power supply

Notes

- 1) VLEDA(8pin), Vcc(3pin) and VIO(2pin)
 - \cdot Other power source can be used difference between VLEDA and Vcc and VIO. (VIO < Vcc +0.3V)
- 2) Caution in designing board lay-out

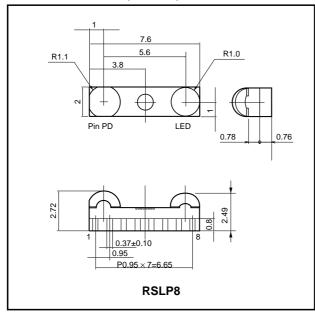
To get maximum potential from RPM872, please keep in mind following instruction.

- The line of RXD (6pin) should be connected at backside via through hole close to RPM872 pin lead. Better not to be close to photo diode side (1pin).
- ⇒This is to minimize feedback supplied to photo diode from RXD.
- · As for C1 between 3-4 pin should be placed close to RPM872.
- Better to be placed more than 1.0cm in radius from photo diode (pin1 side) and also away from the parts which generates noise, such as DC / DC converter.

3) Others

- Please be sure to set up the TXD (7pin) input to be "L" (under 0.3V) except transmitting data (for $< 90\mu$ s. On duty < 20%).
- · Powerdown current might increase if exposed by strong light (ex. direct sunlight) at powerdown mode.
- Please use by the signal format which is specified by IrDA Ver1.2 (Low Power). There might be on error if used by different signal format.
- Dust or dirt on lens portion may affect the characteristics, so pay sufficient attention.
- 4) Eye Safe
 - · IEC825-1 (EN60825-1) Class 1 Eye Safe.

●External dimensions (Unit : mm)



Rev.B

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Appendix1-Rev1.1

