

PROTECTION PRODUCTS - MicroClamp[®] Description

 μ Clamp[®] TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD. They are designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. They feature large cross-sectional area junctions for conducting high transient currents. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The μ Clamp®0545T is in a 6-pin SLP1007N6T package. It measures 1.0 x 0.7 mm with a nominal height of only 0.4mm. The leads are finished with lead-free NiPdAu. Each device will protect five lines operating at 5 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. They may be used to meet the ESD immunity requirements of IEC 61000-4-2. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Features

- High ESD withstand Voltage: +/-12kV (Contact) per IEC 61000-4-2
- Very small PCB area: 0.7mm²
- Protects up to five data lines
- Low reverse current: <10nA typical (VR=5V)</p>
- ◆ Working voltage: +/- 5V
- Low capacitance: 4pF typical
- Low dynamic resistance: 0.70 Ohms (Typ)
- Solid-state silicon-avalanche technology

Mechanical Characteristics

- SLP1007N6T package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 1.0 x 0.7 x 0.40 mm
- Lead Pitch: 0.35mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking : Marking code + dot matrix date code
- Packaging : Tape and Reel

Applications

- Cellular Handsets & Accessories
- Keypads, Side Keys, Audio Ports
- LCD Connectors
- Digital Lines
- Analog Video

Dimensions



Schematic & PIN Configuration



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Absolute Maximum Rating					
Rating	Symbol	Value	Units		
Peak Pulse Power (tp = 8/20µs)	P _{pk}	12	Watts		
Maximum Peak Pulse Current (tp = 8/20µs)	l _{pp}	1	Amps		
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	+/- 15 +/- 12	kV		
Operating Temperature	T	-55 to +125	°C		
Storage Temperature	T _{stg}	-55 to +150	°C		

Electrical Characteristics (T=25°C)

Γ

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}	Any I/O Pin to GND			5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA Any I/O Pin to GND	6	8.2	9.5	V
Reverse Leakage Current	I _R	V _{RWM} = 5V, T=25°C Any I/O Pin to GND		3	50	nA
Clamping Voltage	V _c	I _{PP} = 1A, tp = 8/20μs Any I/O Pin to GND			12	V
ESD Clamping Voltage	V _c	IPP = 16A, tlp = 0.2/100ns		19		V
Dynamic Resistance	R _{Dyn}	tp = 100ns		0.70		Ohms
Junction Capacitance	C _j	V _R = 0V, f = 1MHz Any I/O Pin to GND		4.5	9	pF

Waveform

Parameters tr = 8µs td = 20µs

1

1.2



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Typical Characteristics

Non-Repetitive Peak Pulse Power vs. Pulse Time



12 10 Clamping Voltage -V_c (V) 8

6

4

2

0

0

0.2

0.4

Clamping Voltage vs. Peak Pulse Current (tp=8/20us)

Typical Insertion Loss (S21) 30KHz to 3GhZ

0.6

Peak Pulse Current - IPP (A)

0.8



Normalized Junction Capacitance vs. Reverse Voltage





Typical Characteristics (Con't)



ESD Clamping (+8kV Contact per IEC 61000-4-2)

ESD Clamping (-8kV Contact per IEC 61000-4-2)







Typical Characteristics (Con't)

TLP Characteristic





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Applications Information

Device Connection Options

The μ Clamp0545T is designed to protect five data lines operating up to 5 volts. The device is bidirectional and may be used on lines where the signal polarity is above and below ground. The diagram at the right shows an example pin configuration with pin 2 connected to ground. However, due to the device symmetry, any pin may be connected to ground with the remaining pins conneted to the protected lines.

Assembly Guidelines

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joint. The table below provides Semtech's recommended assembly guidelines for mounting this device. The figure at the right details Semtech's recommended aperture based on the below recommendations. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. The exact manufacturing parameters will require some experimentation to get the desired solder application.

Assembly Parameter	Recommendation		
Solder Stencil Design	Laser cut, Electro-polished		
Aperture shape	Rectangular		
Solder Stencil Thickness	0.100 mm (0.004")		
Solder Paste Type	Type 4 size sphere or smaller		
Solder Reflow Profile	Per JEDEC J-STD-020		
PCB Solder Pad Design	Non-Solder mask defined		
PCB Pad Finish	OSP OR NiAu		

Example Pin Configuration



Recommended Mounting Pattern





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Outline Drawing - SLP1007N6T



Land Pattern - SLP1007N6T





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Marking Code



Notes:

Marking will also include line matrix date code

Carrier Tape Specification

Ordering Information

Part Number	Qty per Reel	Reel Size	
uClamp0545T.TNT	10,000	7 Inch	

Notes:

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SECTION A-A

Device Orientation in Tape



Pin 1 Location (Towards Sprocket Holes)

Contact Information

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