SmartSwitch[™]

DISTINCTIVE CHARACTERISTICS

Standard with Enhanced Illumination:

Programmable to display graphics, alphanumeric characters and animated sequences.

Standard SMARTDISPLAY[™] can be used alone or in conjunction with electromechanical switches.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Viewing area 14.4mm x 11.8mm (horizontal x vertical) at 36 x 24 pixels.



Actual Size



PART NUMBER & DESCRIPTION

Part Number	Terminals	LCD Mode	LED Color
ISO 1BBFEF	Straight PC	Yellow FSTN Positive	* Yellow/Green

LCD & LED SPECIFICATIONS

Characteristics of Display

Items

Supply Voltage for Logistics

Supply Voltage for LCD

Display Operation Mode	FSTN positive
Display Condition	Transflective with built-in LED backlight
Viewing Angle	6 o'clock
Driving Method	1/24 duty. 1/5 bias (built-in driving circuit)
Viewing Area	14.4mm x 11.8mm (horizontal x vertical)
Pixel Format	36 x 24 pixels (horizontal x vertical)
Pixel Size	0.371mm x 0.445mm (horizontal x vertical)
Operating Temp. Range	−20°C ~ +60°C (−4°F ~ +140°F)
Storage Temp. Range	-30°C ~ +70°C (-22°F ~ +158°F)
Backlight LED	Yellow/Green

LCD Absolute Maximum Ratings (Temperature at 25°C)

Symbols

 V_{DD}

 V_{LC}

 $V_{\rm I}$

 V_{o}

Recommended Operating Conditions (Temperature at 25°C)

ltems	Symbols	Minimum	Typical	Maximum
Supply Voltage for Logics	V_{DD}	4.5V	5.0V	5.5V
Supply Voltage LCD	V_{LC}	7.1V	7.3V	7.5V
Input Voltage	Vi	0V		V _{DD}
Driving Frequency	f _{FLM}		150Hz	_

LED Absolute Maximum Ratings (Temperature at 25°C)

ltems	Symbols	Ratings
Forward Current	١ _F	20mA
*Power Dissipation	P _d	130mW

Ratings			
-0.3V to +7.0V	Color	Yellow,	/Green
-0.3V to +12.0V	Color	Yellow	Green
-0.3V to V_{DD} +0.3V	Unicolor	60mW	60mW
–0.3V to V_{DD} +0.3V	LED Overall	130mW	

*For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.



Input Voltage

Output Voltage

<u>SmartSwitch™</u>

LCD & LED SPECIFICATIONS

DC Characteristics of LCD Drive IC (Temperature at -20° C to $+60^{\circ}$ C and $V_{DD} = 5.0V \pm 10\%$)

Items	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V _{IH}		$0.7V_{\text{DD}}$		V _{DD}	V
Low Level Input Voltage	VIL		0		0.3V _{DD}	V
High Level Input Leakage Current	I _{UH}	$V_{I} = V_{DD}$			10	μA
Low Level Input Leakage Current	ILIL	$V_1 = 0V$			10	μA
High Level Output Voltage	V _{OH}	I _{он} = -500µА	V _{DD} -0.5			V
Low Level Output Voltage	V _{OL}	I _{он} = 500µА			0.5	V
High Level Output Leakage Current	I _{LOH}	$V_{O} = V_{DD}$			10	μA
Low Level Output Leakage Current	ILOL	$V_{\odot} = 0V$			10	μA
Supply Current	I _{DD}	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	ILC	$f_{LP} = 2.4 \text{kHz}$ $V_{LC} = 7.3 \text{V}$		500	2,000	μA

Timing Characteristics of LCD Drive IC

Latch Setup Time

Latch Hold Time

FLM Setup Time

FLM Hold Time

Latch High Level Width

SCP, LP Rise/Fall Time

(Temperature at –20°C to +60°C and V_{DD} = 5.0V ±10%) Symbols Minimum Maximum Items **Clock Operation Frequency** $\mathsf{f}_{\mathsf{SCP}}$ 6.0MHz Latch Pulse Frequency f_{LP} 50kHz **Clock High Level Pulse Width** 70ns $\mathbf{t}_{\mathrm{CWH}}$ **Clock Low Level Pulse Width** 70ns $\mathbf{t}_{\mathrm{CWL}}$ **Data Setup Time** t_{DSD} 45ns Data Hold Time 50ns $\mathbf{t}_{\mathsf{DHD}}$ **Data Output Delay Time** 25ns t_{PDO}

 \mathbf{t}_{DSL}

t_{DHL}

 t_{LWH}

 $\mathbf{t}_{\mathsf{DSF}}$

t_{DHF}

 t_r/t_f

50ns

50ns

200ns

50ns

50ns

15ns

Timing Diagram



*1 Last data on first line

*2 Beginning data on second line

*3 Location of LP signal on first line

Display Electrical Characteristics

	ltems		Symbols	Test Condition	Minimum	Typical	Maximum
	c l v h	Logic Circuit	V _{DD}		4.5	5.0	5.5
	Supply Voltage	LCD Circuit	V _{LC}		7.1	7.3	7.5
H	V _{IH}		0.7V _{DD}	_	V _{DD}		
	Input Voltage	L	VIL		0	_	0.3 V _{DD}
LCD		Н	V _{OH}	D _{OUT,} I _{OH} = 500 μA	V _{DD} -0.5	_	-
	Output Voltage	L	V _{OL}	D _{OUT} , I _{OL} = 500 μA	_	_	0.5
		Logic Circuit	I _{DD}	$f_{scp} = 1.0MHz$	_	_	500
	Power	LCD Circuit	I _{LC}	$f_{LP} = 2.4 \text{kHz}$ $V_{LC} = 7.3 \text{V}$	_	500	2,000
	Items		Symbols	Test Condition	Yellow/Green		•
	rl.c					Red	
	Forward Current		I _F			10mA	
LED	Forward Voltage Current Reduction Rate		N	I _F = Forward Current Ta = 25°C		Red	
			V _F			2.0V	
			∆I _F (DC)	Ta = 25°C above	-0.33mA/°C		



<u>SmartSwitch</u>™

BLOCK DIAGRAM & PIN CONFIGURATIONS FOR RGB LEDS





ISO1BBFEF Yellow/Green LED Backlight Black and White LCD

<u>Pin No.</u>	<u>Symbol</u>	Name	Function
\bigcirc	VLC	Power	Power source for LCD drive
2	BL-LED ()	Terminal of Backlight LED	Cathode: green
3	GND	Ground	
4	V_{DD}	Power	Power source for logic circuit
5	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
6	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
7	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
8	BL-LED ()	Terminal of Backlight LED	Cathode: yellow
9	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
10	BL-LED (+)	Terminal of Backlight LED	Anode for common
11	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SMARTDISPLAY. As a result, many SMARTDISPLAYS can be controlled with one clock and data signal.
(12)	NC	None	No connection



SUPER BRIGHT LED SPECIFICATIONS

Typical Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I _F	15/15	mA
Forward Voltage	V _F	2.2/3.3	V

ABSOLUTE MAXIMUM FOR LED

Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I _F	20	mA
Reverse Voltage	V _R	4.0	V
Current Reduction Rate Above 25°C	ΔI _F (DC)	-0.33	mA/°C
*Power Dissipation	P _D	40	mW

* For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.

TYPICAL DISPLAY DIMENSIONS







Terminal numbers are not on the device.



Pixel Detail



Footprint



PRECAUTIONS FOR HANDLING & STORAGE OF LCD 36 x 24 DEVICES

Handling

- 1. The IS Series devices are electrostatic sensitive.
- 2. The IS series devices are not process sealed.
- If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
- 4. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.
- 5. Recommended soldering time and temperature limits:

Do not exceed 70°C at the LCD level. Wave Soldering: see Profile B in the Supplement section. Manual Soldering: see Profile B in the Supplement section.

- 6. Recommendation for backlight color uniformity: Use constant current driver. For current limiting resistor method, the power source should be at least twice the backlight LED forward voltage.
- 7. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic may become damaged.
- 8. Backlight Forward Current should not exceed the derated Absolute Maximum Forward Current based on the temperature.
- 9. Excessive images may result after the same image is emitted continuously for an extended period of time.

Storage

- 1. Store in original container and away from direct sunlight.
- 2. Keep away from static electricity.
- 3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.



