MJE344

Plastic NPN Silicon Medium-Power Transistor

This device is useful for medium voltage applications requiring high f_T such as converters and extended range amplifiers.

Features

• Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	200	Vdc
Collector-Base Voltage	V _{CB}	200	Vdc
Emitter Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	Ic	500	mAdc
Base Current	I _B	250	mAdc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	20 0.16	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	θЈС	6.25	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



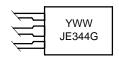
ON Semiconductor®

http://onsemi.com

0.5 AMPERE POWER TRANSISTORS NPN SILICON 150-200 VOLTS, 20 WATTS



MARKING DIAGRAM



Y = Year

WW = Work Week

JE344 = Device Code

G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE344	TO-225	500 Units/Box
MJE344G	TO-225 (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (I _C = 1.0 mAdc, I _B = 0)	V _{CEO(sus)}	200	-	Vdc	
Collector Cutoff Current (V _{CE} = 200 Vdc, I _B = 0)	I _{CEO}	-	1.0	mAdc	
Collector Cutoff Current (V _{CB} = 200 Vdc, I _E = 0)	I _{CBO}	-	0.1	mAdc	
Emitter Cutoff Current (V _{EB} = 5.0 Vdc, I _C = 0)	I _{EBO}	_	0.1	mAdc	
ON CHARACTERISTICS					
DC Current Gain (I _C = 50 mAdc, V _{CE} = 10 Vdc)	h _{FE}	30	300	-	
Collector–Emitter Saturation Voltage (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{CE(sat)}	-	1.0	Vdc	
Base–Emitter On Voltage (I _C = 50 mAdc, V _{CE} = 10 Vdc)	V _{BE(on)}	_	1.0	Vdc	
DYNAMIC CHARACTERISTICS					
Current-Gain - Bandwidth Product (I _C = 50 mAdc, V _{CE} = 25 Vdc, f = 10 MHz)	f _T	15	-	MHz	
Output Capacitance (V _{CB} = 20 Vdc, I _E = 0, f = 100 kHz)	C _{ob}	_	15	pF	
Small-Signal Current Gain (I _C = 50 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	25	-	_	

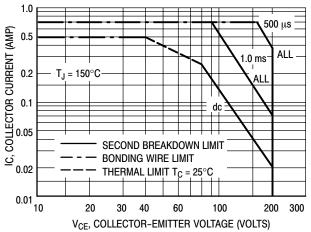
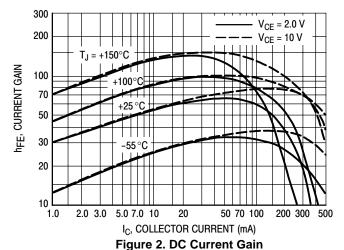
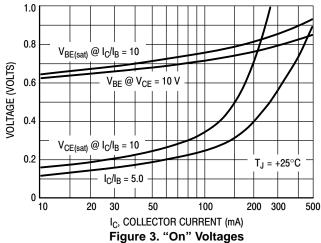


Figure 1. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation then the curves indicate.

The data of Figure 1 is based on $T_{J(pk)} = 150$ °C; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150$ °C. At high case temperatures, thermal limitations will reduce the power that can be handled to values less then the limitations imposed by second breakdown.



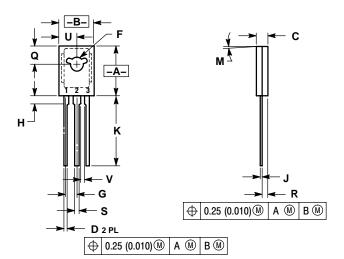


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MJE344

PACKAGE DIMENSIONS

TO-225 CASE 77-09 **ISSUE Z**



- DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: INCH.
 3. 077-01 THRU -08 OBSOLETE, NEW STANDARD

	INC	HES	IES MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094	0.094 BSC		BSC	
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5° TYP		5° TYP		
Q	0.148	0.158	3.76	4.01	
R	0.045	0.065	1.15	1.65	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
v	0.040		1 02		

STYLE 1:

PIN 1. EMITTER

COLLECTOR

3 BASE

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