

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at www.onsemi.com

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, emplo



March 2015

FDPF770N15A

N-Channel PowerTrench[®] MOSFET 150 V, 10 A, 77 m Ω

Features

- $R_{DS(on)}$ = 60 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 10 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(on)}$
- · High Power and Current Handling Capability
- · RoHS Compliant

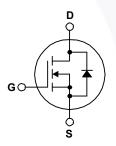
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- · Consumer Appliances
- LED TV
- · Synchronous Rectification for ATX / Sever / Telecom PSU
- · Uninterruptible Power Supply
- · Micro Solar Inverter





Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol		Parameter	FDPF770N15A	Unit
V_{DSS}	Drain to Source Voltage		150	V
V	Cata to Source Voltage	- DC	±20	V
V_{GSS}	Gate to Source Voltage	- AC (f > 1 Hz	±30	V
	Drain Current	- Continuous (T _C = 25°C,Silicon Limited)	10	Α
ID	Dialii Cuitetti	- Continuous (T _C = 100°C,Silicon Limited)	7	A
I _{DM}	Drain Current	- Pulsed (Note 1) 40	Α
E _{AS}	Single Pulsed Avalanche Energy	(Note 2	35	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3	6.0	V/ns
D	Power Dissipation	$(T_C = 25^{\circ}C)$	21	W
P_{D}	Power Dissipation	- Derate Above 25°C	0.17	W/°C
T _J , T _{STG}	Operating and Storage Temperatu	ure Range	-55 to +150	°С
T _L	Maximum Lead Temperature for S	Soldering, 1/8" from Case for 5 Seconds	300	°С

Thermal Characteristics

Symbol	Parameter	FDPF770N15A	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	5.9	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	*C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDPF770N15A	FDPF770N15A	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	-	-	V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	-	0.1	-	V/°C
	Zero Gate Voltage Drain Current	V _{DS} = 120 V, V _{GS} = 0 V	-	-	1	μА
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 120 \text{ V}, T_{C} = 125^{\circ}\text{C}$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 10 A	-	60	77	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 10 A	-	15	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 75 V V 0 V	\ -	575	765	pF
C _{oss}	Output Capacitance	$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz	-	64	85	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1011 12	-	3.9	-	pF
C _{oss(er)}	Energy Related Output Capacitance	V _{DS} = 75 V, V _{GS} = 0 V		113	-	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	8.6	11.2	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DS} = 75 \text{ V}, I_{D} = 10 \text{ A},$ $V_{GS} = 10 \text{ V}$		3.2	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau	V _{GS} = 10 V	-	1.2	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	(Note 4)	-	1.9	-	nC
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	-	0.5	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		- /	12	34	ns
t _r	Turn-On Rise Time	$V_{DD} = 75 \text{ V}, I_{D} = 10 \text{ A},$	-/	8	26	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_{G} = 4.7 Ω	-	15	40	ns
t _f	Turn-Off Fall Time	(Note 4)	-	3	16	ns

Drain-Source Diode Characteristics

I_S	Maximum Continuous Drain to Source Diode Forward Current		-	10	Α
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	40	Α
V_{SD}	Drain to Source Diode Forward Voltage V _{GS} = 0 V, I _{SD} = 10 A	-	- ,,,/	1.25	V
t _{rr}	Reverse Recovery Time $V_{GS} = 0 \text{ V}, I_{SD} = 10 \text{ A}, V_{DD} = 75 \text{ V},$	-	59	- ,	ns
Q _{rr}	Reverse Recovery Charge $dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	124	-	nC

Notes

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. Starting T $_{J}$ = 25°C, $\,$ L = 3 mH, I_{SD} = 4.8 A.
- 3. I $_{SD} \leq$ 10 A, di/dt \leq 200 A/ $\mu s,~V_{DD} \leq$ BV $_{DSS},$ starting T $_{J}$ = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

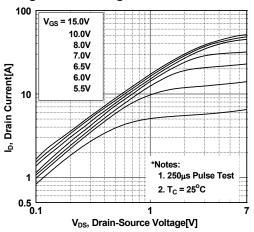


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

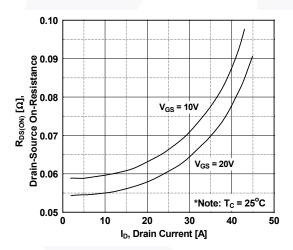


Figure 5. Capacitance Characteristics

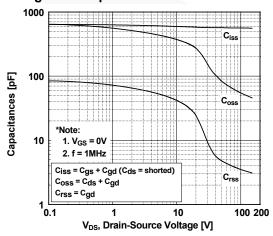


Figure 2. Transfer Characteristics

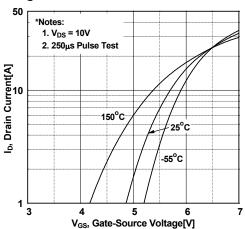


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

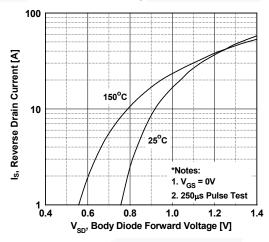
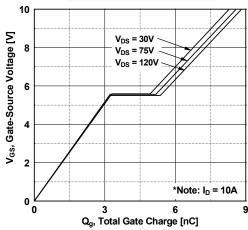


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

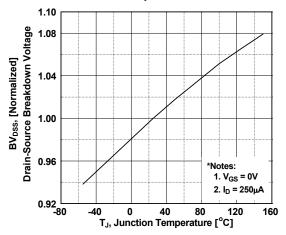


Figure 8. On-Resistance Variation vs. Temperature

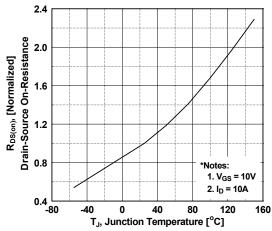


Figure 9. Maximum Safe Operating Area

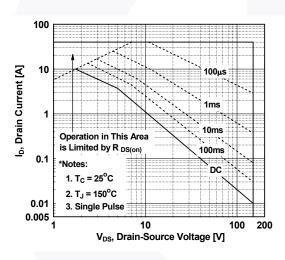


Figure 10. Maximum Drain Current vs. Case Temperature

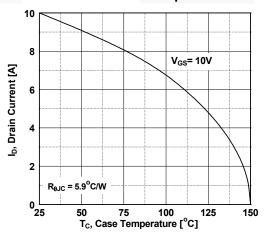
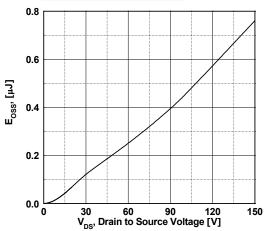
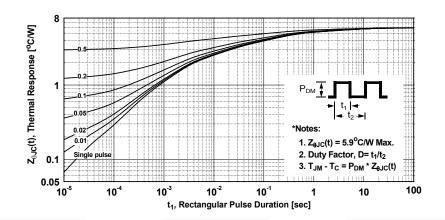


Figure 11. Eoss vs. Drain to Source Voltage



Typical Performance Characteristics (Continued)





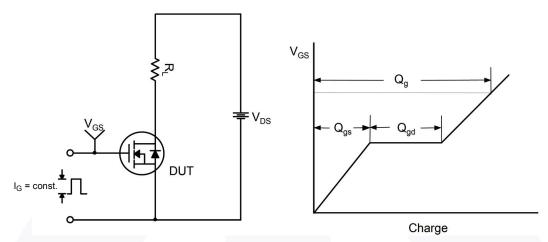


Figure 13. Gate Charge Test Circuit & Waveform

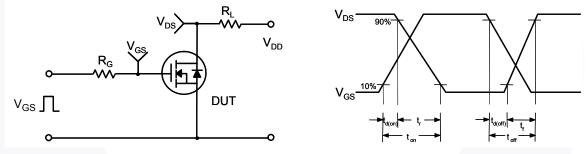


Figure 14. Resistive Switching Test Circuit & Waveforms

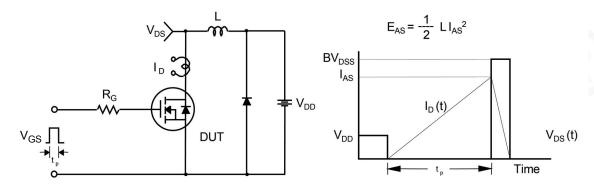


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

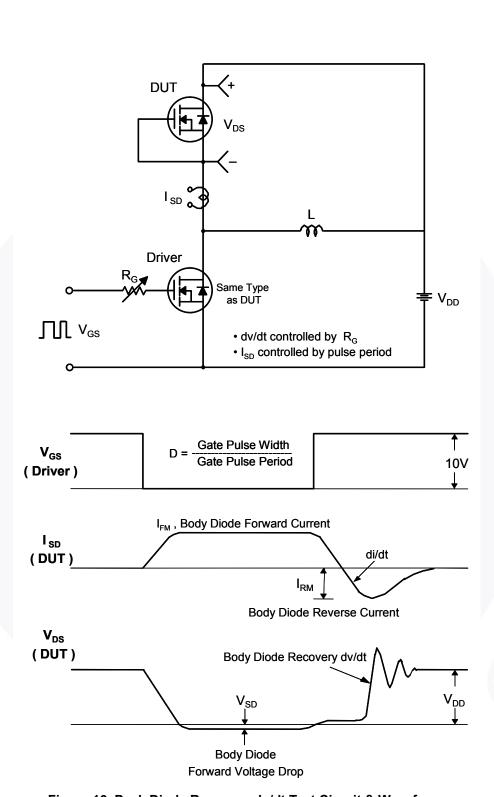
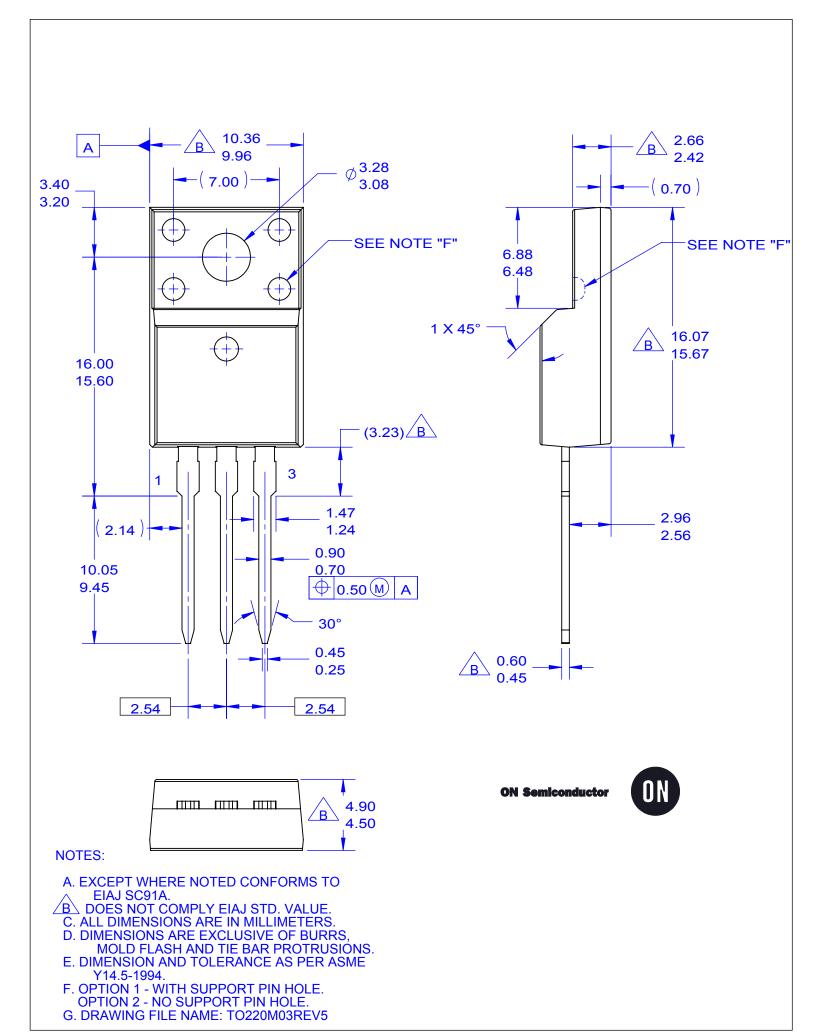


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms



ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor and see any inability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and ex

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative