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FFH30US30DN

30A, 300V Stealth™ Diode

General Description

The FFH30US30DN is a StealthTM diode optimized for low loss performance in output rectification. The StealthTM family exhibits low reverse recovery current ($I_{RM(REC)}$), low V_F and soft recovery under typical operating conditions.

This device is intended for use as an output rectification diode in Telecom power supplies and other power switching applications. Lower V_F and $I_{RM(REC)}$ reduces diode losses.

Formerly developmental type TA49449.

Features

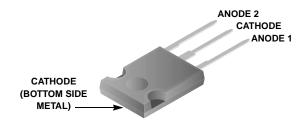
•	Soft Recovery $t_b/t_a > 0.45$
•	Fast Recovery
•	High Operating Temperature 175 ^{o}C
•	Reverse Voltage300V
•	Avalanche Energy Rating

Applications

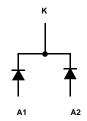
- Switch Mode Power Supplies
- Power Factor Correction
- Uninterruptable Power Supplies
- Motor Drives
- Welders

Package

JEDEC STYLE 3 LEAD TO-247



Symbol



Device Maximum Ratings (per leg) T_C = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{RRM}	Repetitive Peak Reverse Voltage	300	V
V_{RWM}	Working Peak Reverse Voltage	300	V
V _R	DC Blocking Voltage	300	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 160°C) Total Device Current (Both Legs)	30 60	A A
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	70	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	325	Α
P _D	Power Dissipation	230	W
E _{AVL}	Avalanche Energy (1A, 40mH)	20	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C
TL	Maximum Temperature for Soldering		
T_{PKG}	Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Application Note AN-7528	300 260	°C

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Ir Device Marking Device		Package	Tape Width			Quantity		
30US30DN		FFH30US30DN	TO-247	N/A			30	
Electric	al Char	acteristics (per le	eg) T _C = 25°C un	less otherwise noted				
Symbol		Parameter	Test	Conditions	Min	Тур	Max	Units
Off State	Characte	eristics						
I _R	Instantane	ous Reverse Current	V _R = 300V	$V_{R} = 300V$ $T_{C} = 25^{\circ}C$		-	100	μA
				T _C = 125°C	-	-	1	mA
On State	Characte	arietice	•			•		
V _F	Instantaneous Forward Voltage		I _F = 30A	T _C = 25°C	_	0.93	1.0	V
			.,, 00/1	$T_{\rm C} = 125^{\circ}{\rm C}$	-	0.8	0.87	V
CJ	Junction C	apacitance	V _R = 10V, I _F = 0	V _R = 10V, I _F = 0A		410	-	pF
CJ	Junction C	apacitance	$V_R = 10V, I_F = 0$	$V_R = 10V, I_F = 0A$		410	-	pF
Switchin	g Charac	teristics						
t _{rr}	Reverse Recovery Time			$I_F = 1A$, $dI_F/dt = 100A/\mu s$, $V_R = 15V$		29	50	ns
			$I_F = 30A$, dI_F/dt	$I_F = 30A$, $dI_F/dt = 100A/\mu s$, $V_R = 15V$		32	55	ns
t _{rr}	Reverse Recovery Time		$I_{F} = 30A,$	•		46	-	ns
I _{RM(REC)}	Maximum Reverse Recovery Current			$dI_F/dt = 200A/\mu s$,		5.3	-	Α
Q_{RR}	Reverse Recovered Charge		$V_R = 195V, I_C$	V _R = 195V, T _C = 25°C		140	-	nC
t _{rr}	Reverse Recovery Time I _F = 30A,			-	77	-	ns	
S	S Softness Factor (t _b /t _a)			$dI_F/dt = 200A/\mu s,$ $V_R = 195V,$ $-T_C = 125^{\circ}C$		0.45	-	-
I _{RM(REC)}			11			9	-	Α
Q _{RR}			1 _C = 125 °C			400	-	nC
t _{rr}	Reverse R	ecovery Time	I _F = 30A,	I _F = 30A,		54	-	ns
S	Softness F	actor (t _b /t _a)	$dI_F/dt = 1000A/\mu s$,		-	0.49	-	-
I _{RM(REC)}			11	1 1		32	-	Α
Q _{RR}			-	930	-	nC		
	Characte	eristics						
		eristics esistance Junction to Case	TO-247		-	-	0.65	°C/W

Typical Performance Curves (per leg)

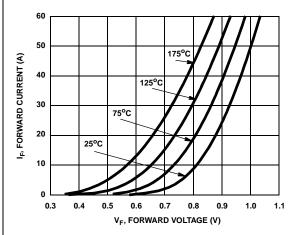
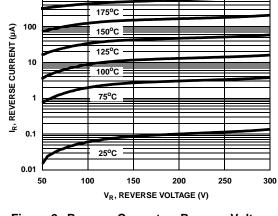


Figure 1. Forward Current vs Forward Voltage



1000

Figure 2. Reverse Current vs Reverse Voltage

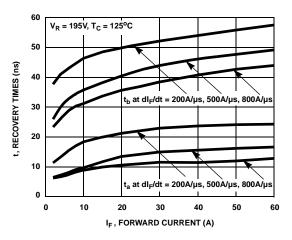


Figure 3. t_a and t_b Curves vs Forward Current

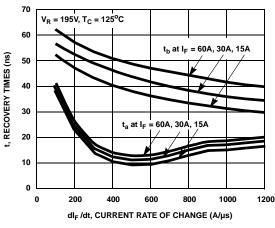


Figure 4. t_a and t_b Curves vs dl_F/dt

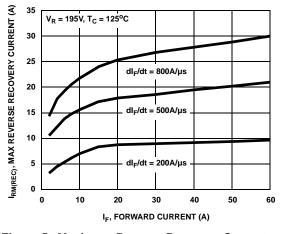


Figure 5. Maximum Reverse Recovery Current vs Forward Current

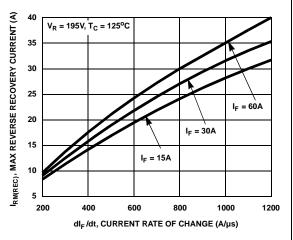


Figure 6. Maximum Reverse Recovery Current vs dI_F/dt

Typical Performance Curves (per leg) (Continued)

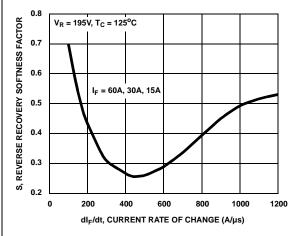


Figure 7. Reverse Recovery Softness Factor vs $\mathrm{dI_F/dt}$

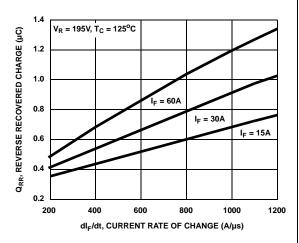


Figure 8. Reverse Recovery Charge vs dl_F/dt

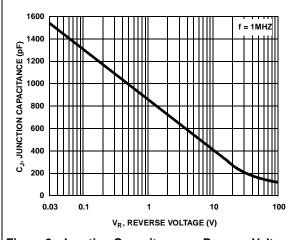


Figure 9. Junction Capacitance vs Reverse Voltage

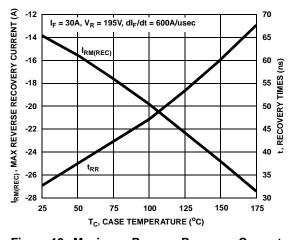


Figure 10. Maximum Reverse Recovery Current and t_{rr} vs Case Temperature

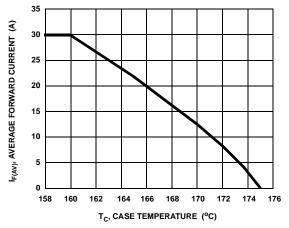


Figure 11. DC CURRENT DERATING CURVE

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Typical Performance Curves (per leg) (Continued)

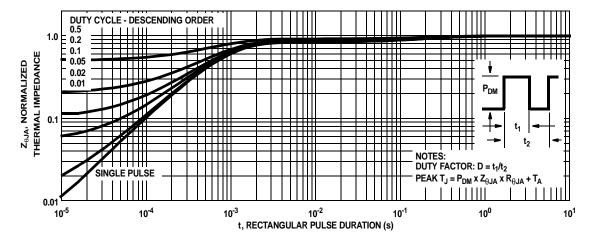
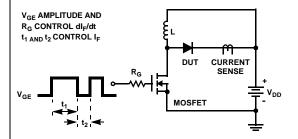


Figure 12. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



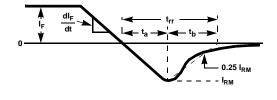
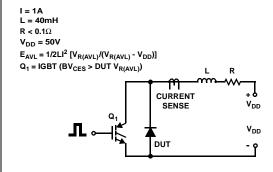


Figure 13. t_{rr} Test Circuit

Figure 14. t_{rr} Waveforms and Definitions



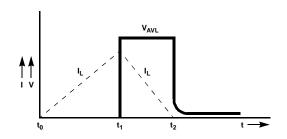


Figure 15. Avalanche Energy Test Circuit

Figure 16. Avalanche Current and Voltage Waveforms

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	CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
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