International Rectifier

40CPQ080 40CPQ100

SCHOTTKY RECTIFIER

40 Amp

$$I_{F(AV)} = 40 Amp$$

 $V_R = 80 - 100 V$

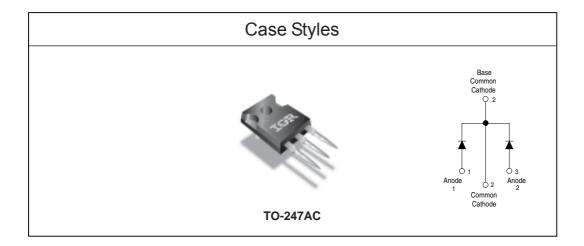
Major Ratings and Characteristics

Characteristics	Values	Units
I _{F(AV)} Rectangular waveform	40	А
V _{RRM}	80-100	V
I _{FSM} @tp=5μssine	2950	Α
V _F @20 Apk, T _J =125°C (per leg)	0.61	V
T _J	- 55 to 175	°C

Description/ Features

The 40CPQ... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175° C junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- 175° C T_J operation
- Center tap TO-247 package
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



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Bulletin PD-2.309 rev. C 10/06



Voltage Ratings

Part number	40CPQ080	40CPQ100	
V _R Max. DC Reverse Voltage (V)	00	400	
V _{RWM} Max. Working Peak Reverse Voltage (V)	80	100	

Absolute Maximum Ratings

	Parameters	40CPQ	Units	Conditions	
I _{F(AV)}	Max. Average Forward Current	40	Α	50% duty cycle @ T _C = 145°	C, rectangular wave form
. (,	* See Fig. 5				
I _{FSM}	Max. Peak One Cycle Non-Repetitive	2950	Α	5μs Sine or 3μs Rect. pulse	Following any rated load condition and with
	Surge Current (Per Leg) *See Fig. 7	300		10ms Sine or 6ms Rect. pulse	rated V _{RRM} applied
E _{AS}	Non-Repetitive Avalanche Energy	11.25	mJ	J T _J = 25 °C, I _{AS} = 2 Amps, L = 5.6 mH	
	(Per Leg)				
I _{AR}	Repetitive Avalanche Current (Per Leg)	0.75	А	Current decaying linearly to zero in 1 μ sec Frequency limited by T_J max. $V_A = 1.5 \text{ x } V_R$ typical	

Electrical Specifications

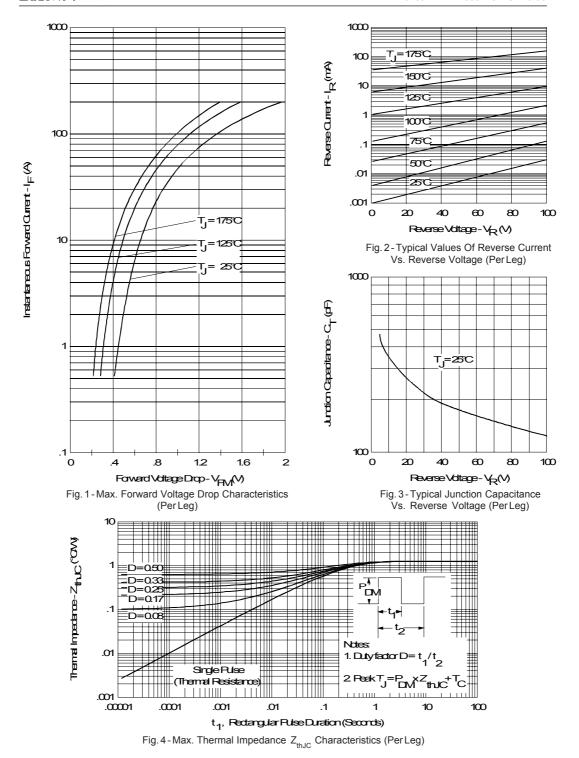
	Parameters	40CPQ	Units	C	Conditions
V _{FM}	Max. Forward Voltage Drop	0.77	V	@ 20A	T ₁ = 25 °C
	(Per Leg) * See Fig. 1 (1)	0.91	V	@ 40A	1 _J = 25 0
		0.61	V	@ 20A	T 405 °O
		0.75	V	@ 40A	T _J = 125 °C
I _{RM}	Max. Reverse Leakage Current	1.25	mA	T _J = 25 °C	V = rated V
	(Per Leg) * See Fig. 2 (1)	15	mA	T _J = 125 °C	V _R = rated V _R
C _T	Max. Junction Capacitance (Per Leg)	600	pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C	
L _s	Typical Series Inductance (Per Leg)	7.5	nH	Measured lead to lead 5mm from package body	
dv/dt	Max. Voltage Rate of Change	10000	V/ µs		
	(Rated V _R)				

(1) Pulse Width < 300µs, Duty Cycle <2%

Thermal-Mechanical Specifications

	Parameters		40CPQ	Units	Conditions
T	Max. Junction Temperature R	ange	-55 to 175	°C	
T _{stg}	Max. Storage Temperature Ra	ange	-55 to 175	°C	
R _{thJC}	Max. Thermal Resistance Jun to Case (Per Leg)	ction	1.25	°C/W	DC operation *See Fig. 4
R _{thJC}	Max. Thermal Resistance Jun to Case (Per Package)	ction	0.63	°C/W	DC operation
R _{thCS}	Typical Thermal Resistance, Case to Heatsink		0.24	°C/W	Mounting surface, smooth and greased
wt	Approximate Weight		6 (0.21)	g (oz.)	
Т	Mounting Torque	Min.	6 (5)	Kg-cm	Non-lubricated threads
		Max.	12 (10)	(lbf-in)	
	Case Style		TO-247AC(TO-3P)	JEDEC
	Device Marking		40CPQ080		
			40CPQ100		

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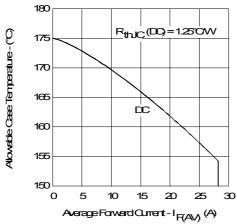


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

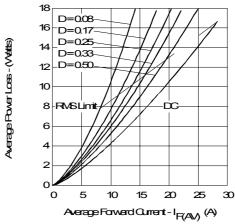


Fig. 6-Forward Power Loss Characteristics (PerLeg)

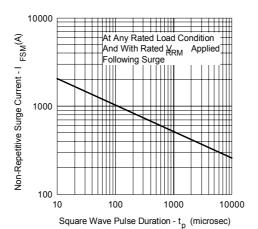


Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

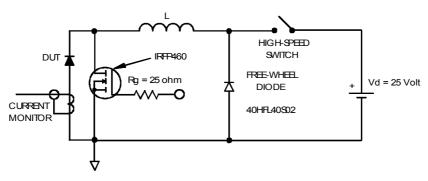
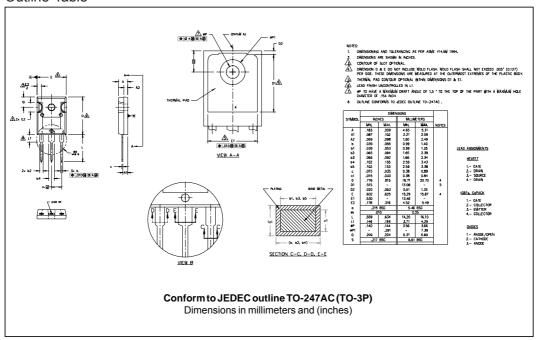
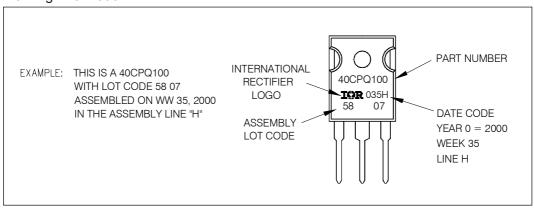


Fig. 8 - Unclamped Inductive Test Circuit

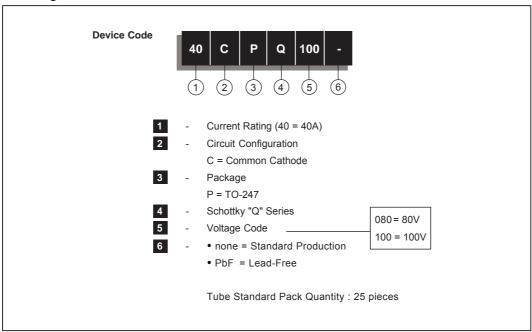
Outline Table



Marking Information



Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level.

Qualification Standards can be found on IR's Web site.



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105 TAC Fax: (310) 252-7309

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