# **AN5277**

## **Dual Channel SEPP Power Amplifier**

### Overview

The AN5277 is a monolithic integrated circuit designed for 10.0 W (26 V, 8 W)output audio power amplifier. It is a dual channel SEPP IC suitable for stereo operation in TV application.

### ■ Features

- Few external components:
  - No Boucherot cells(output C, R)
  - No Bootstrap Capacitors
  - No Negative Feedback Capacitors
- Built-in muting circuit
- Built-in standby circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Compatible with AN5275, AN5276
- Operating voltage range 10 ~ 32 V(26 V typ.)

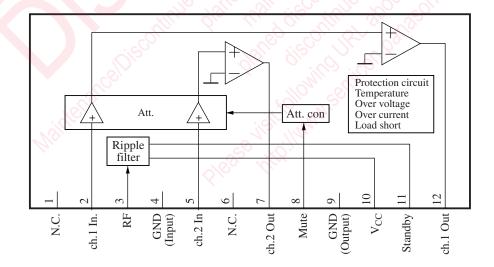
#### Applications

• TV

## ■ Package

• HSIP012-P-0000E

### ■ Block Diagram



## ■ Pin Descriptions

Pin No.	Description	Pin No.	Description
1	N.C.	7	ch.2 Output
2	ch.1 Input	8	Mute
3	Ripple Filter	9	Output GND
4	Input GND	10	V <sub>CC</sub>
5	ch.2 Input	11	Standby
6	N.C.	12	ch.1 Output

## ■ Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	35.0	V
Supply current	$I_{CC}$	4.0	A
Power dissipation *2	$P_{\mathrm{D}}$	37.5	W
Operating ambient temperature *1	T <sub>opr</sub>	-25 to +75	°C
Storage temperature *1	T <sub>stg</sub>	-55 to +150	°C

Note) \*1:  $T_a = 25$  °C except operating ambient temperature and storage temperature.

## ■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V <sub>CC</sub>	10.0 to 32.0	v

## ■ Electrical Caracteristics at $V_{CC}$ = 26 V, $R_L$ = 8 Ω, f = 1 kHz, $T_a$ = 25 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent current	$I_{CQ}$	$V_{IN} = 0 \text{ mV}$	_	40	80	mA
Output end noise voltage *1	V <sub>NO</sub>	No input, $R_G = 10 \text{ k}$	_	0.22	04	mV
Voltage gain	$G_{V}$	V <sub>IN</sub> = 57 mV	32	34	36	dB
Total harmonic distortion *1	THD	$V_{IN} = 57 \text{ mV}$	_	0.2	0.4	%
Maximum output power	Po	V <sub>CC</sub> = 26 V,THD = 10 %	8.0	10.0	_	W
Ripple rejection ratio *1	RR	$V_R = 1 \text{ Vrms}_{,f} f_R = 120 \text{ Hz}, R_G = 10 \text{ k}\Omega,$	45	55	_	dB
Channel balance	СВ	$V_{IN} = 57 \text{ mV}$	-1.0	0	1.0	dB
Muting ratio	MR	$V_{IN} = 57 \text{ mV}$	70	80	_	dB
Muting control voltage	V <sub>MUTE</sub>	V <sub>IN</sub> = 57 mV, MR≥70 dB	3.0	_	_	V
Standby control voltage 'on'	V <sub>STDON</sub>	No input, I <sub>CC</sub> ≤0.1 mA	_	_	5.0	V
Standby control voltage 'off'	V <sub>STDOFF</sub>	No input, I <sub>CC</sub> ≥20 mA	8.5	_	_	V
Channel crosstalk	CT	$V_{IN} = 57 \text{ mV}, R_G = 10 \text{ k}\Omega$	50	60	_	dB

Note) \*1: For this measurement, use the 20 Hz to 20 kHz(12 dB/OCT) filter.

<sup>\*2:</sup> At  $T_a = 70$  °C.

## ■ Terminal Equivalent Circuit

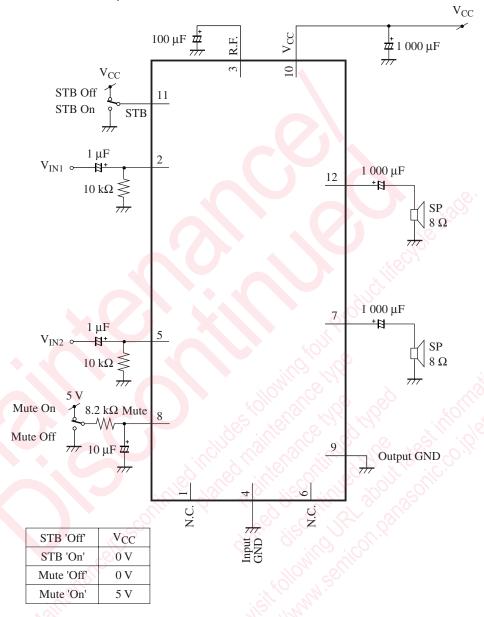
Pin No.	Equivalent Circuit	Description	DC Voltage
1	<del>_</del>	Not connected	_
2	Pin2 200 Ω 400 Ω 30 kΩ	ch.1 Input: This is the amplifier input pins.	0 V
3	30 kΩ 15 kΩ 15.7 kΩ	Ripple Filter: This is the pin to connect the positive terminal of a ripple filter capacitor.	V <sub>CC</sub> – 1.5 V <sub>BE</sub>
4	4	Input GND :	0 V
5	Refer to Pin2	Input ground pin. ch.2 Input: This is the amplifier input pins.	_
6		Not connected	_
7	Pre-amp. $7$ $600 \Omega$ $30 k\Omega$ $9$ $V_{CC}/2$	ch.2 Output : ch.2 output pin	V <sub>CC</sub> /2

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## ■ Terminal Equivalent Circuit (continued)

Pin No.	Equivalent Circuit	Description	DC Voltage
8	3 kΩ 3 kΩ	Mute: Mute input pin. Mute 'On' = 5 V Mute 'Off' = 0 V	_
	$\begin{array}{c c} 10 \text{ k}\Omega \\ \hline \\ \hline \\ 200 \Omega \\ \hline \end{array}$	illechicle st	%°.
9		Output GND: ch.1 & ch.2 output ground.	0 V
10		V <sub>CC</sub> : This is the power supply pin.	typ.: 26 V
11	10 11 15 kΩ 15 kΩ 2 2 3 kΩ	Standby: This is the standby control pin.	oillaigh.
12	Pre-amp. Driver Cct 12 600 Ω 30 kΩ 9	ch.1 Output : ch.1 output pin	V <sub>CC</sub> /2

## ■ Application Circuit Example



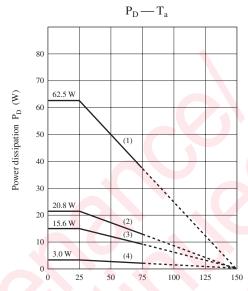
#### ■ Usage Notes

- 1. External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2. Fin of the IC can be connected to GND.
- 3. Please prevent output to  $V_{\text{CC}}$  short and output to GND short.
- 4. Load short protection will only prevent the IC from damaging if operating  $V_{CC}$ <30 V
- The temperature protection circuit will operate at Tj around 150 °C.
   However, if temperature decrease, the protection circuit will automatically be deactivated and resume normal operation.

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## ■ Technical Information

• P<sub>D</sub> — T<sub>a</sub> Curves of HSIP012-P-0000E



Ambient temperature T<sub>a</sub> (°C)

- 1.  $T_C = T_a$ ,62.5  $W(\theta_{j-c} = 2 °C/W)$ 2. 20.83  $W(\theta_f = 4.0 °C/W)$ With a 100 cm<sup>2</sup> X 3 mm Al heat sink(black colour coated)or a 200 cm<sup>2</sup> X 2 mm Al heat sink(not lacquered)
- 3. 15.63 W( $\theta_f = 6.0$  °C/W) With a 100 cm<sup>2</sup> X 2 mm Al heat sink(not lacquered)
- 4. 3.0 W at  $T_a = 25$  °C( $\theta_{j-a} = 42$  °C/W) Without heat sink

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