TOSHIBA BiCD Digital Integrated Circuit Silicon Monolithic

TB62752BFUG

Step Up Type DC/DC Converter for White LED

The TB62752BFUG is a high efficient Step-Up Type DC/DC Converter specially designed for constant current driving of White LED.

This IC can drive 2-7 white LEDs connected series using a Li-ion battery.

This IC contains N-ch MOS-FET Transistor for Coil-Switching, and LED current (IF) is set with an external resistor.

This IC is especially for driving back light white LEDs in LCD

of PDA, Cellular Phone, or Handy Terminal Equipment. This device is Pb-free product.



Weight: 0.016 g (typ.)

Features

- 2-7 white LEDs connected series.
- Variable LED current IF is set with a external resistor: 20 mA (typ.) @RSENS = 15 Ω
- Output power: Available for 800 mW LED loading (7LEDs, $I_F = Over 25 \text{ mA}$)
- High efficiency: 80% over (using recommended external parts)
- Output over voltage shutdown function : Switching operation is shut downed when OVD terminal voltage is over 37 V (typ.).
- IC package: SSOP6-P-0.95B
- Switching frequency: 1.0 MHz (typ.)

Pin Assignment (top view)



Note: This IC could be destroyed in some case if amounted in 180° inverse direction. Please be careful about IC direction in mounting.

Block Diagram



Pin Function

Pin No.	Symbol	Function Description
1	SHDN	Voltage-input terminal for IC-enable/disable LED-I _F . A high input on this pin enables the IC to operate while a low input causes it to shut down. The behavior of the IC is unpredictable if the input on the pin is undefined. Ensure that the pin is tied to either a high or low level.
2	OVD	Over voltage detection terminal. IC switching operation is disabled with detection over voltage. If the voltage returns to detection level or less, operation is enabled again.
3	VIN	Supply voltage input terminal. (2.8 V to 5.5 V)
4	SW	Switch terminal for DC/DC converter. Nch MOSFET built-In.
5	GND	Ground terminal.
6	FB	LED I _F setting resistor connecting terminal.

I/O Equivalent Pin Circuits

1. SHDN Terminal



2. OVD Terminal



3. VIN Terminal to GND Terminal



4. SW Terminal



5. FB Terminal



Absolute Maximum Ratings (Ta = 25°C if without notice)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	VIN	-0.3 to + 6.0	V	
Input voltage	VIN (SHDN)	-0.3 to +VIN + 0.3 (Note 1)	V	
Switching terminal voltage	V _O (SW)	-0.3 to + 40	V	
Switching terminal current	I _O (SW)	1500	mA	
Power dissipation	PD	0.41 (device)	W	
	FD	0.47 (on PCB) (Note 2)	vv	
Thermal resistance		300 (device)	°C/W	
	R _{th (j-a)}	260 (on PCB)	0/00	
Operation temperature range	T _{opr}	-40 to + 85	°C	
Storage temperature range	T _{stg}	-55 to + 150	°C	
Maximum junction temperature	Тј	150	°C	

Note 1: Ensure that the supply voltage never exceeds 6.0 V.

Note 2: Power dissipation must be calculated with subtraction of 3.8 mW/°C from maximum rating with every 1°C if T_{opr} is upper 25°C. (on PCB)

Recommended Operating Condition (Ta = -40 to 85°C if without notice)

Characteristics	Symbol	Tes Circuit	Test Condition	Min	Тур.	Max	Unit
Power supply voltage	VIN	_		2.8	—	5.5	V
LED current	lF	_	VIN = 3.6 V, RSENS = 15 Ω 6 white LEDs, Ta = 25°C		20		mA

Electrical Characteristics (Ta = 25°C, VIN = 2.8 to 5.5 V if without notice)

Characteristics	Symbol	Tes Circuit	Test Condition	Min	Тур.	Max	Unit
Power supply voltage	VIN	_		2.8		5.5	V
Operating consumption current	I _{IN (ON)}	1	$VIN=3.6~V,~RSENS=15~\Omega$	_	0.6	0.9	mA
Quiescent consumption current	I _{IN (OFF)}	2	$VIN = 3.6 V, V_{\overline{SHDN}} = 0 V$	_	0.5	1.0	μΑ
SHDN terminal "H" level input voltage	VSHDNH	3	—	1.3		VIN	V
SHDN terminal "L" level input voltage	VSHDNL	3	_	0		0.4	V
SHDN terminal current	I_SHDN	4	VIN = 3.6 V, V _{SHDN} = 3.6 V or 0 V		0	1.0	μΑ
Integrated MOS-T _r switching frequency	fosc	5	$VIN = 3.6 \text{ V}, V_{\overline{SHDN}} = 3.6 \text{ V}$	0.77	1.0	1.43	MHz
Switching terminal leak current	I _{oz} (SW)	6	—	_	0.5	1	μΑ
FB terminal feedback voltage	V _{FB}	7	VIN = 3.6 V, RSENS = 15 Ω Ta = 25°C, L = 22 μH	285	300	315	mV
FB terminal reedback voltage		7	$\label{eq:VIN} \begin{array}{l} VIN=4.2 \ V, \ RSENS=150 \ \Omega \\ Ta=25^\circC, \ L=22 \ \muH \end{array}$	285	300	315	mV
FB terminal line regulation	ΔV_{FB}	7	VIN = 3.6 V (typ.) VIN = 3.0 to 5.0 V	-5	_	5	%
FB terminal current	I _{FB}	8		_	0.02	_	μΑ
OVD terminal detect voltage	V _{OVD}	9	—	29	31.5	34	V
OVD terminal leakage current	I _{OVD}	10	$V_{OVD} = 30 V$	_	0.5	1	μΑ

Usage Precautions

Protection in LED Opened Condition

The operation with OVD terminal is available for the protection in case LED circuit opened.

When the voltage of OVD terminal is over 37 V (typ.), Nch MOS switching operation is disabled in the IC. When the voltage of OVD terminal drops below 37 V (typ.), Nch MOS switching operation becomes available again.

If load of LED is detached, Nch MOS switching operation is disabled with detection of boost circuit voltage and the IC is protected from unexpected over voltage.

Setting of External Capacitor

Terminal for brightness control, recommended values are C_1 = Over 2.2 (µF), C_2 = Over 1.0 (µF)

The recommended values of the capacitors depend on the control of brightness. For details, please see the item "Control of brightness" from the next page.

These recommended values reduce fluctuation of input current to up accuracy of brightness.

Setting of IF

Resistance connects between RSENS pin and GND. The average current is set by this RSENS value and average current are obtained by the following equation.

$$I_{\rm F}$$
 (mA) = $\frac{300 \, [\rm mV]}{\rm RSENS \, [\Omega]}$

Current value error is within $\pm 5\%$.

Setting of External Inductor Size

Please select the inductor size with referring this table corresponding to each number of LEDs.

[Recommended inductor values]

LEDs	Indictor Size	Note	
2 to 5	10 μH	LED current I _F = 20 mA	
Over 6	22 µH		

Current Dimming Control

Recommended brightness control circuits are 4 types.

(1) Input PWM signal to \overline{SHDN} terminal IF can be adjusted with PWM signal by inputting it to \overline{SHDN} terminal.

<<PWM signal frequency>>

• The recommended PWM signal frequency is from 100 Hz to 10 kHz. There is a possibility to arise the audible frequency in mounting to the board because it is within the auditory area.

<<Constant number of external condenser>>

- To reduce the fluctuation of input current and increase the accuracy of brightness, the values that $C_1 = 4.7 \ (\mu F)$ or more, $C_2 = 0.1 \ (\mu F)$ or less are recommended.
- When the PWM signal is off, the time to drain C₂ of charge depends on the constant number. And so, the actual value is little different from the theoretical value.

<<PWM input signal>>

• Set the amplitude of PWM signal within the range of \overline{SHDN} terminal specification.

<<Rush current in inputting>>

• In case dimming by inputting the PWM signal to the SHDN terminal, this IC turns on and off repeatedly.

And the rush current, which provides the charge to C_2 , arises in turning on. Take care in selecting the condenser.

<<Current value in control with PWM: Ideal equation>>

$$I_{F} [mA] = \frac{300 [mV] \times ON Duty [\%]}{RSENS [\Omega]}$$

<<Recommended application>>



(2) Input analog voltage to FB terminal

 I_{F} can be adjusted with analog voltage input to FB terminal.

This method is without repeating IC ON/OFF, and no need to consider holding rash current.

[Notice]

• LED current value goes over 100% of the current set with RSENS, if the input analog voltage is between 0 V to 300 mV (typ.).

<<Recommended application>>



(3) Input PWM signal with filtering to FB terminal

IF can be adjusted with filtering PWM signal using RC filter indicated in recommended circuit, because the PWM signal can be regard as analog voltage after filtering.

This method is without repeating IC ON/OFF, and no need to consider holding rash current.

[Notice]

• LED current value goes over 100% of the current set with RSENS, if the input voltage after filtering is between 0 V to 300 mV (typ.).

<<Recommended application>>



(4) Input logic signal

 $\rm I_F$ can be adjusted with logic signal input as indicated in recommended circuit. The resistor connected the ON-State Nch MOS drain and RSENS determines IF.

Average of setting current I_O (mA) is next, approximately.

 $I_F \text{ [mA]} = \frac{300 \text{ [mV]}}{\text{Sum of resistor value [}\Omega\text{]}}$

<<Recommended application>>



M1	M2	LED Current
OFF	OFF	<u>300 [mV]</u> RSENS [Ω]
ON	OFF	$300 \text{ [mV]} \times \frac{\text{RSENS } [\Omega] + \text{R1} [\Omega]}{\text{RSENS } [\Omega] \times \text{R1} [\Omega]}$
OFF	ON	$300 \text{ [mV]} \times \frac{\text{RSENS } [\Omega] + \text{R2 } [\Omega]}{\text{RSENS } [\Omega] \times \text{R2 } [\Omega]}$
ON	ON	$300 \text{ [mV]} \times \frac{\text{R}_{\text{SENS}} [\Omega] \times \text{R1} [\Omega] + \text{R}_{\text{SENS}} [\Omega] \times \text{R2} [\Omega] + \text{R1} [\Omega] \times \text{R2} [\Omega]}{\text{R}_{\text{SENS}} [\Omega] \times \text{R1} [\Omega] \times \text{R2} [\Omega]}$

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TEST Circuit

1. I_{IN (ON)}



3. $V_{\overline{SHDNH}}$, $V_{\overline{SHDNL}}$



5. f_{OSC}



2. I_{IN (OFF)}



4. I_{SHDN}



6. I_{OZ} (SW)



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VIN = 3.6 V

2.2 μF

7. V_{FB} , ΔV_{FB}^{*1}







9. V_{OVD}^{*1}

10. I_{OVD}



*1: The locations of the pins differ from the actual ones to simplify the diagram. See page 1 for the actual pin locations.

Package Dimensions

SSOP6-P-0.95B







Weight: 0.016 g (typ.)

Unit: mm

About solderability, following conditions were confirmed

Solderability

- (1) Use of Sn-63Pb solder Bath
 - solder bath temperature = 230°C
 - dipping time = 5 seconds
 - \cdot the number of times = once
 - use of R-type flux
- (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - solder bath temperature = 245°C
 - · dipping time = 5 seconds
 - $\cdot \$ the number of times = once
 - · use of R-type flux

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