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# TO-5 Hermetic Photocell NSL-5532

# **Precision – Control – Results**



## DESCRIPTION

The NSL-5532 is a CdS photoconductive cell on a TO-5 ceramic substrate. The photocell is encapsulated with epoxy for moisture resistance.

#### RELIABILITY

Contact Luna for recommendations on specific test conditions and procedures.

#### **FEATURES**

- Passive resistance output
- Ceramic package

# **APPLICATIONS**

• Industrial

#### **ABSOLUTE MAXIMUM RATINGS**

SYMBOL	MIN		MAX	UNITS	(TA)= 23°C UNLESS OTHERWISE NOTED
Voltage (peak AC or DC)	-	-	320	V	-
Power Dissipation @ 25°C <sup>1</sup>	-	-	125	mW	-
Operation Temperature	-60	to	+75	°C	-
Storage Temperature	-60	to	+75	°C	
Soldering Temperature <sup>2</sup>	-	-	+260	°C	-
NOTE:		1			

1. Derate linearly to 0 at 75°C

**2.** >0.08" from case for <5 sec.

3. Cells light adapted at 30 to 50 Ftc for 16 hrs minimum prior to electrical tests

Information in this technical datasheet is believed to be correct and reliable. However, no responsibility is assumed for possible inaccuracies or omission. Specifications are subject to change without notice.

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Luna Optoelectronics, 1240 Avenida Acaso, Camarillo CA 93012 • Phone (805) 987-0146 • Fax (805) 484-9935

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**`OPTO-ELECTRICAL PARAMETERS** 

### $T_a = 23^{\circ}C$ UNLESS NOTED OTHERWISE

PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNITS
Light Resistance	2ftc., 2854°K <sup>3</sup>	110	165	220	KΩ
	100 ftc., 2854°K	-	10	-	
Dark Resistance	5 sec after removal of test light	11	-	-	MΩ
Spectral Peak	-	-	550	-	nm

NOTE:

Derate linearly to 0 at 75°C 1.

2.

>0.08" from case for <5 sec. Cells light adapted at 30 to 50 ftc. for 16 hrs. minimum prior to electrical tests. 3.

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