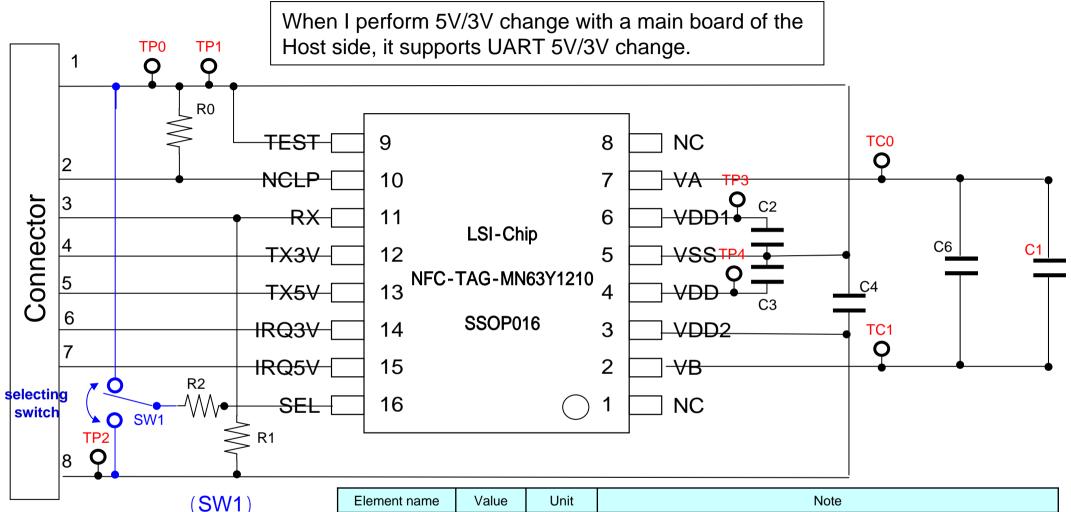
Evaluation Board Circuit Diagram

Part No.	MN63Y1210
Package Code No.	SSOP016-P-0225E

Publication date: February 2013

Connection diagram of the evaluation board Panasonic



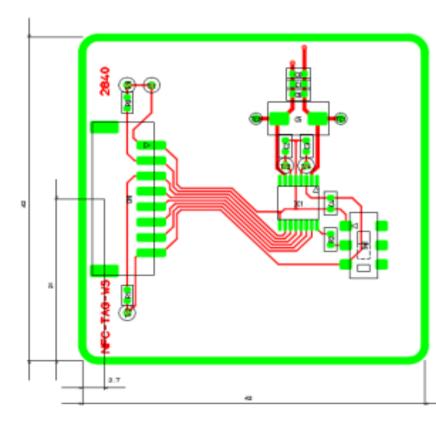
S8B-PH-SM4-TB

CL-SB-22B-01

Element name	Value	Unit	Note
C1	27	pF	Condenser implementation for resonance frequency adjustment
C2	0.1	mF	
C3	0.01	mF	
C4	0.1	mF	
C6	330	pF	Condenser implementation for resonance frequency adjustment
R0, R1	100	k	It is for reference board alone evaluations
R2	0		

Pattern drawing and part list of Evaluation board Panasonic

Pattern drawing



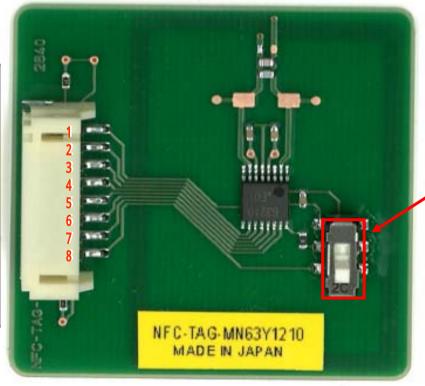
Parts list

No		Part Number	Manufacturer	Parameter	Tolerance	Rated V/ W	LxW [mm]
IC1		NFC-TAG-WS	Panasonic	-	-	4.6V	6.4×5
CN1		S8B-PH-SM4-TB	JST	-	-	100V	19.9x8.6
	TP0	Unconnected Pin	-	-	-	-	-
	TP1	Unconnected Pin	-	-	-	-	-
	TP2	Unconnected Pin	-	-	-	-	-
	TP3	Unconnected Pin	-	-	-	-	-
	TP4	Unconnected Pin	-	-	-	-	-
	R0	RK73B1JTTD104J	KOA	100k	±5%	0.1W	1.6x0.8
	R1	RK73B1JTTD104J	KOA	100k	±5%	0.1W	1.6x0.8
R2	UART	RK73Z1JTTD	KOA	0 ~ 50m	-	0.1W	1.6x0.8
	CLK synchronous	Unconnected Pin	-	-	-	-	-
R3	UART	Unconnected Pin	-	-	-	-	-
	CLK synchronous	RK73Z1JTTD	KOA	0 ~ 50m	-	0.1W	1.6x0.8
	C0	-	-	-	-	-	-
C1		-	-	-	-	-	-
-	C2	GRM188R71E104KA01D	Murata	0.1 μ F	± 10%	25V	1.6×0.8
	C3	GRM188R71H103KA01D	Murata	0.01 μ F	± 10%	25V	1.6×0.8
	C4	GRM188R71E104KA01D	Murata	0.1 μ F	± 10%	25V	1.6×0.8
	C6	GRM1885C1H331JA01D	Murata	330pF	± 5%	50V	1.6×0.8

The change with the expression, please reach with a switch in UART and the CLK same period.

After having changed it, please carry out an initialization of the smartphone side.

Pin Number	Terminal Name
1	VSS
2	NCLP
3	RX
4	TX3V
5	TX5V
6	IRQ3V
7	IRQ5V
8	VDD2



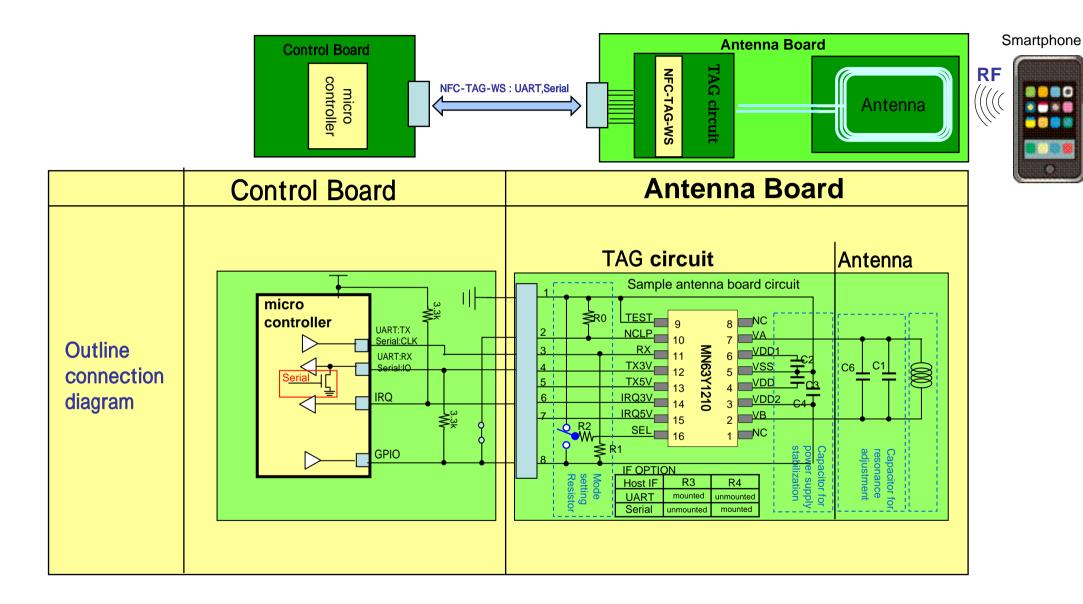
selector switch

Upper : UART

Under : CLK synchronization type

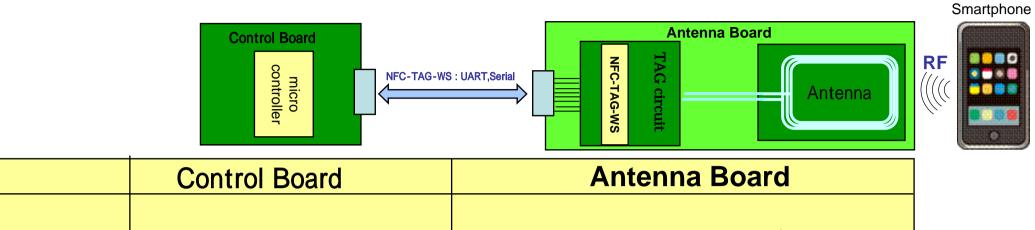
(Default)

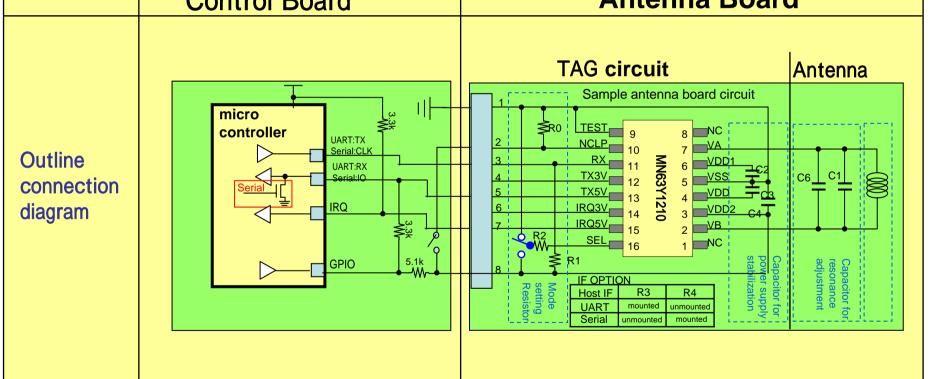
Connection example with MCU board for 3.3Vpanasonic



Connection example with MCU board for 5V

Panasonic





Inquiries

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Panasonic Corporation

URL: http://www.semicon.panasonic.co.jp/en

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