## 2SD2573

### Silicon NPN triple diffusion planar type

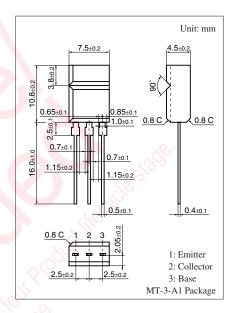
For high current amplification, power amplification

#### ■ Features

- ullet Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- Allowing supply with the radial taping

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	80	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	60	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	6	V	
Collector current	$I_{C}$	3	A	
Peak collector current	I <sub>CP</sub>	6	A	
Collector power dissipation $T_C = 25^{\circ}C$	$P_{C}$	1.5	W	
Junction temperature	$T_{j}$	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	



#### ■ Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

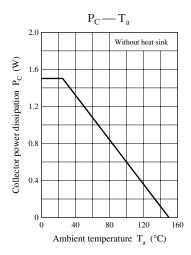
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 25 \text{ mA}, I_B = 0$	60	9	0,,	V
Collector-base cutoff current (Emitter open)	I <sub>CBO</sub>	$V_{CB} = 80 \text{ V}, I_{E} = 0$	-07/		100	μΑ
Collector-emitter cutoff current (Base open)	$I_{CEO}$	$V_{CE} = 40 \text{ V}, I_{B} = 0$	20	5	100	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_C = 0$	20/		100	μΑ
Forward current transfer ratio *	$h_{FE}$	$V_{CE} = 4 \text{ V}, I_{C} = 0.5 \text{ A}$	500		2500	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 2 A, I_B = 0.05 A$	)		1.0	V
Transition frequency	$f_T$	$V_{CE} = 12 \text{ V}, I_{C} = 0.2 \text{ A}, f = 10 \text{ MHz}$		50		MHz

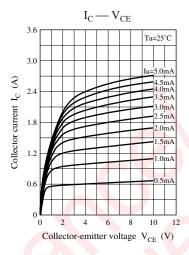
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

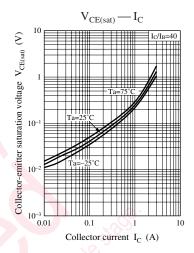
#### 2. \*: Rank classification

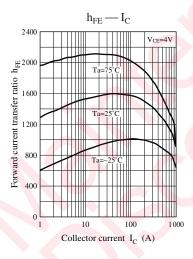
Rank	Р	Q	S RO
$h_{\mathrm{FE}}$	500 to 1 000	800 to 1 500	1 200 to 2 500

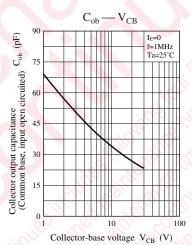
## **Panasonic**











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