EMI Filter with ESD Protection

Features:

- 4 × 4 mm Lead Less MLF Surface Mount Package
- 9 EMI/RFI Bi-directional "Pi" Low-Pass Filters
- ESD Protection Meets IEC6000–4–2
- 50 Watt Peak Pulse Power, $8 \times 20 \ \mu s$ (all diodes under power)
- Diode Capacitance: 7 10 pF
- "Pi" Filter Line Capacitance: 22 ±20% pF
- Low Zener Diode Leakage: 1 µA Maximum
- Zener Breakdown Voltage; 6 8 Volts
- Moisture Sensitivity Level 1

Benefits:

- Suppresses EMI/RFI Noise in Systems Subjected to Electromagnetic Interference
- Small Package Size Minimizes Parasitic Inductance, Thus a More "Ideal" Low Pass Filtering Response

Typical Applications:

- Cellular Phones
- Communication Systems
- Computers
- Portable Products with Input/Output Conductors

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation (Note 1) $8 \times 20 \ \mu s$ Pulse	P _{PK}	50	Watts
Maximum Junction Temperature	T,I	150	°C

1. All diodes in parallel under power



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24 PIN MLF CASE 485F PLASTIC

MARKING DIAGRAM









ORDERING INFORMATION

Device		Package	Shipping
NZMM7V0T4	Ļ	24 PIN	4000/Tape & Reel

ELECTRICAL CHARACTERISTICS

Symbol	Characteristic	Min	Тур	Мах	Unit
VZ	Zener Breakdown Voltage, @ I _{ZT} = 1 mA	6.0	-	8.0	V
I _r	Zener Leakage Current, @ V _R = 3 V	N/A	-	1.0	μΑ
V _F	Zener Forward Voltage, @ I _F = 50 mA	N/A	-	1.25	V
Capacitance	Zener Internal Capacitance, @ 0 V Bias	7.0	-	10	pF
Capacitance	Zener/Resistor Array Line Capacitance	17.6	_	26.4	pF
Resistor	Resistance	90	-	110	Ω
F _C (Note 2)	Cutoff Frequency	_	220	_	MHz

2. 50 Ω Source and 50 Ω Lead Termination per Figure 2

0

Frequency Response Specification











Applications Information

Suppressing Noise at the Source

- Filter all I/O signals leaving the noisy environment
- Locate I/O driver circuits close to the connector
- Use the longest rise/fall times possible for all digital signals

Reducing Noise at the Receiver

- Filter all I/O signals entering the unit
- Locate the I/O filters as close as possible to the connector

Minimizing Noise Coupling

- Use multilayer PCBs to minimize power and ground inductance
- Keep clock circuits away from the I/O connector
- Ground planes should be used whenever possible
- Minimize the loop area for all high speed signals
- Provide for adequate power decoupling

ESD Protection

- Locate the suppression devices as close to the I/O connector as possible
- Minimize the PCB trace length to the suppression device
- Minimize the PCB trace length for the ground return for the suppression device

OUTLINE DIMENSIONS

EMI Filter with ESD Protection



24 PIN MLF PLASTIC PACKAGE CASE 485F-01 ISSUE O



- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETERS 2. 3.
- DIE THICKNESS ALLOWABLE IS 0.305 MM MAXIMUM (0.012 INCHES MAXIMUM). DIMENSION D APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 MM FORM TERMINAL 4.
- AND IS MEASURED BE I WEEN 0.20 AND 0.23 MM FROM TERMINAL. THE PIN # I IDENTIFIER MUST BE ON THE TOP SURFACE OF THE PACKAGE BY USING IDENTIFICATION MARK OR OTHER FEATURE OF DAVIAGE ORDER 5. PACKAGE BODY. 6. EXACT SHAPE AND SIZE OF THIS FEATURE IS
- EAACI SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.
 THE SHAPE SHOWN ON FOUR CORNERS ARE NOT ACTUAL I/O.
 PACKAGE WARPAGE MAX 0.05 MM.

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.00	4.00 BSC		BSC	
В	4.00	4.00 BSC		BSC	
C		1.00		0.039	
D	0.18	0.30	0.007	0.012	
E	3.75 BSC		0.14	B BSC	
F	3.75 BSC		0.148 BSC		
G	0.50 BSC		0.020 BSC		
Н	2.00 BSC		0.79 BSC		
K	0.01	0.05	0.000	0.002	
L	0.30	0.55	0.012	0.022	
M		12 °		12 °	
N	2.00 BSC		0.079 BSC		
Р	1.88 BSC		0.074 BSC		
Q	0.50 DIA		0.020 DIA		
R	1.88 BSC		0.079 BSC		
V	2.50 BSC		0.098 BSC		
W	1.30 BSC		0.051 BSC		
Z	2.50 BSC		0.098 BSC		
AA	0.65	0.80	0.026	0.031	
AB	0.20 REF		0.008 REF		
AD	1.30 BSC		0.051 BSC		
AE	0.13	0.23	0.005	0.009	
AF	0.24	0.60	0.009	0.024	
AG	0.30	0.45	0.012	0.018	

<u>Notes</u>

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