



# N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
20	0.065 at V <sub>GS</sub> = 4.5 V	3.9		
	0.075 at V <sub>GS</sub> = 2.5 V	3.6		
	0.096 at V <sub>GS</sub> = 1.8 V	3.2		

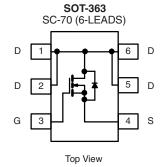
#### **FEATURES**

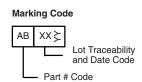
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs: 1.8 V Rated
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

- · Load Switching
- PA Switch
- · Level Switch





Ordering Information: Si1406DH-T1-E3 (Lead (Pb)-free)

Si1406DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 8			
Continuous Drain Current /T 150 °C\2	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	3.9	3.1	^	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		2.8	2.2		
Pulsed Drain Current		I <sub>DM</sub>	10		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.4	0.9		
Mariana Barra Biratiani	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.56	1.0	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		0.81	0.52	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	60	80	°C/W
	Steady State		100	125	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	34	45	

Note:

a. Surface mounted on 1" x 1" FR4 board.

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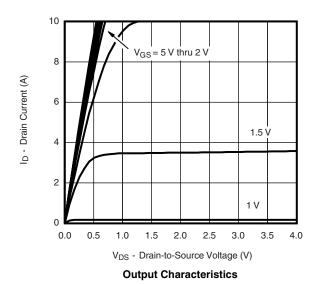
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.45		1.2	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Cata Valtaga Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			5	- μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	8			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 3.9 \text{ A}$		0.053	0.065	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 3.6 \text{ A}$		0.062	0.075		
		$V_{GS} = 1.8 \text{ V}, I_D = 2 \text{ A}$		0.079	0.096		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 3.9 \text{ A}$		11		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 1.4 A, V_{GS} = 0 V$		0.75	1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			4.9	7.5		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.9 \text{ A}$		1.0		nC	
Gate-Drain Charge	$Q_{gd}$			0.95		1	
Turn-On Delay Time	t <sub>d(on)</sub>			27	41		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 20 $\Omega$		47	71	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 0.5$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 6 $\Omega$		54	81		
Fall Time	t <sub>f</sub>			29	44		
Source-Drain Reverse Recovery	t <sub>rr</sub>	I <sub>F</sub> = 1.4 A, dI/dt = 100 A/μs		35	60		

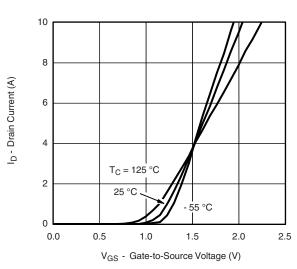
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





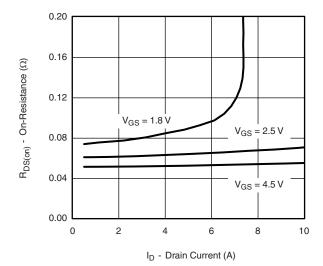
**Transfer Characteristics** 



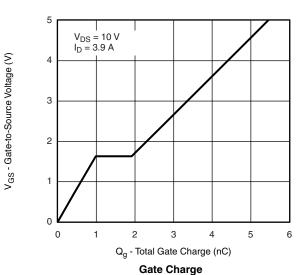




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

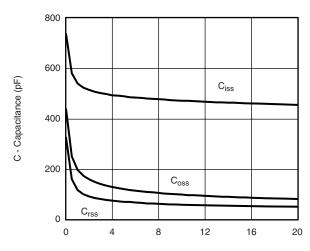


#### **On-Resistance vs. Drain Current**



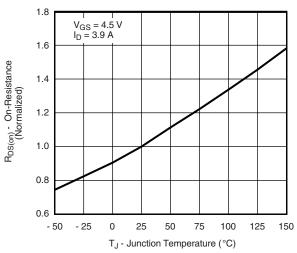
 $T_{J} = 150 \, ^{\circ}\text{C}$   $T_{J} = 25 \, ^{\circ}\text{C}$   $0.0 \quad 0.2 \quad 0.4 \quad 0.6 \quad 0.8 \quad 1.0 \quad 1.2$   $V_{SD} - \text{Source-to-Drain Voltage (V)}$ 

Source-Drain Diode Forward Voltage

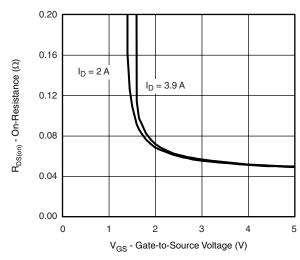


 $V_{DS}$  - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature



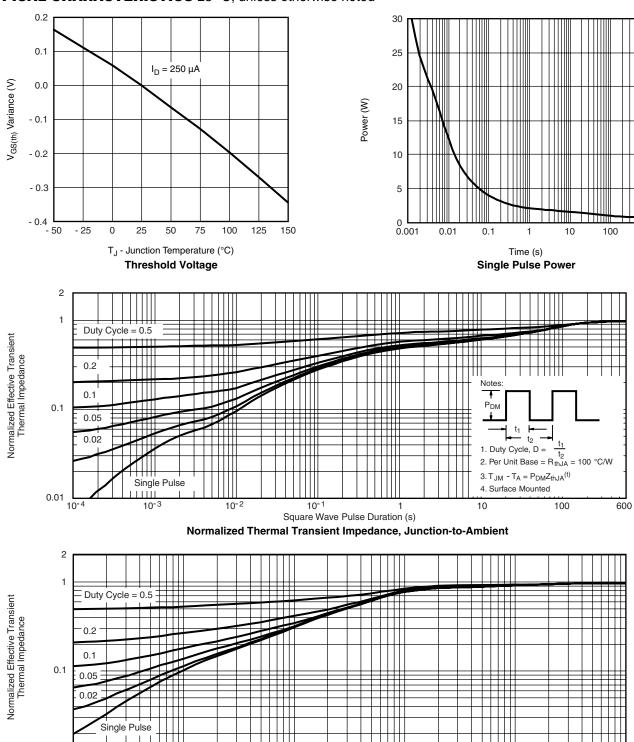
On-Resistance vs. Gate-to-Source Voltage

Is - Source Current (A)

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Foot

10-1

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