TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7S66F, TC7S66FU

Bilateral Switch

The TC7S66 is a high Speed C^2MOS Bilateral Switch fabricated with silicon gate C^2MOS technology.

It consists of a high speed switch capable of controlling either digital or analog signals while maintaining the ${\rm C^2MOS}$ low power dissipation.

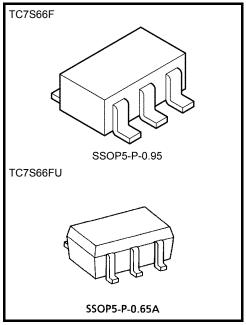
Control input (C) is provided to control the switch.

The switch turns ON while the C input is high, and the switch turns OFF while low.

Input is equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 7 \text{ ns (typ.)} @V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \mu A \text{ (max) } @Ta = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Low ON resistance: $R_{ON} = 100 \Omega$ (typ.) @ $V_{CC} = 9 V$
- Low T.H.D: THD = 0.05% (typ.) @V_{CC} = 5 V
- Pin and function compatible with TC4S66F



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

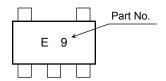
Characteristics	Symbol	Rating	Unit	
DC Supply voltage	V_{CC}	–0.5 to 13	V	
Control input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V	
Switch I/O voltage	V _{I/O}	-0.5 to V _{CC} + 0.5	V	
Control diode current	I _{CK}	±20	mA	
I/O diode current	l _{IOK}	±20	mA	
Through I/O current	Ι _Τ	±12.5	mA	
DC V _{CC} /ground current	ICC	±25	mA	
Power dissipation	P _D	200	mW	
Storage temperature range	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

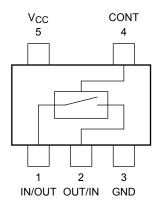
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 1991-06

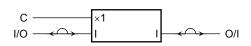
Marking



Pin Configuration (top view)



Logic Diagram



Truth Table

Control	Switch Function
Н	ON
L	OFF

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2 to 12	V
Control input voltage	V _{IN}	0 to V _{CC}	V
Switch I/O voltage	V _{I/O}	0 to V _{CC}	V
Operating temperature range	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	ns
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	
	ү, ч	0 to 400 (V _{CC} = 6.0 V)	115
		0 to 250 (V _{CC} = 10.0 V)	

Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition		Symbol Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
		V _{CC} (V)	Min	Тур.	Max	Min	Max			
				2.0	1.5	_	_	1.5	_	
	High level			4.5	3.15	_	_	3.15	_	
	riigirievei	VIHC	_	9.0	6.3	_	_	6.3	_	
Control input				12.0	8.4	_	_	8.4	_	V
voltage				2.0		_	0.5		0.5	V
	Low level	V_{ILC}		4.5		_	1.35	_	1.35	
	LOW level	VILC	_	9.0		_	2.7	_	2.7	
				12.0		_	3.6	_	3.6	
			V _{IN} = V _{IHC}	4.5		192	340		400	
		$V_{I/O} = V_{CC} \text{ to GND}$ $I_{I/O} \le 1 \text{ mA}$ R_{ON} $V_{IN} = V_{IHC}$	9.0		110	170	_	200		
			I _{I/O} ≤ 1 mA	12.0		90	160	_	180	Ω
ON resistance				2.0	_	320	_	_	_	
			4.5	_	140	200	_	260		
		$V_{I/O} = V_{CC}$ or GND $I_{I/O} \le 1$ mA	9.0	_	100	150	_	190		
				12.0	_	90	140	_	180	
Input/output lea current (switch		l _{OFF}	$V_{OS} = V_{CC}$ or GND $V_{IS} = GND$ or V_{CC} $V_{IN} = V_{ILC}$	12.0	_	_	±100	_	±1000	nA
Switch input leacurrent (switch on, outp	•	I _{IZ}	V _{OS} = V _{CC} or GND V _{IN} = V _{IHC}	12.0		_	±100	_	±1000	nA
Control input co	control input current I_{IN} $V_{IN} = V_{CC}$ or GND		12.0		_	±100	_	±1000	nA	
Quiescent device current		current I _{CC} V _{IN} = V _{CC} or GND		6.0		_	1.0	_	10.0	
			9.0		_	4.0	_	40.0	μА	
				12.0	_	_	8.0	_		80.0

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AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	, ,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_	2.0	_	20	75	_	100	- ns
Phase difference between	φI-O		4.5	_	7	15	_	20	
input and output	ΨΙ-Ο		9.0		4	12		15	
			12.0		4	11		14	
			2.0		20	150		190	
Output enable time	t _{pZL}	R _L = 1 kΩ	4.5		13	30		38	ns
Output enable time	t _{pZH}	K[= 1 K22	9.0		9	18		33	is is
			12.0		8	18		27	
	t _{pLZ} t _{pHZ}	$R_L = 1 \text{ k}\Omega$	2.0		40	170		220	ns - MHz
Output disable time			4.5		11	35	_	44	
			9.0		10	30		38	
			12.0		9	27		33	
	_	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$ $V_{OUT} = 1/2 \text{ V}_{CC}$	2.0		30			_	
Maximum control input			4.5		30			_	
frequency			9.0	_	30	_	_	_	
			12.0	_	30	_	_	_	
Control input capacitance	C _{IN}	_			5	10	_	10	pF
Switch terminal capacitance	C _{I/O}	_			6			_	pF
Feedthrough capacitance	C _{IOS}	_			0.5	_	_		pF
Power dissipation capacitance	C _{PD}		(Note)	_	15	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

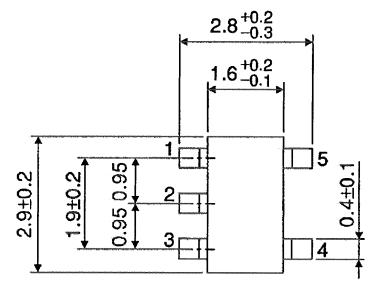
Analog Switch Characteristics (GND = 0 V, Ta = 25°C) (Note)

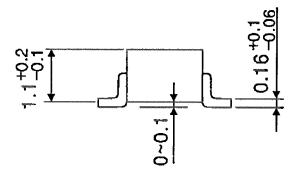
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
		$f_{IN} = 1 \text{ kHz}, V_{IN} = 4 V_{p-p} (V_{CC} = 4.5 \text{ V})$	4.5	0.05	%
Total harmonic distortion (T.H.D)		$R_L = 10 \text{ k}\Omega, \text{ V}_{IN} = 8 \text{ V}_{p\text{-}p} \text{ (V}_{CC} = 9.0 \text{ V)}$ $C_L = 50 \text{ pF}$	9.0	0.04	
Maximum propagation frequency (switch on)	fMAX	Adjust f _{IN} voltage to obtain 0dBm at V _{OS} increase f _{IN} frequency until dB meter reads	4.5	200	
		-3dB. R _L = 50 Ω, C _L = 10 pF f _{IN} = 1 MHz, Sine wave	9.0	200	MHz
Feedthrough (switch on)	_	V_{IN} is centered at $V_{CC}/2$ adjust input for 0dBm $R_L=600~\Omega,~C_L=50~pF$ $f_{IN}=1~MHz,~Sine~wave$	4.5	-60	dB
			9.0	-60	uБ
Crosstalk (control switch)	_	$\begin{aligned} R_L &= 600 \ \Omega, \ C_L = 50 \ pF \\ f_{IN} &= 1 \ MHz, \ Pulse \ (t_r = t_f = 6 \ ns) \end{aligned}$	4.5	60	mV
Orosotant (control switch)			9.0	100	1110

Note: These characteristics are determined by design of devices.

Package Dimensions

SSOP5-P-0.95 Unit: mm





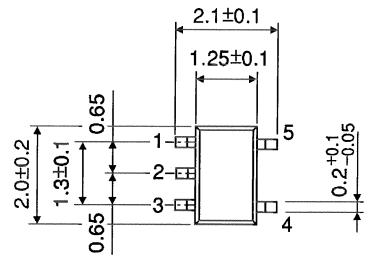
Weight: 0.016 g (typ.)

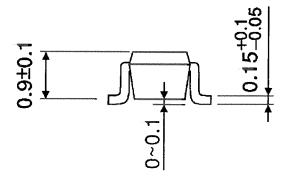
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TC7S66F/FU

Package Dimensions

SSOP5-P-0.65A Unit: mm





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Weight: 0.006 g (typ.)

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