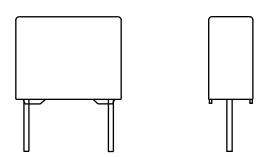




# DC Film Capacitors MKT Radial Potted Type



# FEATURES

- 10 mm lead pitch. Supplied loose in box and taped on reel or ammopack
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

# APPLICATIONS

Blocking and coupling, bypass and energy reservoir

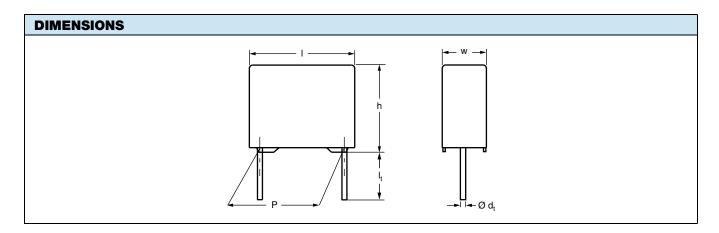
Po
Pb-free

COMPLIANT

QUICK REFERENCE DATA	
Capacitance tolerance	± 10 %, ± 5 %
Capacitance range (E12 series)	0.0047 μF to 0.68 μF
Rated DC voltage	100 V, 250 V, 400 V, 630 V
Rated AC voltage	63 V, 160 V, 220 V, 250 V
Climatic testing class (according to IEC 60068-1)	55/105/56
Rated temperature	85 °C
Maximum application temperature	105 °C
Performance grade	Grade 1 (long life)
Leads	Tinned wire
Reference standards	IEC 60384-2
Dielectric	Polyester film
Electrodes	Metallized
Construction	Mono construction
CONSTRUCTION	
Encapsulation	Flame retardant plastic case and epoxy resin (UL-class 94 V-0)
Marking C-value; tolerance; rated voltage; manufacturer's symbol; year and manufacturer; manufacturer's type	

#### Note

For more detailed data and test requirements, contact <u>dc-film@vishay.com</u>

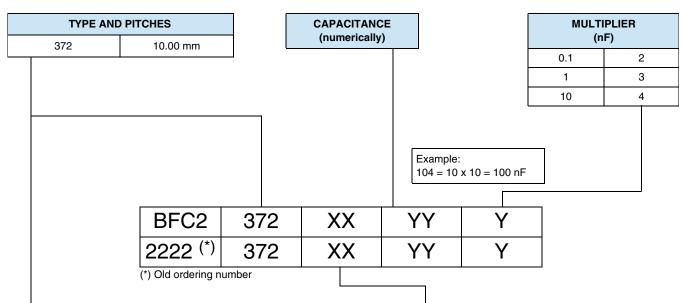


1 For technical questions, contact: <u>dc-film@vishay.com</u> Document Number: 28192

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## **COMPOSITION OF CATALOG NUMBER**



ТҮРЕ	PACKAGING	LEAD CONFIGURATION	PREFERRED TYPES					
TIFE	FACKAGING		C-TOL.	100 V	250 V	400 V	630 V	
	Loose in box	Lead length 4.0 mm + 1.0 mm/- 0.5 mm	± 10 %	21	41	51	61	
			± 5 %	22	42	52	62	
372	Taped on reel <sup>(1)</sup>	$H^{(1)} = 18.5 \text{ mm}$ $P_0 = 12.7 \text{ mm}$ Reel diameter = 356 mm	± 10 %	25	45	55	65	
372			±5%	26	46	56	66	
	Ammopack <sup>(1)</sup>	H <sup>(1)</sup> = 18.5 mm	± 10 %	28	48	58	68	
	Ammopack (*)	P <sub>0</sub> = 12.7 mm	±5%	29	49	59	69	

### Note

<sup>(1)</sup> For detailed tape specifications refer to packaging information: <u>www.vishay.com/doc?28139</u>

SPECIFIC REFERENCE DATA							
DESCRIPTION	VALUE						
Tangent of loss angle:	at 1 kHz		at 10 kHz			at 100 kHz	
C ≤ 0.1 µF	≤ 75 x 10 <sup>-4</sup>		≤ <b>1</b> 30	x 10 <sup>-4</sup>		$\le 250 \text{ x } 10^{-4}$	
$0.1 \ \mu F < C \le 0.68 \ \mu F$	≤ 75 x 10 <sup>-4</sup>		≤ 130	x 10 <sup>-4</sup>		$\le 250 \text{ x } 10^{-4}$	
Deted voltage pulse clane (dl.l/dt) et	100 V <sub>DC</sub>		250 V <sub>DC</sub>	400 V <sub>DC</sub>		630 V <sub>DC</sub>	
Rated voltage pulse slope (dU/dt) <sub>R</sub> at	34 V/µs 5		50 V/µs	us 80 V/µs		120 V/µs	
R between leads, for C $\leq$ 0.33 $\mu F$							
at 10 V; 1 min	> 15 000 MΩ						
at 100 V; 1 min	>		5 000 MΩ	> <b>30 000 M</b> Ω		$>$ 30 000 M $\Omega$	
RC between leads, for C > 0.33 $\mu$ F at 100 V; 1 min			> 50	)00 s			
R between interconnecting leads and case (foil method)	> 30 000 MΩ						
Withstanding (DC) voltage (cut off current 10 mA) $^{(1)};$ rise time $\leq$ 1000 V/s	160 V; 1 min	40	0 V; 1 min	640 V; 1 m	nin	1008 V; 1 min	
Withstanding (DC) voltage between leads and case	200 V; 1 min	50	0 V; 1 min	800 V; 1 m	nin	1260 V; 1 min	
Maximum application temperature 105 °C							

#### Note

<sup>(1)</sup> See "Voltage Proof Test for Metallized Film Capacitors": <u>www.vishay.com/doc?28169</u>

# **MKT372**



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ELEC	ELECTRICAL DATA									
							FC2 372 XXY			1
				LOOSE IN BOX		REEL (1)(2)			PACK <sup>(2)</sup>	4
	CAP.	DIMENSIONS w x h x l	MASS		0 mm /- 0.5 mm		3.5 mm; 2.7 mm		.5 mm; 2.7 mm	C-VALUE
(V)	(μF)	(mm)	(g) <sup>(3)</sup>	C-TOL. = ± 10 %	C-TOL. = ± 5 %	C-TOL. = ± 10 %	C-TOL. = ± 5 %	C-TOL. = ± 10 %	C-TOL. = ± 5 %	
				XX (SPQ)	XX (SPQ)	XX (SPQ)	XX (SPQ)	XX (SPQ)	XX (SPQ)	YYY
							0.60 mm ± 0		(,	
100	0.10 0.12 0.15 0.18 0.22 0.27 0.33	4.0 x 10.0 x 12.5	0.65	<b>21</b> (1000)	<b>22</b> (1000)	<b>25</b> (1400)	<b>26</b> (1400)	<b>28</b> (750)	<b>29</b> (750)	104 124 154 184 224 274 334
	0.39	5.0 x 11.0 x 12.5	0.87	<b>21</b>	<b>22</b>	<b>25</b>	<b>26</b>	<b>28</b>	<b>29</b>	394
	0.47			(1000)	(1000)	(1100)	(1100)	(600)	(600)	474 564
	0.56	6.0 x 12.0 x 12.5	1.15	<b>21</b> (750)	<b>22</b> (750)	<b>25</b> (900)	<b>26</b> (900)	<b>28</b> (500)	<b>29</b> (500)	504 684
	0.00	L	$U_{BAC} = 1$				= 0.60 mm ± 0		(000)	
250	0.047 0.056 0.068 0.082 0.10	4.0 x 10.0 x 12.5	0.65	<b>41</b> (1000)	<b>42</b> (1000)	<b>45</b> (1400)	<b>46</b> (1400)	<b>48</b> (750)	<b>49</b> (750)	473 563 683 823 104
	0.12 0.15	5.0 x 11.0 x 12.5	0.87	<b>41</b> (1000)	<b>42</b> (1000)	<b>45</b> (1100)	<b>46</b> (1100)	<b>48</b> (600)	<b>49</b> (600)	124 154
	0.18	6.0 x 12.0 x 12.5	1.15	<b>41</b> (750)	<b>42</b> (750)	<b>45</b> (900)	<b>46</b> (900)	<b>48</b> (500)	<b>49</b> (500)	184 224
	0.22		$U_{\rm BAC} = 2$	( )	· · · /	· · ·	= 0.60 mm ± 0	( )	(000)	227
400	0.0047 0.0056 0.0068 0.0082 0.010 0.012 0.015 0.018 0.022 0.027 0.033	4.0 x 10.0 x 12.5	0.65	<b>51</b> (1000)	<b>52</b> (1000)	<b>55</b> (1400)	<b>56</b> (1400)	<b>58</b> (750)	<b>59</b> (750)	472 562 682 103 123 153 183 223 273 333
	0.039 0.047 0.056	5.0 x 11.0 x 12.5	0.87	<b>51</b> (1000)	<b>52</b> (1000)	<b>55</b> (1100)	<b>56</b> (1100)	<b>58</b> (600)	<b>59</b> (600)	393 473 563
	0.068 0.082	6.0 x 12.0 x 12.5	1.15	<b>51</b> (750)	<b>52</b> (750)	<b>55</b> (900)	<b>56</b> (900)	<b>58</b> (500)	<b>59</b> (500)	683 823
	0.010	[	U <sub>RAC</sub> = 2	250 V; PITCH	= 10.0 mm ±	. 0.4 mm; d <sub>t</sub> =	= 0.60 mm ± 0	).06 mm		400
630	0.010 0.012 0.015 0.018 0.022	4.0 x 10.0 x 12.5	0.65	<b>61</b> (1000)	<b>62</b> (1000)	<b>65</b> (1400)	<b>66</b> (1400)	<b>68</b> (750)	<b>69</b> (750)	103 123 153 183 223
	0.027 0.033	5.0 x 11.0 x 12.5	0.87	<b>61</b> (1000)	<b>62</b> (1000)	<b>65</b> (1100)	<b>66</b> (1100)	<b>68</b> (600)	<b>69</b> (600)	273 333
	0.039 0.047	6.0 x 12.0 x 12.5	1.15	<b>61</b> (750)	<b>62</b> (750)	<b>65</b> (900)	<b>66</b> (900)	<b>68</b> (500)	<b>69</b> (500)	393 473

### Notes

• SPQ = Standard Packing Quantity

<sup>(1)</sup> Reel diameter = 356 mm is available on request

<sup>(2)</sup>  $H = in-tape height; P_0 = sprocket hole distance; for detailed specifications refer to packaging information: <u>www.vishay.com/doc?28139</u>$ 

<sup>(3)</sup> Weight for short lead product only



# MOUNTING

### Normal Use

The capacitors are designed for mounting on printed-circuit boards. The capacitors packed in bandoliers are designed for mounting in printed-circuit boards by means of automatic insertion machines.

For detailed tape specifications refer to packaging information: <u>www.vishay.com/doc?28139</u>

### Specific Method of Mounting to Withstand Vibration and Shock

In order to withstand vibration and shock tests, it must be ensured that stand-off pips are in good contact with the printed-circuit board:

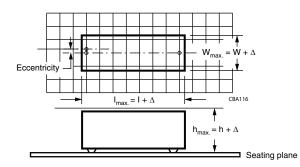
- For pitches  $\leq$  15 mm capacitors shall be mechanically fixed by the leads
- For larger pitches the capacitors shall be mounted in the same way and the body clamped

### Space Requirements On Printed-Circuit Board

The maximum space for length ( $I_{max}$ ), width ( $w_{max}$ ) and height ( $h_{max}$ ) of film capacitors to take in account on the printed-circuit board is shown in the drawing:

- For products with pitch  $\leq$  15 mm,  $\Delta w$  =  $\Delta I$  = 0.3 mm and  $\Delta h$  = 0.1 mm
- For products with 15 mm < pitch  $\leq$  27.5 mm,  $\Delta w$  =  $\Delta I$  = 0.5 mm and  $\Delta h$  = 0.1 mm

Eccentricity defined as in drawing. The maximum eccentricity is smaller than or equal to the lead diameter of the product concerned.



## SOLDERING

For general soldering conditions and wave soldering profile, we refer to the application note: **"Soldering Guidelines for Film Capacitors"**: <u>www.vishay.com/doc?28171</u>

## Storage Temperature

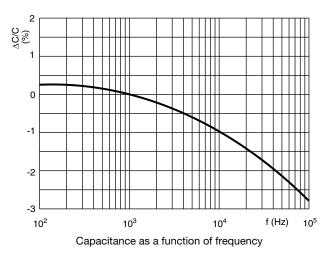
 $T_{sta} = -25$  °C to +35 °C with RH maximum 75 % without condensation

## **Ratings and Characteristics Reference Conditions**

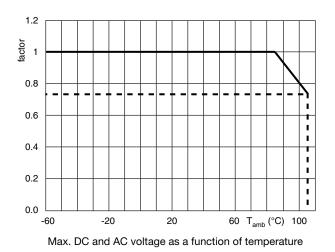
Unless otherwise specified, all electrical values apply to an ambient free air temperature of 23 °C  $\pm$  1 °C, an atmospheric pressure of 86 kPa to 106 kPa and a relative humidity of 50 %  $\pm$  2 %.

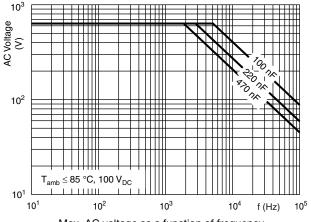
For reference testing, a conditioning period shall be applied over 96 h  $\pm$  4 h by heating the products in a circulating air oven at the rated temperature and a relative humidity not exceeding 20 %.

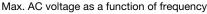
**CHARACTERISTICS** 

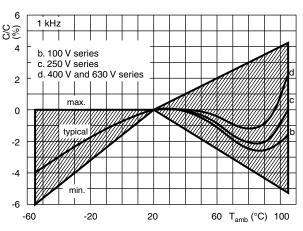


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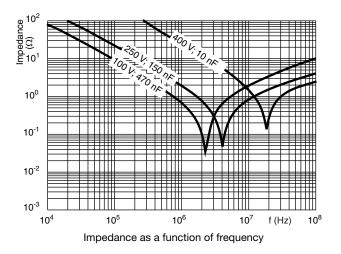


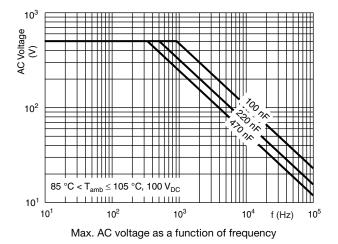






Capacitance as a function of ambient temperature

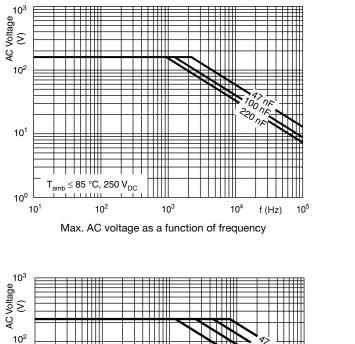




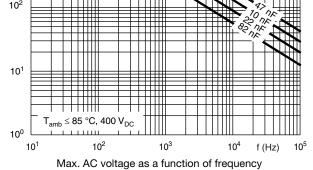
Revision: 29-Jun-16

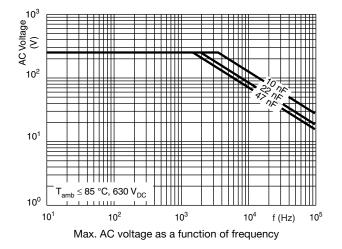
5

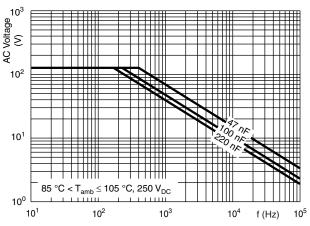
Document Number: 28192



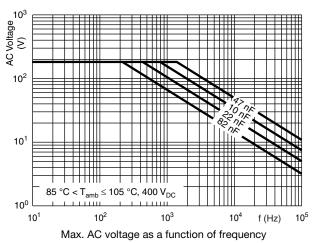
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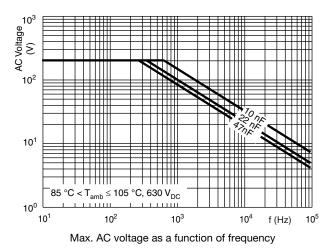






Max. AC voltage as a function of frequency





Revision: 29-Jun-16

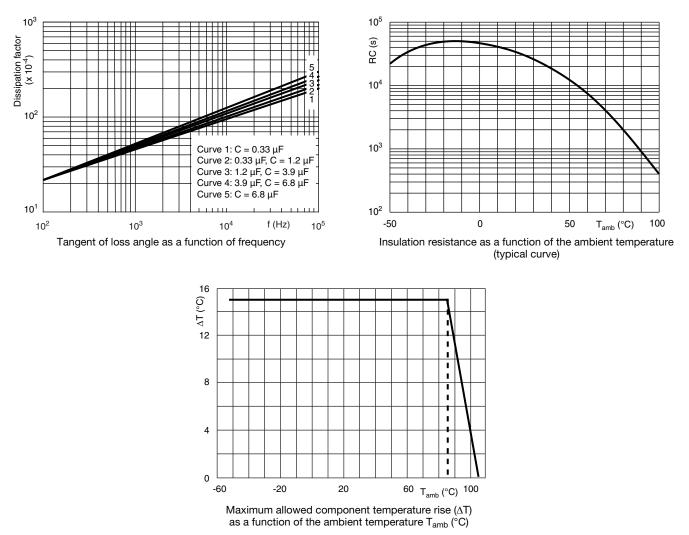
6 For technical questions, contact: <u>dc-film@vishay.com</u>

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# Maximum RMS current (sinewave) as a function of frequency

U<sub>AC</sub> is the maximum AC voltage depending on the ambient temperature in the curves "Max. RMS voltage and AC current as a function of frequency".



HEAT CONDUCTIVITY (G) AS A FUNCTION OF (ORIGINAL) PITCH AND CAPACITOR BODY THICKNESS IN mW/°C					
W <sub>MAX.</sub>	HEAT CONDUCTIVITY (mW/°C)				
(mm)	PITCH 10.0 mm				
4.0 6.0					
5.0	5.0 7.5				
6.0	6.0 9.0				

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# POWER DISSIPATION AND MAXIMUM COMPONENT TEMPERATURE RISE

The power dissipation must be limited in order not to exceed the maximum allowed component temperature rise as a function of the free ambient temperature.

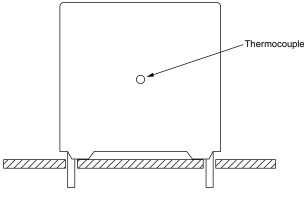
The power dissipation can be calculated according type detail specification "HQN-384-01/101: Technical Information Film Capacitors", <u>www.vishay.com/doc?28147</u>.

The component temperature rise ( $\Delta$ T) can be measured (see section "Measuring the component temperature" for more details) or calculated by  $\Delta$ T = P/G:

- $\Delta T$  = component temperature rise (°C)
- P = power dissipation of the component (mW)
- G = heat conductivity of the component (mW/°C)

## **MEASURING THE COMPONENT TEMPERATURE**

A thermocouple must be attached to the capacitor body as in:



The temperature is measured in unloaded ( $T_{amb}$ ) and maximum loaded condition ( $T_C$ ). The temperature rise is given by  $\Delta T = T_C - T_{amb}$ .

To avoid radiation or convection, the capacitor should be tested in a wind-free box.

## **APPLICATION NOTE AND LIMITING CONDITIONS**

These capacitors are not suitable for mains applications as across-the-line capacitors without additional protection, as described hereunder. These mains applications are strictly regulated in safety standards and therefore electromagnetic interference suppression capacitors conforming the standards must be used.

For capacitors connected in parallel, normally the proof voltage and possibly the rated voltage must be reduced. For information depending of the capacitance value and the number of parallel connections contact: <u>dc-film@vishay.com</u>

To select the capacitor for a certain application, the following conditions must be checked:

- 1. The peak voltage ( $U_P$ ) shall not be greater than the rated DC voltage ( $U_{RDC}$ )
- 2. The peak-to-peak voltage (U<sub>P-P</sub>) shall not be greater than  $2\sqrt{2} \times U_{BAC}$  to avoid the ionization inception level
- 3. The voltage peak slope (dU/dt) shall not exceed the rated voltage pulse slope in an RC-circuit at rated voltage and without ringing. If the pulse voltage is lower than the rated DC voltage, the rated voltage pulse slope may be multiplied by U<sub>RDC</sub> and divided by the applied voltage.

For all other pulses following equation must be fulfilled:

$$2 x \int_{0}^{1} \left(\frac{dU}{dt}\right)^{2} x \left(dt < U_{RDC} x \left(\frac{dU}{dt}\right)_{rated}\right)$$

T is the pulse duration.

- 4. The maximum component surface temperature rise must be lower than the limits (see graph "Max. allowed component temperature rise").
- 5. Since in circuits used at voltages over 280 V peak-to-peak the risk for an intrinsically active flammability after a capacitor breakdown (short circuit) increases, it is recommended that the power to the component is limited to 100 times the values mentioned in the table: "Heat Conductivity"
- 6. When using these capacitors as across-the-line capacitor in the input filter for mains applications or as series connected with an impedance to the mains the applicant must guarantee that the following conditions are fulfilled in any case (spikes and surge voltages from the mains included).

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VOLTAGE CONDITIONS FOR 6 ABOVE					
ALLOWED VOLTAGES	T <sub>amb</sub> ≤ 85 °C	85 °C < T <sub>amb</sub> ≤ 105 °C			
Maximum continuous RMS voltage	U <sub>RAC</sub>	See "Max. AC voltage as function of temperature" per characteristics			
Maximum temperature RMS-overvoltage (< 24 h)	1.25 x U <sub>RAC</sub>	U <sub>RAC</sub>			
Maximum peak voltage (V <sub>O-P</sub> ) (< 2 s)	1.6 x U <sub>RDC</sub>	1.3 x U <sub>RDC</sub>			

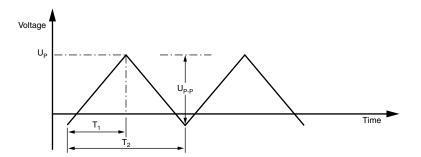
### Example

C = 330 nF - 63 V used for the voltage signal shown in next drawing.  $U_{P-P} = 40 \text{ V}; U_P = 35 \text{ V}; T_1 = 100 \ \mu\text{s}; T_2 = 200 \ \mu\text{s}$ The ambient temperature is 35 °C

Checking conditions:

- 1. The peak voltage  $U_{P}$  = 35 V is lower than 63  $V_{DC}$
- 2. The peak-to-peak voltage 40 V is lower than  $2\sqrt{2} \times 40 V_{AC} = 113 U_{P-P}$
- 3. The voltage pulse slope (dU/dt) = 40 V/100  $\mu$ s = 0.4 V/ $\mu$ s
  - This is lower than 60 V/µs (see specific reference data for each version)
- 4. The dissipated power is 16.2 mW as calculated with fourier terms The temperature rise for W<sub>max.</sub> = 3.5 mm and pitch = 5 mm will be 16.2 mW/3.0 mW/°C = 5.4 °C This is lower than 15 °C temperature rise at 35 °C, according figure "Max. allowed component temperature rise"
- 5. Not applicable
- 6. Not applicable

### Voltage Signal



### **INSPECTION REQUIREMENTS**

#### **General Notes**

Sub-clause numbers of tests and performance requirements refer to the "Sectional Specification, Publication IEC 60384-2 and Specific Reference Data".

GROUP C INSPECTION REQUIREMENTS						
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS				
SUB-GROUP C1A PART OF SAMPLE OF SUB-GROUP C1						
4.1 Dimensions (detail)		As specified in chapters "General Data" of this specification				
4.3.1 Initial measurements	Capacitance Tangent of loss angle: for C $\leq$ 470 nF at 100 kHz for 470 nF $<$ C $\leq$ 10 $\mu$ F at 10 kHz for C $>$ 10 $\mu$ F at 1 kHz					
4.3 Robustness of terminations	Tensile and bending	No visible damage				
4.4 Resistance to soldering heat	Method: 1A Solder bath: 280 °C ± 5 °C Duration: 10 s					



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GROUP C INSPECTION REQUIREMENTS						
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS				
SUB-GROUP C1A PART OF SAMPLE OF SUB-GROUP C1						
4.14 Component solvent resistance	Isopropylalcohol at room temperature Method: 2 Immersion time: 5 min ± 0.5 min Recovery time: min. 1 h, max. 2 h					
4.4.2 Final measurements	Visual examination	No visible damage Legible marking				
	Capacitance	$ \Delta C/C  \le 2$ % of the value measured initially				
	Tangent of loss angle	Increase of tan $\delta$ $\leq 0.005$ for: C $\leq 100$ nF or $\leq 0.010$ for: 100 nF < C $\leq 220$ nF or $\leq 0.015$ for: 220 nF < C $\leq 470$ nF and $\leq 0.003$ for: C > 470 nF Compared to values measured in 4.3.1				
SUB-GROUP C1B PART OF SAMPLE OF SUB-GROUP C1						
4.6.1 Initial measurements	Capacitance Tangent of loss angle: for C $\leq$ 470 nF at 100 kHz for 470 nF < C $\leq$ 10 $\mu$ F at 10 kHz for C > 10 $\mu$ F at 1 kHz	No visible damage				
4.6 Rapid change of temperature	$\theta A = -55 \ ^{\circ}C$ $\theta B = +105 \ ^{\circ}C$ 5 cycles Duration t = 30 min					
4.7 Vibration	Visual examination Mounting: see section "Mounting" of this specification Procedure B4 Frequency range: 10 Hz to 55 Hz Amplitude: 0.75 mm or Acceleration 98 m/s <sup>2</sup> (whichever is less severe) Total duration 6 h	No visible damage				
SUB-GROUP C1B PART OF SAMPLE						
OF SUB-GROUP C14.7.2Final inspection	Visual examination	No visible damage				
4.9 Shock	Mounting: see section "Mounting" of this specification Pulse shape: half sine Acceleration: 490 m/s <sup>2</sup> Duration of pulse: 11 ms					
4.9.3 Final measurements	Visual examination	No visible damage				
	Capacitance	$ \Delta C/C  \le 3$ % of the value measured in 4.6.1				
	Tangent of loss angle	Increase of tan $\delta$ $\leq$ 0.005 for: C $\leq$ 100 nF or $\leq$ 0.010 for: 100 nF < C $\leq$ 220 nF or $\leq$ 0.015 for: 220 nF < C $\leq$ 470 nF and $\leq$ 0.003 for: C > 470 nF Compared to values measured in 4.6.1				
	Insulation resistance	As specified in section "Insulation Resistance" of this specification				

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GROL	GROUP C INSPECTION REQUIREMENTS						
SUB-CI	LAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS				
SUB-GROUP C1 COMBINED SAMPLE OF SPECIMENS OF SUB-GROUPS C1A AND C1B							
4.10	Climatic sequence						
4.10.2	Dry heat	Temperature: +105 °C Duration: 16 h					
4.10.3	Damp heat cyclic Test Db, first cycle						
4.10.4	Cold	Temperature: -55 °C Duration: 2 h					
4.10.6	Damp heat cyclic Test Db, remaining cycles						
4.10.6.2	P Final measurements	Voltage proof = $U_{RDC}$ for 1 min within 15 min after removal from testchamber	No breakdown of flash-over				
		Visual examination	No visible damage Legible marking				
		Capacitance	$ \Delta C/C  \le 3$ % of the value measured in 4.4.2 or 4.9.3				
		Tangent of loss angle	Increase of tan $\delta$ $\leq 0.005$ for: C $\leq 100$ nF or $\leq 0.010$ for: 100 nF < C $\leq 220$ nF or $\leq 0.015$ for: 220 nF < C $\leq 470$ nF and $\leq 0.005$ for: C > 470 nF Compared to values measured in 4.3.1 or 4.6.1				
		Insulation resistance	$\ge$ 50 % of values specified in section "Insulation Resistance" of this specification				
SUB-GI	ROUP C2						
4.11 [	Damp heat steady state	56 days, 40 °C, 90 % to 95 % RH					
4.11.1	nitial measurements	Capacitance Tangent of loss angle at 1 kHz					
4.11.3 F	-inal measurements	Voltage proof = U <sub>RDC</sub> for 1 min within 15 min after removal from testchamber	No breakdown of flash-over				
		Visual examination	No visible damage Legible marking				
		Capacitance	$ \Delta C/C  \le 5$ % of the value measured in 4.11.1.				
		Tangent of loss angle	Increase of tan $\delta \leq$ 0.005 Compared to values measured in 4.11.1				
		Insulation resistance	$\geq$ 50 % of values specified in section "Insulation Resistance" of this specification				
SUB GF	ROUP C3						
4.12 E	Endurance	Duration: 2000 h 1.25 x U <sub>RDC</sub> at 85 °C 0.8 x 1.25 U <sub>RDC</sub> at 105 °C					

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GROUP C INSPECTION REQUIREMENTS					
SUB-CLAUSE NUMBER AND TEST	CONDITIONS	PERFORMANCE REQUIREMENTS			
SUB GROUP C3					
4.12.1 Initial measurements	Capacitance Tangent of loss angle: for C $\leq$ 470 nF at 100 kHz for 470 nF < C $\leq$ 10 $\mu$ F at 10 kHz for C > 10 $\mu$ F at 1 kHz				
4.12.5 Final measurements	Visual examination	No visible damage Legible marking			
	Capacitance	$ \Delta C/C  \le 5$ % compared to values measured in 4.12.1			
	Tangent of loss angle	$\begin{array}{l} \mbox{Increase of tan } \delta \\ \leq 0.005 \mbox{ for: } C \leq 100 \mbox{ nF or } \\ \leq 0.010 \mbox{ for: } 100 \mbox{ nF < } C \leq 220 \mbox{ nF or } \\ \leq 0.015 \mbox{ for: } 220 \mbox{ nF < } C \leq 470 \mbox{ nF and } \\ \leq 0.003 \mbox{ for: } C > 470 \mbox{ nF } \\ \mbox{ Compared to values measured in } 4.12.1 \end{array}$			
	Insulation resistance	$\geq$ 50 % of values specified in section "Insulation Resistance" of this specification			
SUB-GROUP C4					
4.13 Charge and discharge	10 000 cycles Charged to U <sub>RDC</sub> Discharge resistance: $R = \frac{U_R}{C \times 2.5 \times (dU/dt)_R}$				
4.13.1 Initial measurements	Capacitance Tangent of loss angle: for C $\leq$ 470 nF at 100 kHz for 470 nF $<$ C $\leq$ 10 $\mu$ F at 10 kHz for C $>$ 10 $\mu$ F at 1 kHz				
4.13.3 Final measurements	Capacitance	$ \Delta C/C  \leq 3$ % compared to values measured in 4.13.1			
	Tangent of loss angle	Increase of tan $\delta$ $\leq 0.005$ for: C $\leq 100$ nF or $\leq 0.010$ for: 100 nF < C $\leq 220$ nF or $\leq 0.015$ for: 220 nF < C $\leq 470$ nF and $\leq 0.003$ for: C > 470 nF Compared to values measured in 4.13.1			
	Insulation resistance	$\geq$ 50 % of values specified in section "Insulation Resistance" of this specification			



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